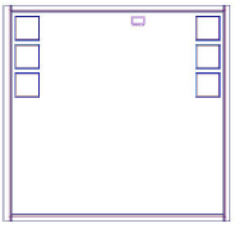
MMIC

REFLECTIONLESS FILTER DICE

50Ω DC to 21 GHz



X-Series

Available in Low Pass, High Pass and Band Pass designs

The Big Deal

- *Patented design eliminates in band spurs*
- *Pass band cut-off up to 21 GHz*
- *Stop band up to 35 GHz*
- *Excellent repeatability through IPD* process*
- *Unpackaged Die Form*

Product Overview

Mini-Circuits' X-Series reflectionless filters employ a novel filter topology which absorbs and terminates stop band signals internally rather than reflecting them back to the source. This new capability enables unique applications for filter circuits beyond those suited to traditional approaches. Traditional filters are reflective in the stop band, sending signals back to the source at 100% of the power level which interact with neighboring components and often result in intermodulation and other interferences. Reflectionless filters eliminate stop band reflections, allowing them to be paired with sensitive devices and used in applications that otherwise require circuits such as isolators, isolation amplifiers or attenuators.

Key Features	Advantages
Easy integration with sensitive reflective components, e.g. mixers, multipliers	Reflectionless filters absorb unwanted signals, preventing reflections back to the source. This reduces generation of additional unwanted signals without the need for extra components like attenuators, improving system dynamic range and saving board space.
Enables stable integration of wideband amplifiers	Because reflectionless filters maintain good impedance in the stop band; they can be integrated with high gain, wideband amplifiers without the risk of creating instabilities in these out of band regions.
Cascadable	Reflectionless filters can be cascaded in multiple sections to provide sharper and higher attenuation, while also preventing any standing waves that could affect pass band signals.
Excellent power handling in a tiny surface mount device	High power handling extends the usability of these filters to the transmit path for inter-stage filtering.
Excellent repeatability of RF performance	Through semiconductor IPD process, X-series filters are inherently repeatable for large volume production.
Excellent stability over temperature	With ±0.3 dB variation over temperature ideal for use in wide temperature range applications without the need for additional temperature compensation.
Operating Temperature up to 105°C	Suitable for operation close to high power components
Unpackaged Die form	Enables direct integration into customer hybrids

*IPD – Integrated Passive Device, is a GaAs semiconductor process

Reflectionless

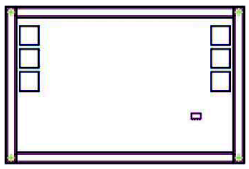
Low Pass Filter Die

XLF-173-D+

50Ω DC to 17000 MHz

Features

- Match to 50Ω in the stop band, eliminates undesired reflections
- Cascadable
- Excellent Power handling
- Protected by US Patent No. 8,392,495



Applications

- Harmonics Rejection
- Satellite
- Radar
- Military & Space

+RoHS Compliant

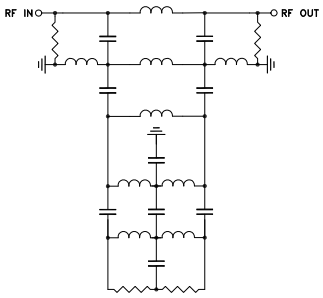
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Ordering Information: Refer to Last Page

General Description

Mini-Circuits' XLF-173-D+ reflectionless filter die employs a novel filter topology which absorbs and terminates stop band signals internally rather than reflecting them back to the source. This new capability enables unique applications for filter circuits beyond those suited to traditional approaches. Traditional filters are reflective in the stop band, sending signals back to the source at 100% of the power level. These reflections interact with neighboring components and often result in inter-modulation and other interferences. Reflectionless filters eliminate stop band reflections, allowing them to be paired with sensitive devices and used in applications that otherwise require circuits such as isolation amplifiers or attenuators.

Simplified Schematic



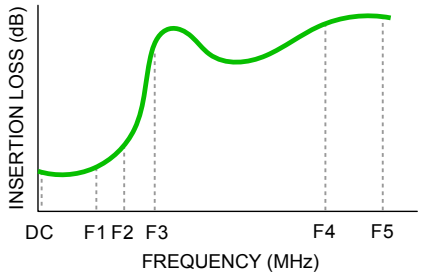
Pad	Description
RF-IN	RF Input Pad
RF-OUT	RF Output Pad
Ground	Ground Bonding Pad

Electrical Specifications[1] at 25°C

Parameter		F#	Frequency (MHz)	Min.	Typ.	Max.	Unit
Pass Band	Insertion Loss	DC - F1	DC-17000		2.3		dB
	Frequency Cut-off	F2	18000		3.0		dB
	VSWR	DC - F1	DC-17000		1.2		:1
Stop Band	Rejection	F3 - F4	23900 - 26000		17		dB
		F4 - F5	26000 - 33000		21		dB
	VSWR	F3 - F4	23900 - 26000		2.6		:1
		F4 - F5	26000 - 33000		3		:1

[1] Measured on Mini-Circuits Characterization test board. Die packaged in 3mm x 3mm, 12-lead MCLP package and soldered on TB-844-173+

Specification Definition



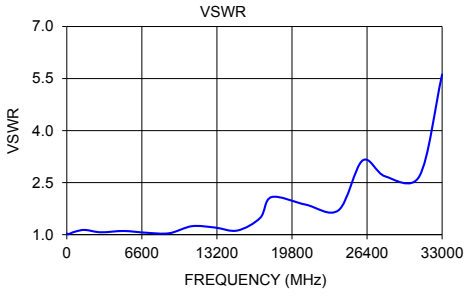
Absolute Maximum Ratings[1,4]

Parameter	Ratings
Operating Temperature	-55°C to +105°C
RF Power Input, Passband (DC-F1)[2]	2W at 25°C
RF Power Input, Stopband (F2-F5)[3]	30mW at 25°C

[2] Passband rating derates linearly to 1W at 105°C ambient
[3] Stopband rating derates linearly to 15mW at 105°C ambient
[4] Permanent damage may occur if any of these limits are exceeded.

Typical Performance Data at 25°C[1]

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)
10	0.49	1.04
100	0.49	1.01
400	0.54	1.05
800	0.56	1.09
1600	0.57	1.14
3000	0.52	1.07
5000	0.60	1.11
7000	0.83	1.06
9000	1.03	1.04
11000	1.27	1.24
13000	1.47	1.21
15000	1.67	1.12
17000	2.24	1.48
18000	3.07	2.08
21000	5.62	1.87
23900	18.50	1.71
26000	50.38	3.14
28000	27.85	2.68
31000	21.48	2.68
33000	27.29	5.62



Die Layout

Fig 1. Die Layout

Bonding Pad Position

(Dimensions in µm, Typical)

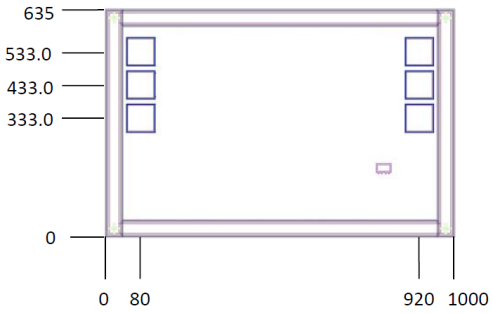


Fig 2. Bonding Pad Positions

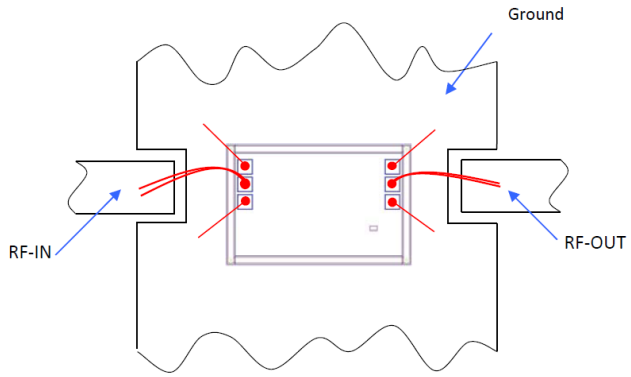
Critical Dimensions

Parameter	Values
Die Thickness, µm	100
Die Width, µm	1000
Die Length, µm	635
Bond Pad Size (Ground pad), µm	75 x 75

Assembly and Handling Procedure

1. Storage
   Dice should be stored in a dry nitrogen purged desiccators or equivalent.
2. ESD
   MMIC Gallium Arsenide (GaAs) filter dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static worksta tion. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.
3. Die Attach
   The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030Hk-PT/H579/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufac turer's cure condition. It is recommended to use antistatic die pick up tools only.
4. Wire Bonding
   Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.

Assembly Diagram



Recommended Wire Length, Typical

Wire	Wire Length (mm)	Wire Loop Height (mm)
All wires	1.0	0.15

Note: Use double bond wire at RF IN & RF OUT

Additional Detailed Technical Information

additional information is available on our dash board.

Performance Data	Data Table
	Swept Graphs
	S-Parameter (S2P Files) Data Set with and without port extension(.zip file)
Case Style	Die
Die Ordering and packaging information	Quantity, Package — Model No. Small, Gel - Pak: 10,50,100 KGD* — XLF-173-DG+ Medium†, Partial wafer: KGD*<5K — XLF-173-DP+ †*Available upon request contact sales representative* Refer to AN-60-067
Environmental Ratings	ENV-80

*Known Good Dice ("KGD") means that the dice are taken from PCM good wafer and visually inspected according to Mini-Circuits inspection criteria. While this is not definitive, it does help to provide a higher degree of confidence that dice are capable of meeting typical RF electrical parameters specified by Mini-Circuits.

ESD Rating**

Human Body Model (HBM): Class 1A (250V to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

** Tested in industry standard MCLP 3x3mm 12 lead package.

Additional Notes

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.

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