

Surface Mount

Digital Step Attenuator

DAT-15575A Series

75Ω 0 to 15.5 dB, 0.5 dB Step 1MHz to 2.5 GHz

The Big Deal

- Wideband, operates up to 2.5 GHz
- Glitchless attenuation transitions
- High IP3, 52 dBm



CASE STYLE: DG983-2

Product Overview

The DAT-15575A+ series of 75Ω digital step attenuators provides adjustable attenuation from 0 to 15.5 dB in 0.5 dB steps. The control is a 5-bit serial/parallel interface, and the attenuators operate with either single positive or dual (positive and negative) supply voltage. DAT-15575A+ series models are produced by a unique CMOS process on silicon, offering the performance of GaAs with the advantages of conventional CMOS devices.

Key Features

Feature	Advantages
Wideband operation, specified from 1MHz to 2.5 GHz	Can be used in multiple applications such as various versions of DOCSIS, satellite and defense, reducing part count.
Serial or parallel interface	Models available with serial or parallel interface mode to suit customer demand.
Good VSWR, 1.3:1 typ.	Eases interfacing with adjacent components and results in low amplitude ripple.
Single positive supply models: (Model suffixes: -SP+ and -PP+) +2.3 to +3.6V+	Use of single positive supply simplifies power supply design. An internal negative voltage generator supplies the desired negative voltage. Single positive supply results in excellent spurious performance, -140 dBm typical.
Dual supply models: (Model suffixes: -SN+ and -PN+) +2.7 to +3.6V (Positive) and -3.6 to -3.2V (Negative)	Dual supply provides spurious-free operation. It also allows fast switching up to 1 MHz (vs. 25 kHz for single supply).
Useable over a wide range of supply voltages, +2.3/2.7 to 5.2V	Wide range of positive operating voltages allows the DAT-15575A+ Series of models to be used in a wide range of applications. See Application Note AN-70-032 for operation above +3.6V
Footprint compatible to DAT-15575-XX+ Series (XX=SN/SP/PN/PP)	Can fit into existing footprint and provide wideband performance, to 2.5 GHz instead of 2.0 GHz.
Glitchless Attenuation Transitions, 0.26 typical	Compared to previous generation of digital attenuators which is a vast improvement.



Digital Step Attenuator

75Ω 1-2500 MHz

15 dB, 0.5 dB Step

5 Bit, Parallel Control Interface, Single Supply Voltage

- Single positive supply voltage
- Immune to latch up
- Glitchless attenuation transitions
- Excellent accuracy, 0.1 dB Typ
- Low Insertion Loss
- High IP3, +55-59 dBm Typ
- Very low DC power consumption
- Excellent return loss, 18 dB Typ
- Small size 4.0 x 4.0 mm



Generic photo used for illustration purposes only

DAT-15575A-PP+

CASE STYLE: DG983-2

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

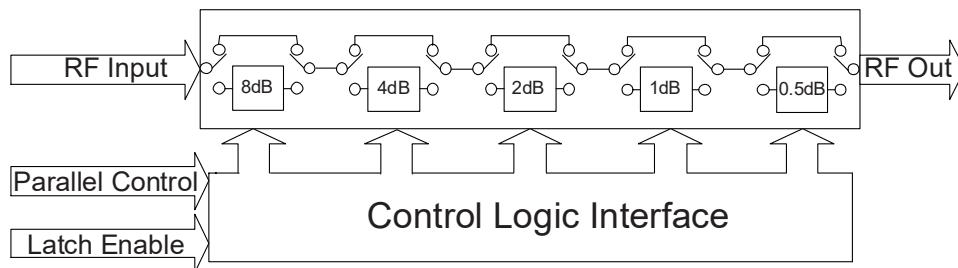
Typical Applications

- DOCSIS 3.1
- Portable Wireless
- CATV & DBS
- MMDS & Wireless LAN
- Wireless Local Loop
- UNII & Hiper LAN
- Power amplifier distortion canceling loops

General Description

The DAT-15575A-PP+ is a 75Ω RF digital step attenuator that offers an attenuation range up to 15.5 dB in 0.5 dB steps. The control is a 5-bit parallel interface, operating on single (positive) supply voltage. The DAT-15575A-PP+ is produced using a unique CMOS process on silicon, offering the performance of GaAs, with the advantages of conventional CMOS devices.

Simplified Schematic



RF Electrical Specifications, 1-2500 MHz, $T_{AMB}=25^{\circ}\text{C}$, $V_{DD}=+3\text{V}$, 75Ω

Parameter	Freq. Range (GHz)	Min.	Typ.	Max.	Units
Accuracy @ 0.5 dB Attenuation Setting	0.001-1.2	—	0.03	0.17	dB
	1.2-2.0	—	0.05	0.18	
	2.0-2.5	—	0.1	0.19	
Accuracy @ 1 dB Attenuation Setting	0.001-1.2	—	0.03	0.18	dB
	1.2-2.0	—	0.1	0.20	
	2.0-2.5	—	0.1	0.23	
Accuracy @ 2 dB Attenuation Setting	0.001-1.2	—	0.07	0.21	dB
	1.2-2.0	—	0.15	0.26	
	2.0-2.5	—	0.15	0.31	
Accuracy @ 4 dB Attenuation Setting	0.001-1.2	—	0.05	0.27	dB
	1.2-2.0	—	0.15	0.36	
	2.0-2.5	—	0.2	0.47	
Accuracy @ 8 dB Attenuation Setting	0.001-1.2	—	0.1	0.39	dB
	1.2-2.0	—	0.24	0.60	
	2.0-2.5	—	0.35	0.79	
Insertion Loss ¹ @ all attenuator set to 0dB	0.001-1.2	—	1.2	1.8	dB
	1.2-2.5	—	1.6	1.9	
VSWR	0.001-1.2	—	1.3	—	:1
	1.2-2.5	—	1.4	—	
Input IP3 (at Min. and Max. Attenuation)	.005-2.5	—	55-69	—	dBm
Input IP2	.005-2.5	—	See Fig. 1	—	dBm
Input Power @ 0.2dB Compression (at Min. and Max. Attenuation)	0.030-2.5	—	+30	—	dBm
Input Operating Power	1 MHz to 30 MHz	—	—	See Fig. 2	dBm
	>30 MHz	—	—	+24	
Thermal Resistance (Junction to case)	—	—	25	—	$^{\circ}\text{C/W}$

DC Electrical Specifications

Parameter	Min.	Typ.	Max.	Units
V_{DD} , Supply Voltage	2.3	3	3.6 ²	V
I_{DD} , Supply Current	—	—	200	μA
Control Input Low	-0.3	—	0.6 ³	V
Control Input High	1.17	—	3.6	V
Control Current	—	—	20	μA

1. I. Loss values are de-embedded from test board Loss (test board's Insertion Loss: 0.10dB @ 100MHz, 0.40dB @ 1200MHz, 0.55dB @ 2000MHz, 0.75dB @ 4000MHz).

2. For operation above +3.6V see application note, AN-70-032

3. 0V during power-up.

Absolute Maximum Ratings⁴

Parameter	Ratings
Operating Temperature	-40°C to 105°C
Storage Temperature	-65°C to 150°C
V_{DD}	-0.3V Min., 5.5V Max.
Voltage on any input	-0.3V Min., 3.6V Max.
Input Power	1-30 MHz Figure 2
	30-2500MHz +30dBm

4. Permanent damage may occur if any of these limits are exceeded.

5. Operation between max operating and absolute max input power will result in reduced reliability.

6. 0 Volt during power up.

Switching Specifications

Parameter	Min.	Typ.	Max.	Units
Switching Speed, 50% Control to 90% or 10% RF	—	0.4	0.7	μSec
Switching Control Frequency	—	—	25	kHz

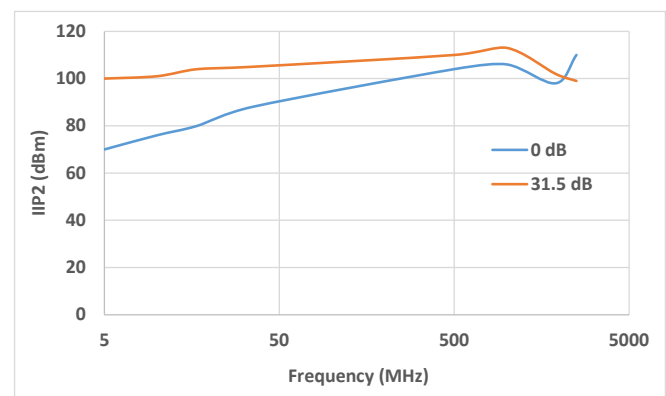


Figure 1. IP2 vs. frequency and attenuation

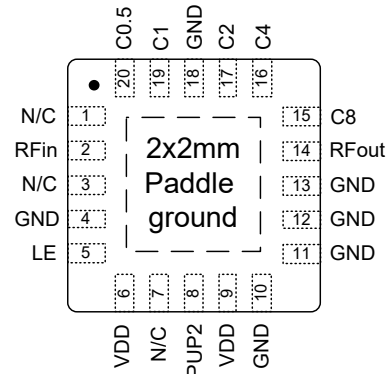
Pin Description

Function	Pin Number	Description
N/C	1	Not connected (Note 4,7)
RF in	2	RF in port (Note 1)
N/C	3	Not connected (Note 4)
GND	4	Ground connection
LE	5	Latch Enable Input (Note 2)
V _{DD}	6	Positive Supply Voltage
N/C	7	Not connected (Note 7)
PUP2	8	Power-up selection
V _{DD}	9	Positive Supply Voltage
GND	10	Ground connection
GND	11	Ground connection
GND	12	Ground connection (Note 6)
GND	13	Ground connection
RF out	14	RF out port (Note 1)
C8	15	Control for attenuation bit, 8 dB
C4	16	Control for attenuation bit, 4 dB
C2	17	Control for attenuation bit, 2 dB
GND	18	Ground Connection
C1	19	Control for attenuation bit, 1 dB
C0.5	20	Control for attenuation bit, 0.5 dB (Note 7)
GND	Paddle	Paddle ground (Note 5)

Notes:

- Both RF ports must be held at 0VDC or DC blocked with an external series capacitor.
- Latch Enable (LE) has an internal 2M Ω to internal positive supply voltage.
- N/A
- Place a shunt 10K Ω resistor to GND
- The exposed solder pad on the bottom of the package (See Pin configuration) must be grounded for proper device operation.
- Ground must be less than 80 mil (0.08") from Pin 12 for proper device operation.
- This pin has an internal 1M Ω resistor to ground.

Pin Configuration (Top View)



Device Marking

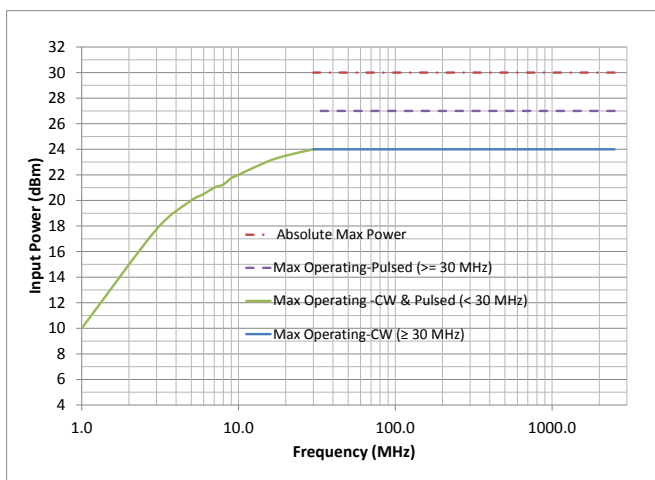
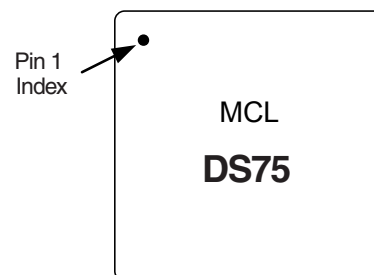
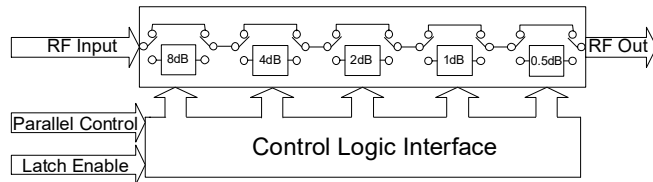


Figure 2. Max Input power vs. frequency.
Pulsed Power: 5% duty cycle, 4620 μ S period

Simplified Schematic



The DAT-15575A-PP+ parallel interface consists of 5 control bits that select the desired attenuation state, as shown in Table 1: Truth Table

Table 1. Truth Table					
Attenuation State	C8	C4	C2	C1	C0.5
Reference	0	0	0	0	0
0.5 (dB)	0	0	0	0	1
1 (dB)	0	0	0	1	0
2 (dB)	0	0	1	0	0
4 (dB)	0	1	0	0	0
8 (dB)	1	0	0	0	0
15.5 (dB)	1	1	1	1	1

Note: Not all 32 possible combinations of C0.5 - C8 are shown in table

The parallel interface timing requirements are defined by Figure 3 (Parallel Interface Timing Diagram) and Table 2 (Parallel Interface AC Characteristics), and switching speed.

For latched parallel programming the Latch Enable (LE) should be held LOW while changing attenuation state control values, then pulse LE HIGH to LOW (per Figure 1) to latch new attenuation state into device.

For direct parallel programming, the Latch Enable (LE) line should be pulled HIGH. Changing attenuation state control values will change device state to new attenuation. Direct mode is ideal for manual control of the device (using hardware, switches, or jumpers).

Figure 3: Parallel Interface Timing Diagram

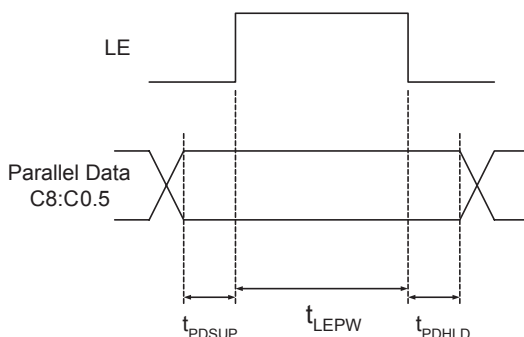


Table 2. Parallel Interface AC Characteristics

Symbol	Parameter	Min.	Max.	Units
t_{LEPW}	LE minimum pulse width	10		ns
t_{PDSUP}	Data set-up time before clock rising edge of LE	10		ns
t_{PDHL}	Data hold time after clock falling edge of LE	10		ns

Power-up Control Settings

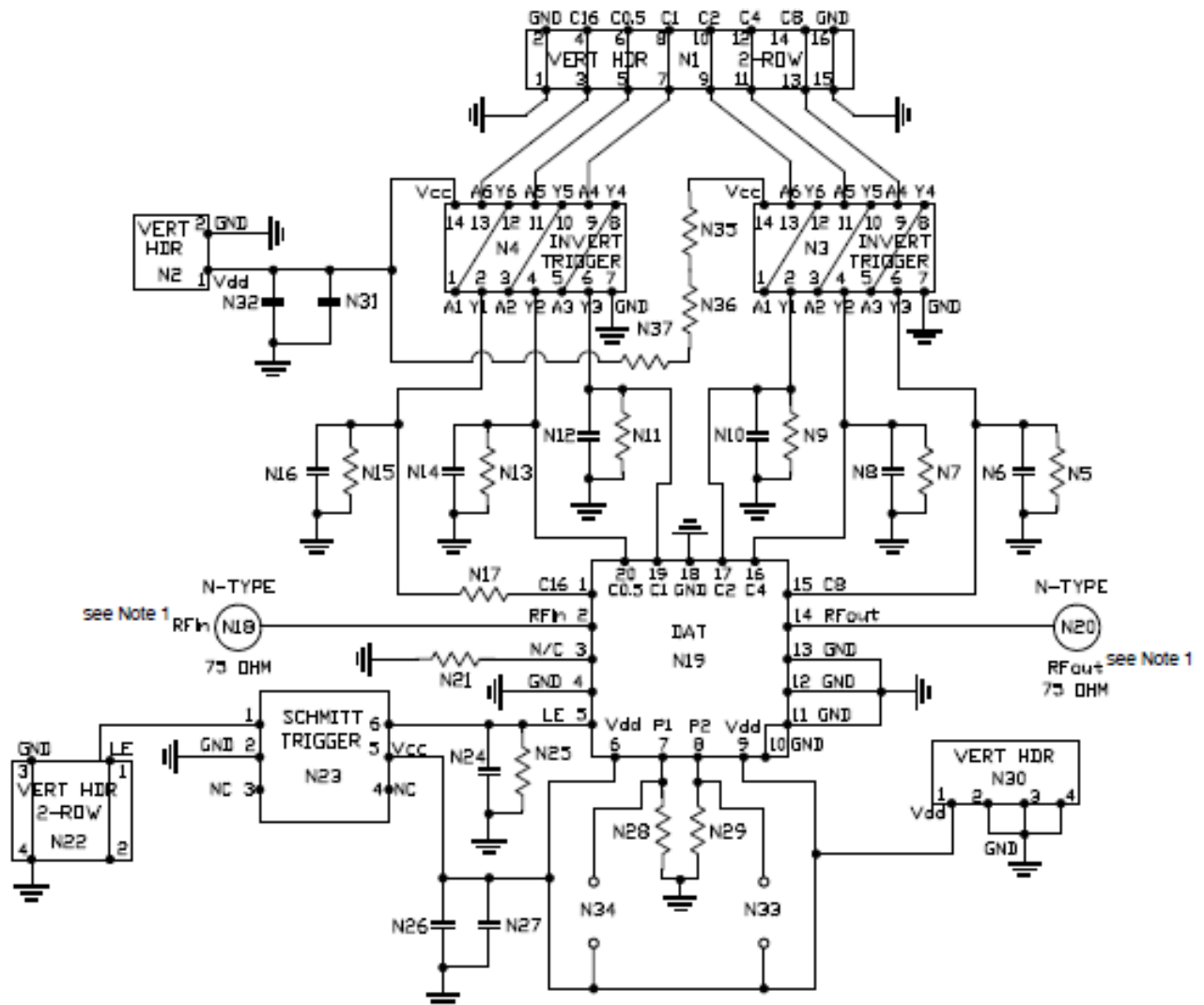
The DAT-15575A-PP+ always assumes a specifiable attenuation setting on power-up, allowing a known attenuation state to be established before an initial parallel control word is provided.

When the attenuator powers up with LE=0, the control bits are automatically set to one of two possible values. These two values are selected by the power-up control bit, PUP2, as shown in Table 3: (Power-Up Truth Table, Parallel Mode).

Table 3. Power-Up Truth Table, Parallel Mode		
Attenuation State	PUP2	LE
Reference	0	0
8 (dB)	1	0
Defined by C0.5-C8 (See Table 1-Truth Table)	X (Note 1)	1
Note 1: PUP2 Connection may be 0, 1, GROUND, or not connect, without effect on attenuation state.		

Power-Up with LE=1 provides normal parallel operation with C0.5-C8 and PUP2 is not active.

TB-337 Evaluation Schematic Diagram



Note 1: Both RF ports must be held at 0VDC or DC blocked with an external series capacitor.

Bill of Materials	
N5, N7, N9, N11, N13, N15, N21 & N25	Resistor 0603 10 KOhm +/- 1%
N28 & N29	Resistor 0603 475 Ohm +/- 1%
N35-N37	Resistor 0603 0 Ohm
N17	Resistor 0402 10 KOhm +/- 1%
N6, N8, N10, N12, N14, N16, N24, N26 & N32	NPO Capacitor 0603 100pF +/- 5%
N27 & N31	Tantalum Capacitor 0805 100nF +/- 10%
N3 & N4	Hex Invert Schmitt Trigger MSL1
N23	Dual Schmitt Trigger Buffer SC-70 MSL1



TB-337

Additional Detailed Technical Information

additional information is available on our dash board. To access this information [click here](#)

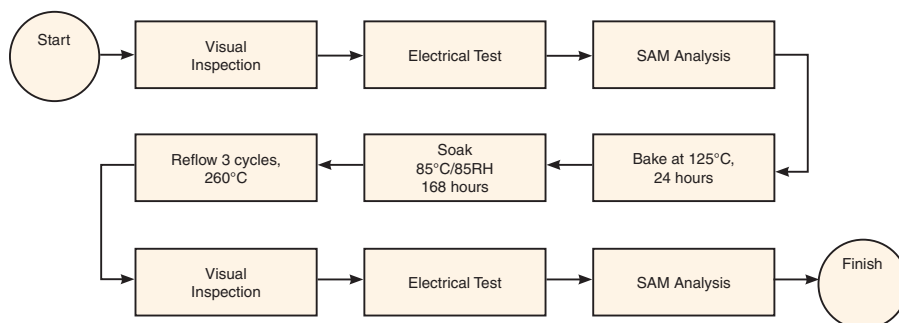
Performance Data	Data Table
	Swept Graphs
	S-Parameter (S2P Files) Data Set (.zip file)
Case Style	DG983-2 Plastic package, exposed paddle, lead finish: NiPdAu
Tape & Reel Standard quantities available on reel	F87 7" reels with 20, 50, 100, 200, 500 Or 1000 devices 13" reels with 3K devices
Suggested Layout for PCB Design	PL-201
Evaluation Board	TB-337
Environmental Ratings	ENV33T1

ESD Rating

Human Body Model (HBM): Class 1C (1000 to <2000V) in accordance with MIL-STD-883 method 3015
Charge Device Model class C2 (500 to <1000V) per JESD22-C101

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

MSL Test Flow Chart**Additional Notes**

- Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

Digital Step Attenuator

DAT-15575A-PP+

Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, TEMPERATURE=-45degC

FREQUENCY (MHz)	STEP ATTENUATION* AT TTL CONTROL STATE (dB)						
	00000 THRU LOSS	00001 0.5 dB	00010 1.0 dB	00100 2.0 dB	01000 4.0 dB	10000 8.0 dB	11111 15.5 dB
10	0.94	0.53	1.04	2.05	4.14	8.21	15.77
50	0.94	0.53	1.05	2.06	4.13	8.21	15.80
100	0.94	0.54	1.05	2.05	4.12	8.20	15.79
150	0.95	0.53	1.05	2.05	4.12	8.19	15.78
200	0.96	0.53	1.05	2.05	4.12	8.20	15.78
250	0.96	0.53	1.05	2.06	4.12	8.20	15.79
300	0.96	0.53	1.05	2.05	4.13	8.20	15.80
350	0.96	0.53	1.05	2.05	4.13	8.21	15.81
400	0.97	0.53	1.05	2.05	4.13	8.20	15.80
450	0.98	0.53	1.05	2.05	4.13	8.20	15.79
500	1.00	0.53	1.04	2.05	4.12	8.19	15.77
550	1.02	0.53	1.04	2.04	4.11	8.18	15.77
600	1.04	0.53	1.04	2.04	4.10	8.17	15.76
700	1.06	0.53	1.03	2.04	4.10	8.16	15.75
800	1.10	0.52	1.03	2.03	4.08	8.13	15.71
900	1.12	0.52	1.03	2.03	4.08	8.14	15.73
1000	1.14	0.52	1.03	2.03	4.08	8.14	15.77
1100	1.17	0.51	1.02	2.02	4.07	8.14	15.76
1200	1.17	0.52	1.02	2.02	4.07	8.13	15.73
1300	1.17	0.51	1.02	2.02	4.07	8.12	15.66
1400	1.18	0.51	1.02	2.02	4.06	8.11	15.62
1500	1.21	0.51	1.02	2.02	4.07	8.14	15.70
1600	1.27	0.51	1.03	2.03	4.09	8.16	15.76
1700	1.29	0.51	1.03	2.03	4.10	8.17	15.76
1800	1.29	0.51	1.02	2.02	4.09	8.15	15.68
1900	1.28	0.51	1.02	2.02	4.10	8.16	15.66
2000	1.26	0.51	1.02	2.02	4.11	8.17	15.65
2100	1.23	0.51	1.03	2.03	4.13	8.19	15.63
2200	1.24	0.51	1.03	2.04	4.14	8.21	15.62
2300	1.27	0.51	1.04	2.05	4.17	8.28	15.74
2400	1.38	0.52	1.04	2.06	4.19	8.33	15.85
2500	1.43	0.52	1.04	2.05	4.18	8.32	15.76
2600	1.37	0.52	1.05	2.07	4.23	8.38	15.78
2700	1.25	0.52	1.06	2.09	4.30	8.46	15.83
2800	1.32	0.51	1.06	2.10	4.35	8.54	16.02
2900	1.67	0.50	1.04	2.07	4.34	8.56	16.21
3000	2.18	0.47	1.00	2.01	4.28	8.54	16.47

* Step Attenuation above Thru Loss (TTL Logic 00000).



REV. OR
DAT-15575A-PP+

10/12/2016

Page 1 of 9

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 • Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site
The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com



IF/RF MICROWAVE COMPONENTS

Digital Step Attenuator

DAT-15575A-PP+

Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, TEMPERATURE=-45degC

FREQUENCY (MHz)	INPUT RETURN LOSS AT TTL CONTROL STATE (dB)						
	00000 0 dB	00001 0.5 dB	00010 1.0 dB	00100 2.0 dB	01000 4.0 dB	10000 8.0 dB	11111 15.5dB
10	20.27	21.72	22.99	24.58	36.55	28.90	24.43
50	19.95	21.44	22.64	24.26	38.02	29.48	25.06
100	19.98	21.44	22.58	24.15	37.33	29.78	25.30
150	19.79	21.19	22.26	23.73	38.80	30.85	26.08
200	19.78	21.17	22.24	23.69	39.57	31.04	26.17
250	19.61	20.99	22.07	23.55	41.31	31.42	26.35
300	19.63	21.06	22.19	23.71	38.60	30.49	25.77
350	19.56	20.96	22.09	23.63	36.68	30.05	25.60
400	19.58	20.97	22.08	23.65	33.29	28.94	25.11
450	19.46	20.84	21.92	23.39	32.08	28.77	25.23
500	19.42	20.75	21.78	23.15	30.50	28.25	25.20
550	19.36	20.67	21.66	22.98	30.41	28.58	25.61
600	19.33	20.59	21.55	22.77	29.65	28.34	25.69
700	19.43	20.64	21.54	22.67	29.23	28.24	25.96
800	19.34	20.48	21.34	22.35	28.24	27.60	25.87
900	19.04	20.16	20.96	21.88	27.21	26.75	25.51
1000	18.58	19.64	20.42	21.30	26.45	26.22	25.41
1100	18.13	19.15	19.87	20.67	25.90	26.10	25.84
1200	17.76	18.73	19.42	20.16	25.67	26.28	26.75
1300	17.37	18.29	18.96	19.64	25.79	26.94	28.45
1400	16.89	17.75	18.41	19.07	25.54	27.23	30.20
1500	16.71	17.57	18.21	18.88	25.89	28.38	33.28
1600	16.86	17.71	18.36	19.04	26.87	30.62	41.50
1700	17.73	18.57	19.21	19.83	28.13	31.79	32.98
1800	19.11	19.82	20.41	20.89	27.04	28.10	26.32
1900	20.27	20.76	21.16	21.50	24.08	24.29	22.69
2000	20.92	21.04	21.22	21.51	21.40	21.41	20.09
2100	21.01	20.78	20.76	20.98	19.24	19.15	18.05
2200	20.75	20.38	20.25	20.42	18.02	17.78	16.79
2300	23.19	22.52	22.16	22.12	18.19	17.51	16.43
2400	27.07	25.04	23.69	22.59	17.28	16.12	15.07
2500	26.58	24.28	22.49	20.84	15.87	14.50	13.58
2600	22.77	21.61	20.39	18.85	14.67	13.24	12.42
2700	19.11	19.01	18.47	17.47	14.00	12.55	11.78
2800	15.19	15.66	15.75	15.53	13.38	12.11	11.45
2900	12.35	12.93	13.30	13.58	12.69	11.83	11.37
3000	10.45	11.05	11.52	12.06	12.14	11.78	11.60



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 • Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site
The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com



IF/RF MICROWAVE COMPONENTS

REV. OR
DAT-15575A-PP+
10/12/2016
Page 2 of 9

Digital Step Attenuator

DAT-15575A-PP+

Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, TEMPERATURE=-45degC

FREQUENCY (MHz)	OUTPUT RETURN LOSS AT TTL CONTROL STATE						
	00000 0 dB	00001 0.5 dB	00010 1.0 dB	00100 2.0 dB	01000 4.0 dB	10000 8.0 dB	11111 15.5 dB
10	20.05	21.73	22.44	24.36	28.47	29.16	24.78
50	19.84	21.45	22.13	24.12	27.74	30.06	25.23
100	19.61	21.19	21.83	23.70	27.11	30.65	25.65
150	19.32	20.83	21.43	23.17	26.28	32.23	26.62
200	19.39	20.91	21.53	23.31	26.49	32.12	26.53
250	19.44	20.96	21.58	23.38	26.66	32.08	26.43
300	19.51	21.09	21.73	23.62	27.07	30.71	25.76
350	19.30	20.89	21.54	23.41	26.76	30.39	25.65
400	19.05	20.56	21.21	23.05	26.04	29.59	25.44
450	18.73	20.17	20.76	22.49	25.21	29.77	25.87
500	18.40	19.79	20.35	21.97	24.36	29.36	26.09
550	18.28	19.63	20.17	21.73	23.96	29.66	26.64
600	18.23	19.55	20.06	21.56	23.62	29.39	26.81
700	18.36	19.68	20.14	21.60	23.49	29.76	27.39
800	18.77	20.11	20.55	22.03	23.76	29.94	27.57
900	19.36	20.75	21.16	22.62	24.14	29.08	26.93
1000	19.54	20.87	21.24	22.59	23.67	27.84	26.36
1100	19.24	20.49	20.79	21.94	22.65	26.80	26.16
1200	19.35	20.58	20.80	21.90	22.39	26.60	26.31
1300	19.64	20.89	21.06	22.09	22.39	26.81	26.67
1400	19.63	20.84	20.96	21.93	22.11	27.08	27.34
1500	19.23	20.39	20.47	21.37	21.58	27.79	28.94
1600	18.30	19.30	19.37	20.14	20.49	27.62	30.75
1700	17.49	18.41	18.50	19.23	19.67	26.82	30.75
1800	17.18	18.07	18.22	18.99	19.79	28.02	33.67
1900	17.21	18.06	18.32	19.19	20.54	29.86	34.33
2000	17.42	18.23	18.62	19.61	21.84	29.56	28.74
2100	17.58	18.36	18.87	19.93	23.15	26.06	24.13
2200	18.38	19.17	19.80	20.92	25.21	23.10	21.04
2300	20.64	21.55	22.36	23.38	28.57	20.73	18.72
2400	27.11	28.56	29.67	28.59	28.18	18.59	16.85
2500	40.31	31.33	29.48	25.58	22.09	16.46	15.18
2600	29.63	26.89	25.31	22.64	19.20	15.03	13.95
2700	23.12	23.86	22.87	21.40	17.51	14.22	13.14
2800	16.46	17.51	17.55	17.84	15.47	13.55	12.61
2900	12.59	13.49	13.78	14.57	13.60	13.06	12.46
3000	10.45	11.25	11.60	12.52	12.32	12.90	12.75

Digital Step Attenuator

DAT-15575A-PP+

Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, TEMPERATURE=+25degC

FREQUENCY (MHz)	STEP ATTENUATION* AT TTL CONTROL STATE (dB)						
	00000 THRU LOSS	00001 0.5 dB	00010 1.0 dB	00100 2.0 dB	01000 4.0 dB	10000 8.0 dB	11111 15.5 dB
10	1.08	0.52	1.02	2.02	4.03	8.03	15.61
50	1.08	0.51	1.02	2.02	4.03	8.03	15.54
100	1.08	0.52	1.02	2.02	4.03	8.03	15.54
150	1.09	0.51	1.02	2.02	4.03	8.03	15.54
200	1.10	0.52	1.01	2.02	4.03	8.03	15.54
250	1.11	0.51	1.02	2.02	4.03	8.03	15.54
300	1.12	0.51	1.02	2.02	4.02	8.03	15.53
350	1.14	0.51	1.01	2.02	4.02	8.03	15.54
400	1.15	0.51	1.02	2.02	4.02	8.02	15.53
450	1.17	0.51	1.01	2.02	4.02	8.02	15.53
500	1.18	0.51	1.01	2.01	4.01	8.02	15.52
550	1.20	0.51	1.01	2.01	4.01	8.00	15.51
600	1.22	0.51	1.01	2.01	4.01	8.00	15.51
700	1.25	0.51	1.00	2.01	4.00	8.00	15.51
800	1.28	0.50	1.00	2.00	4.00	7.99	15.49
900	1.31	0.50	1.00	2.00	3.99	7.99	15.51
1000	1.35	0.50	1.00	2.00	3.99	8.00	15.53
1100	1.37	0.50	1.00	2.00	3.99	8.00	15.52
1200	1.39	0.50	1.00	2.00	3.99	7.99	15.50
1300	1.41	0.50	1.00	2.00	3.98	7.97	15.43
1400	1.43	0.49	0.99	1.99	3.98	7.96	15.40
1500	1.46	0.49	1.00	2.00	3.99	8.00	15.48
1600	1.52	0.49	1.00	2.00	4.01	8.02	15.54
1700	1.54	0.49	1.00	2.00	4.02	8.03	15.54
1800	1.54	0.49	1.00	2.00	4.02	8.03	15.46
1900	1.54	0.49	1.00	2.00	4.03	8.04	15.47
2000	1.54	0.49	1.00	2.00	4.04	8.05	15.45
2100	1.55	0.49	1.00	2.00	4.05	8.06	15.44
2200	1.58	0.49	1.00	2.00	4.05	8.08	15.43
2300	1.61	0.49	1.00	2.00	4.06	8.12	15.53
2400	1.57	0.49	1.00	2.01	4.08	8.16	15.58
2500	1.40	0.49	1.00	2.01	4.10	8.17	15.50
2600	1.31	0.49	1.01	2.02	4.14	8.23	15.54
2700	1.30	0.49	1.01	2.02	4.17	8.26	15.56
2800	1.48	0.48	1.00	2.02	4.19	8.31	15.70
2900	1.85	0.46	0.98	1.99	4.18	8.33	15.92
3000	2.34	0.44	0.95	1.95	4.14	8.31	16.13

* Step Attenuation above Thru Loss (TTL Logic 00000).



REV. OR
DAT-15575A-PP+

10/12/2016

Page 4 of 9

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 • Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site
The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com



IF/RF MICROWAVE COMPONENTS

Digital Step Attenuator

DAT-15575A-PP+

Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, TEMPERATURE=+25degC

FREQUENCY (MHz)	INPUT RETURN LOSS AT TTL CONTROL STATE (dB)						
	00000 0 dB	00001 0.5 dB	00010 1.0 dB	00100 2.0 dB	01000 4.0 dB	10000 8.0 dB	11111 15.5 dB
10	19.01	19.92	20.54	21.19	32.11	37.76	45.93
50	18.92	19.82	20.43	21.09	31.44	36.72	48.47
100	18.83	19.73	20.33	21.02	31.25	36.37	46.22
150	18.76	19.65	20.24	20.90	30.54	35.08	45.15
200	18.64	19.51	20.11	20.78	30.06	34.17	42.61
250	18.56	19.43	20.00	20.66	29.54	33.16	41.39
300	18.49	19.34	19.92	20.55	29.06	32.47	39.66
350	18.43	19.26	19.82	20.44	28.63	31.70	38.75
400	18.44	19.25	19.81	20.40	28.41	31.27	37.83
450	18.48	19.30	19.85	20.43	28.35	31.09	37.93
500	18.56	19.36	19.91	20.47	28.32	30.91	37.40
550	18.61	19.43	19.96	20.51	28.33	30.90	37.87
600	18.69	19.49	20.03	20.53	28.31	30.73	37.33
700	18.83	19.62	20.16	20.63	28.29	30.48	36.51
800	18.77	19.54	20.07	20.52	27.65	29.34	34.09
900	18.57	19.32	19.84	20.27	26.72	28.03	31.62
1000	18.11	18.85	19.34	19.75	25.61	26.73	29.87
1100	17.46	18.15	18.64	19.01	24.42	25.40	28.47
1200	16.84	17.49	17.93	18.28	23.30	24.24	27.15
1300	16.35	16.93	17.35	17.64	22.38	23.23	25.85
1400	15.98	16.53	16.90	17.14	21.66	22.38	24.59
1500	16.01	16.53	16.88	17.07	21.60	22.28	24.23
1600	16.35	16.81	17.13	17.27	21.76	22.38	23.78
1700	17.07	17.46	17.72	17.78	21.88	22.36	22.92
1800	17.95	18.21	18.36	18.36	21.50	21.90	21.68
1900	18.69	18.77	18.82	18.82	20.65	21.08	20.42
2000	19.16	19.07	19.03	19.09	19.59	20.09	19.25
2100	19.30	19.08	18.97	19.16	18.49	18.99	18.13
2200	19.27	19.02	18.91	19.16	17.88	18.31	17.46
2300	21.05	20.69	20.48	20.77	18.37	18.56	17.52
2400	24.26	23.55	23.01	23.04	18.82	18.42	17.24
2500	32.75	29.14	26.89	25.35	18.53	17.37	16.21
2600	27.67	26.94	25.51	23.51	17.56	16.04	15.06
2700	20.47	20.97	20.98	20.21	16.57	14.98	14.18
2800	15.91	16.55	16.97	17.04	15.51	14.24	13.67
2900	12.98	13.61	14.15	14.59	14.50	13.74	13.48
3000	11.00	11.60	12.15	12.76	13.63	13.45	13.59

Digital Step Attenuator

DAT-15575A-PP+

Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, TEMPERATURE=+25degC

FREQUENCY (MHz)	OUTPUT RETURN LOSS AT TTL CONTROL STATE (dB)						
	00000 0 dB	00001 0.5 dB	00010 1.0 dB	00100 2.0 dB	01000 4.0 dB	10000 8.0 dB	11111 15.5 dB
10	18.83	19.90	20.01	21.10	22.20	36.70	46.64
50	18.87	19.91	20.10	21.07	22.09	36.02	47.94
100	18.67	19.70	19.92	20.89	21.99	35.26	46.38
150	18.57	19.57	19.78	20.75	21.78	34.08	44.79
200	18.39	19.39	19.60	20.55	21.62	33.26	42.37
250	18.29	19.29	19.49	20.41	21.41	32.32	41.51
300	18.13	19.12	19.32	20.24	21.21	31.55	39.29
350	18.01	18.98	19.15	20.06	20.97	30.74	38.49
400	17.85	18.79	18.99	19.87	20.76	30.07	37.26
450	17.76	18.69	18.88	19.75	20.58	29.59	36.72
500	17.70	18.64	18.80	19.64	20.45	29.19	35.93
550	17.70	18.62	18.78	19.63	20.39	29.00	35.82
600	17.78	18.69	18.84	19.68	20.38	28.97	35.67
700	18.03	18.97	19.11	19.93	20.56	29.39	36.82
800	18.37	19.33	19.46	20.28	20.81	29.86	37.84
900	18.73	19.71	19.81	20.63	21.01	30.14	37.89
1000	18.84	19.83	19.89	20.68	20.85	29.29	35.60
1100	18.68	19.63	19.66	20.38	20.38	27.67	32.32
1200	18.54	19.44	19.43	20.08	19.89	26.39	30.22
1300	18.43	19.28	19.22	19.76	19.43	25.49	28.91
1400	18.30	19.10	18.95	19.39	18.93	24.64	27.65
1500	18.06	18.76	18.57	18.91	18.46	23.83	26.37
1600	17.35	17.94	17.76	18.03	17.69	22.47	24.41
1700	16.88	17.41	17.26	17.54	17.35	21.91	23.60
1800	16.62	17.12	17.02	17.37	17.46	21.99	23.41
1900	16.53	17.01	17.02	17.42	17.89	22.38	23.50
2000	16.65	17.11	17.24	17.74	18.78	22.94	23.38
2100	17.06	17.52	17.73	18.32	20.05	23.14	22.64
2200	18.45	18.89	19.19	19.79	22.43	23.15	21.63
2300	21.22	21.55	21.93	22.40	26.71	22.47	20.44
2400	26.08	25.84	26.47	26.25	33.37	21.04	19.09
2500	32.28	30.00	30.88	28.42	28.05	19.20	17.61
2600	36.88	43.17	36.94	29.97	23.57	17.90	16.47
2700	22.46	24.39	24.06	24.21	19.57	16.71	15.49
2800	16.44	17.59	17.75	18.62	16.40	15.60	14.77
2900	12.90	13.80	14.07	15.03	14.08	14.69	14.41
3000	10.84	11.61	11.91	12.88	12.57	14.20	14.50

Digital Step Attenuator

DAT-15575A-PP+

Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, TEMPERATURE=+105degC

FREQUENCY (MHz)	STEP ATTENUATION* AT TTL CONTROL STATE						
	00000 THRU LOSS	00001 0.5 dB	00010 1.0 dB	00100 2.0 dB	01000 4.0 dB	10000 8.0 dB	11111 15.5 dB
10	1.26	0.49	0.99	2.00	3.94	7.88	15.34
50	1.25	0.50	0.99	2.00	3.94	7.87	15.31
100	1.25	0.50	0.99	2.00	3.94	7.88	15.31
150	1.26	0.50	1.00	2.00	3.95	7.89	15.31
200	1.27	0.50	0.99	2.00	3.95	7.88	15.31
250	1.29	0.50	0.99	2.00	3.94	7.88	15.30
300	1.32	0.50	0.99	2.00	3.94	7.88	15.30
350	1.35	0.49	0.99	1.99	3.93	7.87	15.28
400	1.38	0.49	0.99	1.99	3.93	7.87	15.28
450	1.40	0.49	0.99	1.99	3.93	7.86	15.27
500	1.42	0.49	0.98	1.99	3.93	7.86	15.28
550	1.43	0.49	0.99	1.99	3.93	7.86	15.27
600	1.45	0.49	0.99	1.99	3.93	7.86	15.29
700	1.47	0.49	0.99	1.99	3.93	7.87	15.28
800	1.51	0.49	0.98	1.99	3.93	7.86	15.28
900	1.54	0.49	0.98	1.99	3.93	7.88	15.30
1000	1.59	0.48	0.98	1.99	3.93	7.88	15.33
1100	1.63	0.48	0.98	1.99	3.93	7.88	15.32
1200	1.68	0.48	0.98	1.98	3.92	7.87	15.29
1300	1.71	0.48	0.98	1.98	3.92	7.85	15.23
1400	1.75	0.48	0.98	1.98	3.93	7.86	15.23
1500	1.81	0.47	0.98	1.99	3.94	7.89	15.30
1600	1.87	0.48	0.98	1.99	3.96	7.92	15.35
1700	1.89	0.47	0.98	1.99	3.96	7.93	15.34
1800	1.90	0.47	0.98	1.99	3.96	7.93	15.29
1900	1.88	0.47	0.98	1.99	3.97	7.95	15.30
2000	1.85	0.47	0.98	1.99	3.97	7.95	15.28
2100	1.80	0.47	0.97	1.98	3.97	7.97	15.27
2200	1.74	0.47	0.98	1.98	3.98	7.98	15.27
2300	1.65	0.47	0.98	1.98	4.00	8.03	15.37
2400	1.53	0.47	0.97	1.98	4.02	8.05	15.36
2500	1.41	0.46	0.97	1.97	4.02	8.03	15.24
2600	1.42	0.46	0.96	1.97	4.03	8.05	15.26
2700	1.49	0.45	0.96	1.96	4.04	8.07	15.28
2800	1.71	0.44	0.95	1.95	4.04	8.09	15.39
2900	2.09	0.43	0.93	1.93	4.04	8.11	15.56
3000	2.55	0.42	0.91	1.90	4.02	8.11	15.75

* Step Attenuation above Thru Loss (TTL Logic 00000).



REV. OR
DAT-15575A-PP+
10/12/2016
Page 7 of 9

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 • Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site
The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com



IF/RF MICROWAVE COMPONENTS

Digital Step Attenuator

DAT-15575A-PP+

Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, TEMPERATURE=+105degC

FREQUENCY (MHz)	INPUT RETURN LOSS AT TTL CONTROL STATE						
	(dB)						
	00000 0 dB	00001 0.5 dB	00010 1.0 dB	00100 2.0 dB	01000 4.0 dB	10000 8.0 dB	11111 15.5 dB
10	17.84	18.25	18.38	18.51	23.12	23.28	24.95
50	17.60	18.02	18.19	18.25	22.85	22.99	24.63
100	17.68	18.13	18.34	18.43	23.17	23.43	25.28
150	17.45	17.92	18.12	18.24	22.90	23.23	25.08
200	17.49	17.97	18.18	18.31	23.01	23.36	25.25
250	17.30	17.75	17.96	18.08	22.52	22.81	24.54
300	17.29	17.72	17.94	18.03	22.31	22.52	24.12
350	17.17	17.56	17.74	17.80	21.86	21.95	23.36
400	17.24	17.62	17.78	17.79	21.75	21.77	23.08
450	17.27	17.62	17.77	17.73	21.60	21.50	22.67
500	17.41	17.76	17.88	17.81	21.65	21.48	22.64
550	17.52	17.85	17.96	17.86	21.70	21.46	22.55
600	17.68	18.02	18.12	18.02	21.93	21.68	22.77
700	18.06	18.41	18.52	18.40	22.56	22.29	23.43
800	18.27	18.66	18.80	18.71	23.18	22.93	24.26
900	18.15	18.57	18.78	18.73	23.35	23.18	24.77
1000	17.57	18.01	18.24	18.25	22.65	22.60	24.29
1100	16.70	17.12	17.37	17.41	21.38	21.40	22.96
1200	15.84	16.23	16.47	16.51	20.07	20.10	21.43
1300	15.18	15.53	15.75	15.74	18.94	18.88	19.94
1400	14.84	15.12	15.30	15.22	18.14	17.95	18.74
1500	14.92	15.15	15.26	15.10	17.82	17.51	18.05
1600	15.24	15.39	15.43	15.19	17.65	17.27	17.55
1700	15.77	15.82	15.77	15.46	17.54	17.15	17.14
1800	16.25	16.20	16.08	15.74	17.35	17.07	16.84
1900	16.59	16.45	16.29	16.03	17.14	17.08	16.69
2000	16.86	16.70	16.55	16.40	17.03	17.27	16.75
2100	17.24	17.06	16.91	16.92	17.07	17.58	16.95
2200	17.84	17.69	17.55	17.70	17.59	18.33	17.59
2300	20.03	19.88	19.75	20.05	19.51	20.49	19.35
2400	24.39	24.37	24.34	24.98	22.68	23.55	21.52
2500	32.09	35.18	38.04	43.16	24.02	23.14	21.23
2600	25.65	27.30	29.00	28.63	22.51	20.77	19.65
2700	19.89	20.82	21.73	21.91	20.53	18.85	18.28
2800	16.10	16.83	17.58	18.01	18.56	17.43	17.31
2900	13.49	14.13	14.81	15.36	16.87	16.37	16.73
3000	11.56	12.14	12.77	13.37	15.44	15.55	16.42

Digital Step Attenuator

DAT-15575A-PP+

Typical Performance Data

TEST CONDITIONS: INPUT POWER=-10dBm, Vdd=+3V, TEMPERATURE=+105degC

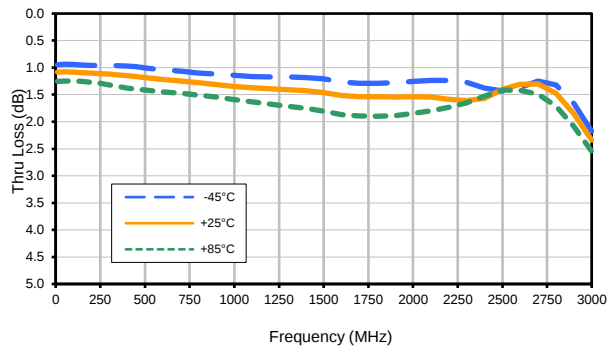
FREQUENCY (MHz)	OUTPUT RETURN LOSS AT TTL CONTROL STATE						
	(dB)						
	00000 0 dB	00001 0.5 dB	00010 1.0 dB	00100 2.0 dB	01000 4.0 dB	10000 8.0 dB	11111 15.5 dB
10	17.70	18.25	18.11	18.39	18.21	23.12	25.02
50	17.57	18.12	17.99	18.25	18.11	22.84	24.73
100	17.75	18.33	18.19	18.51	18.41	23.43	25.52
150	17.56	18.14	18.03	18.36	18.29	23.28	25.36
200	17.46	18.05	17.96	18.32	18.27	23.24	25.37
250	17.05	17.62	17.52	17.87	17.82	22.43	24.32
300	16.80	17.33	17.25	17.59	17.52	21.89	23.67
350	16.51	17.03	16.93	17.23	17.13	21.20	22.76
400	16.42	16.93	16.81	17.09	16.95	20.90	22.35
450	16.43	16.92	16.79	17.03	16.84	20.63	21.97
500	16.54	17.01	16.87	17.09	16.89	20.65	21.96
550	16.74	17.22	17.04	17.24	17.00	20.74	21.99
600	16.94	17.41	17.24	17.43	17.16	20.99	22.21
700	17.42	17.91	17.72	17.91	17.60	21.67	22.92
800	17.86	18.41	18.20	18.41	18.05	22.43	23.77
900	18.20	18.80	18.60	18.83	18.41	23.19	24.69
1000	18.24	18.89	18.67	18.95	18.41	23.41	25.18
1100	17.94	18.58	18.38	18.66	18.04	22.93	24.82
1200	17.52	18.13	17.93	18.19	17.48	21.99	23.76
1300	17.15	17.70	17.46	17.64	16.83	20.87	22.27
1400	16.86	17.33	17.03	17.07	16.20	19.75	20.76
1500	16.63	16.98	16.61	16.55	15.67	18.74	19.45
1600	16.14	16.39	16.03	15.92	15.13	17.82	18.34
1700	15.91	16.11	15.76	15.65	14.99	17.49	17.90
1800	15.82	15.98	15.69	15.61	15.17	17.55	17.81
1900	15.99	16.13	15.90	15.86	15.66	17.95	18.08
2000	16.44	16.56	16.40	16.41	16.58	18.68	18.60
2100	17.04	17.13	17.07	17.14	17.82	19.57	19.17
2200	18.01	18.03	18.10	18.25	19.62	20.70	19.94
2300	19.39	19.44	19.61	19.86	22.23	22.15	20.78
2400	21.84	21.98	22.28	22.66	27.02	23.79	21.52
2500	25.17	25.90	26.54	27.61	35.50	24.34	21.58
2600	25.74	28.77	29.31	34.81	27.36	23.42	21.00
2700	20.68	22.58	22.75	24.90	20.96	21.25	19.85
2800	16.41	17.61	17.84	19.23	17.25	19.11	18.67
2900	13.41	14.32	14.57	15.72	14.70	17.40	17.85
3000	11.39	12.16	12.42	13.43	12.95	16.19	17.37

Digital Step Attenuator

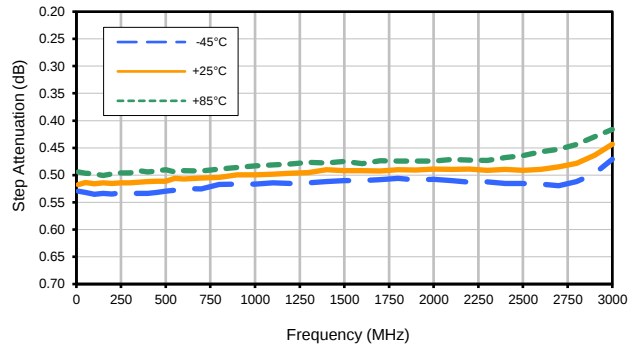
Typical Performance Curves

DAT-15575-PP+

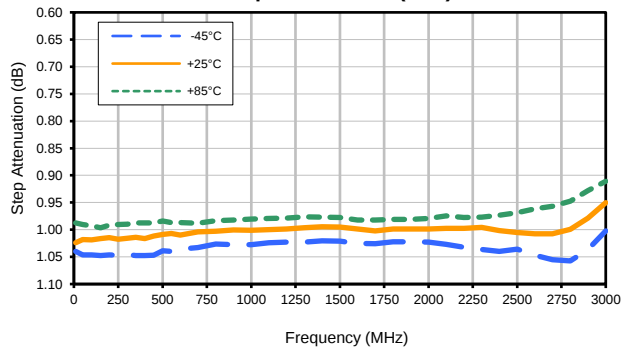
Thru Loss



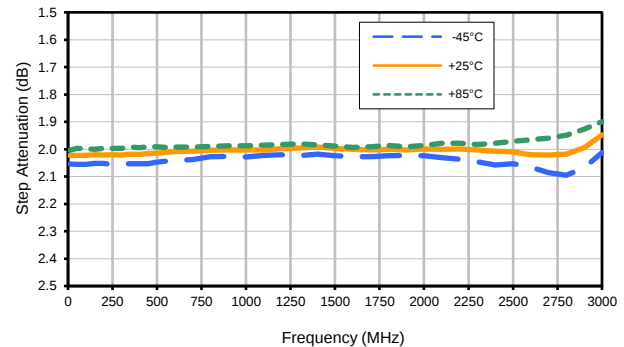
Step Attenuation (0.5dB)



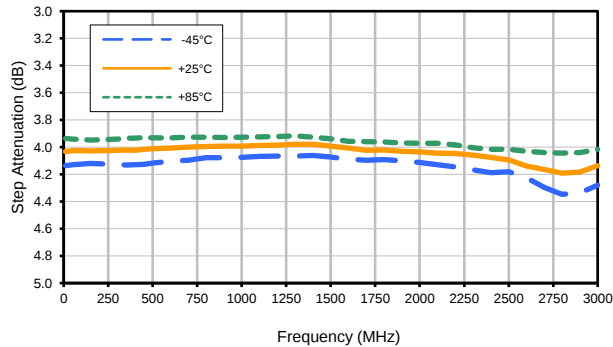
Step Attenuation (1dB)



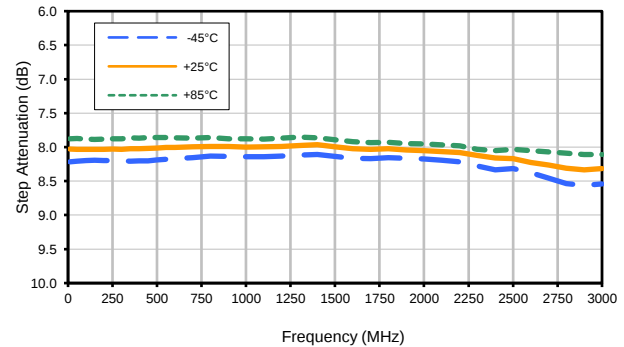
Step Attenuation (2dB)



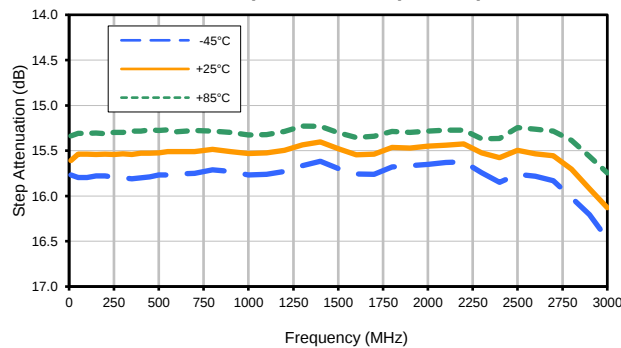
Step Attenuation (4dB)



Step Attenuation (8dB)



Step Attenuation (15.5 dB)

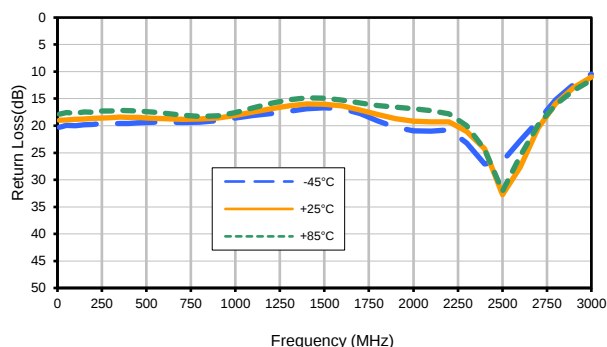


Digital Step Attenuator

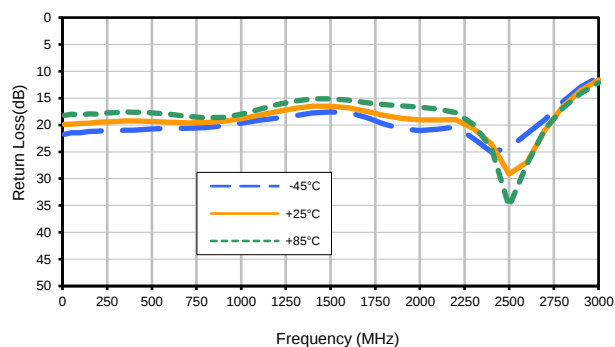
Typical Performance Curves

DAT-15575-PP+

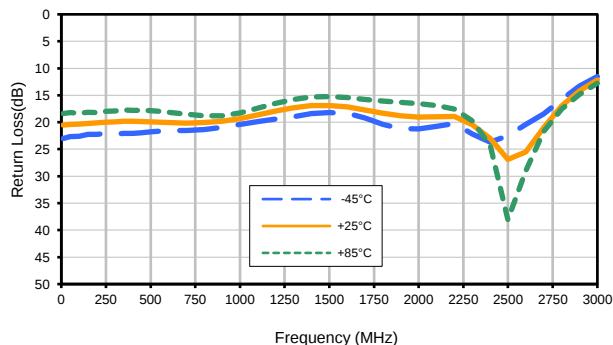
Input Return Loss (0dB)



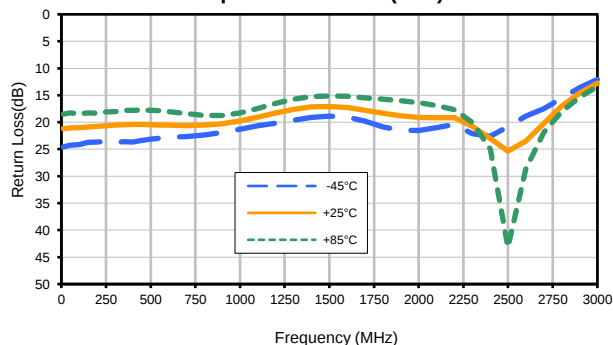
Input Return Loss (0.5dB)



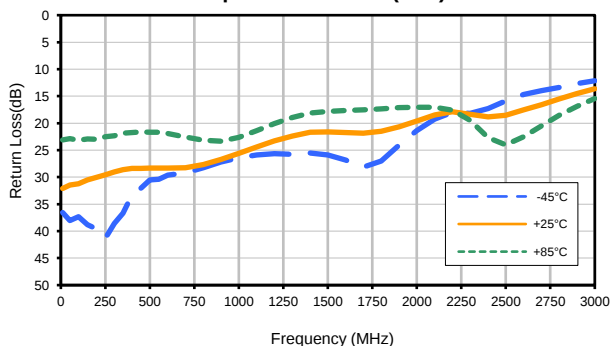
Input Return Loss (1dB)



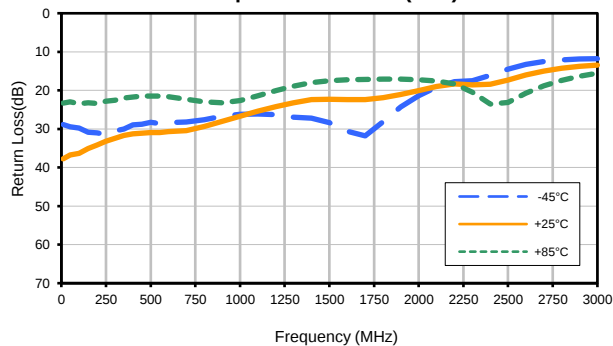
Input Return Loss (2dB)



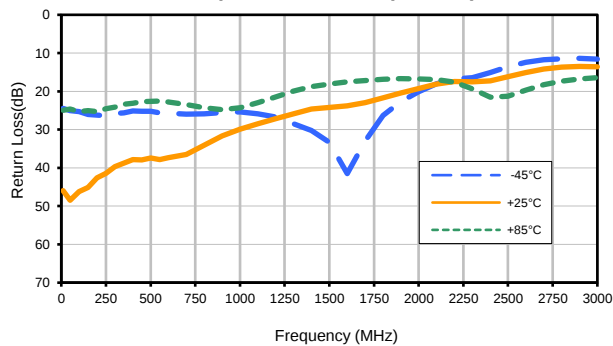
Input Return Loss (4dB)



Input Return Loss (8dB)



Input Return Loss (15.5 dB)



Mini-Circuits®
ISO 9001 ISO 14001 AS 9100 CERTIFIED

minicircuits.com

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 • Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site
The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com



IF/RF MICROWAVE COMPONENTS

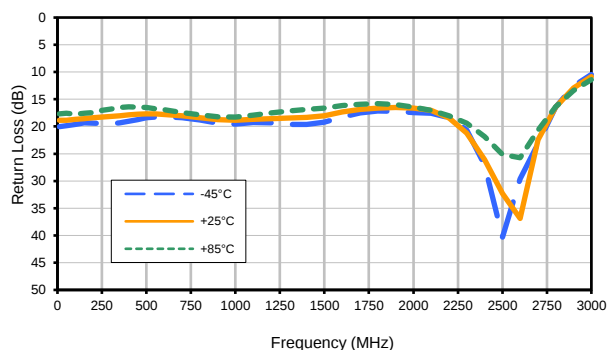
REV. OR
DAT-15575A-PP+
10/12/2016
Page 2 of 3

Digital Step Attenuator

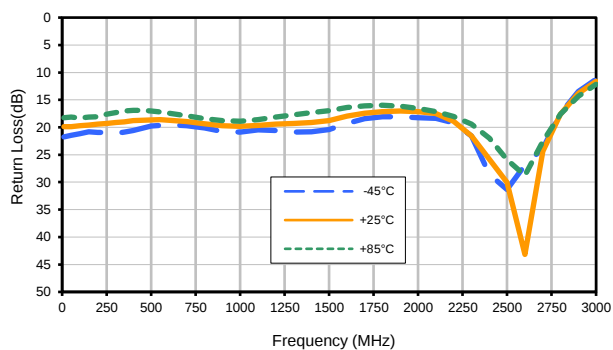
Typical Performance Curves

DAT-15575-PP+

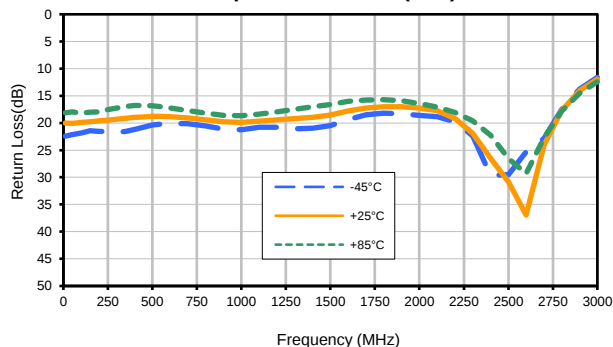
Output Return Loss (0dB)



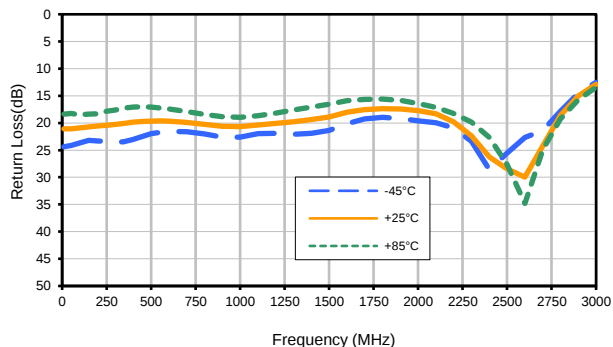
Output Return Loss (0.5dB)



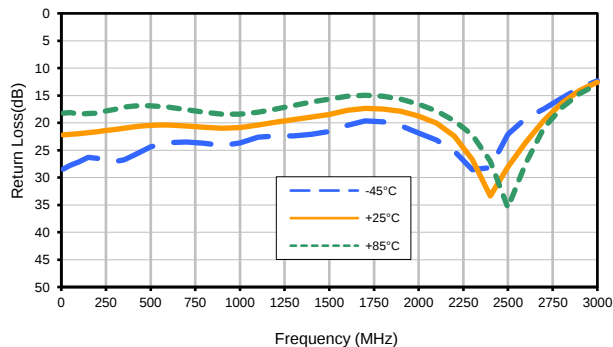
Output Return Loss (1dB)



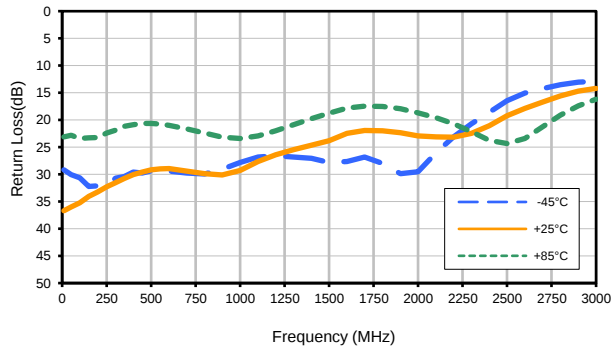
Output Return Loss (2dB)



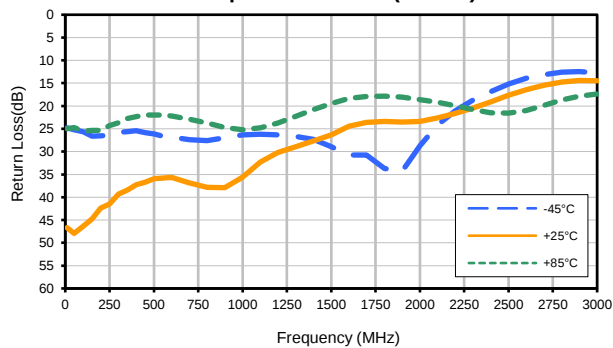
Output Return Loss (4dB)



Output Return Loss(8dB)



Output Return Loss(15.5dB)



Mini-Circuits®
ISO 9001 ISO 14001 AS 9100 CERTIFIED

minicircuits.com

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 • Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

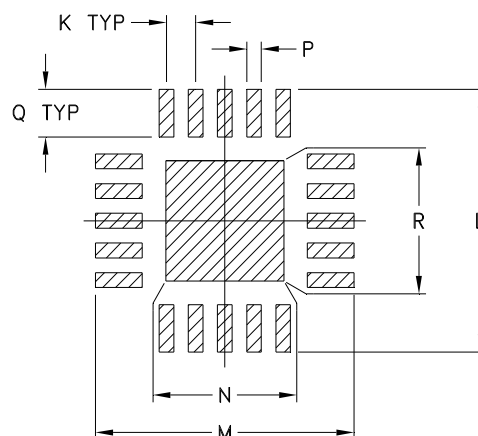
IF/RF MICROWAVE COMPONENTS

REV. OR
DAT-15575A-PP+
10/12/2016
Page 3 of 3

Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm.002$

CASE #	A	B	C	D	E	F	G	H	J	K
DG983-2	.157 (4.00)	.157 (4.00)	.033 (0.85)	.008 (0.20)	.085 (2.15)	.085 (2.15)	.009 (0.23)	--	.022 (0.55)	.020 (0.50)

CASE #	L	M	N	P	Q	R	WT. GRAM
DG983-2	.177 (4.50)	.177 (4.50)	.081 (2.06)	.010 (0.25)	.032 (0.81)	.081 (2.06)	.04

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01$; 3 Pl. $\pm .005$

Notes:

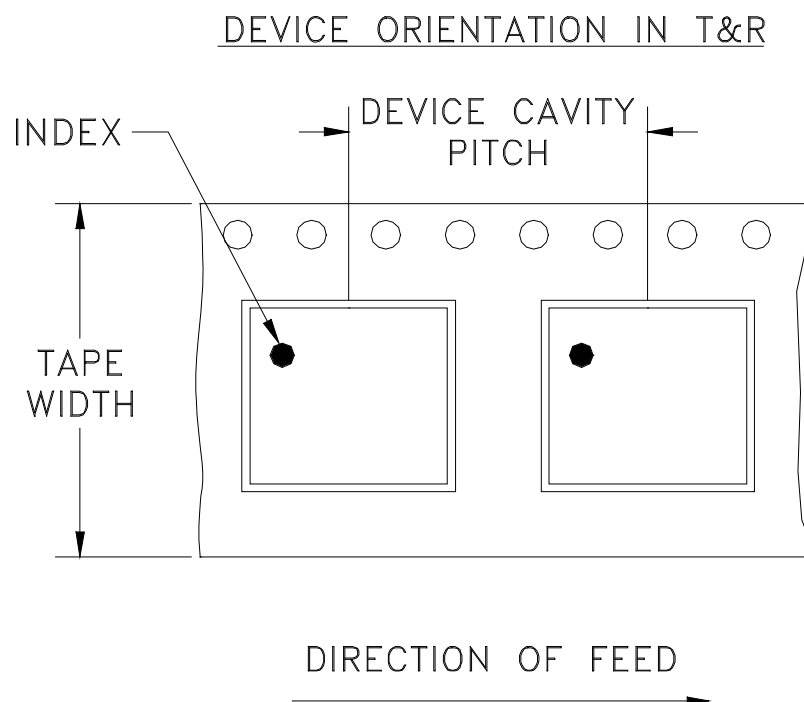
1. Case material: Plastic.
2. Termination finish:

For RoHS Case Styles: 0.2 μ inches of Gold (Au) over 0.1 μ inched of Palladium (Pd) over 10 μ inches of Nickel (Ni). All models, (+) suffix.

For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



Tape & Reel Packaging TR-F87



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
12	8	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
		13	Standard	3000

Note : Please Consult individual model data sheet to determine device per reel availability

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



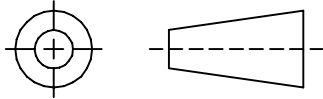
Distribution Centers NORTH AMERICA 800-654-7949 • 417-335-5935 • Fax 417-335-5945 • EUROPE 44-1252-832600 • Fax 44-1252-837010

Mini-Circuits ISO 9001 & ISO 14001 Certified

INTERNET <http://www.minicircuits.com>

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661

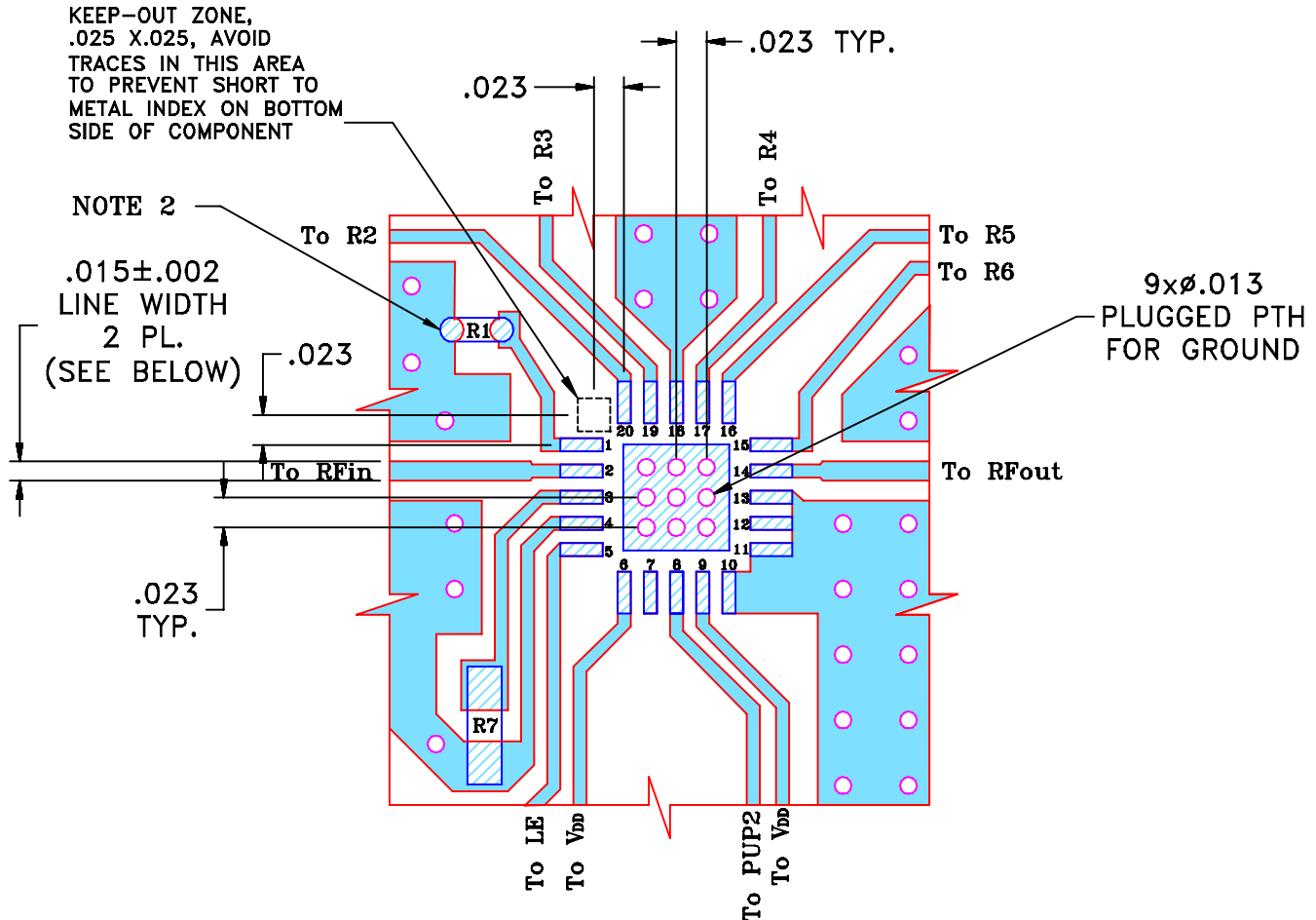
THIRD ANGLE PROJECTION



REVISIONS

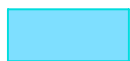
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M97254	NEW RELEASE (FROM RAVON)	03/05	DK	HH
A	M102713	MODIFIED HATCH, NOTES & ADDED "...WITH SMOBC"	01/06	GT	IL
B	M103510	ADD R7 & CHANGE LOCATION DESIGNATORS	07/09	EM	KN
B	R63339	ADD R7 & CHANGE LOCATION DESIGNATORS	07/09	EM	KN

SUGGESTED MOUNTING CONFIGURATION

FOR DG983-1 CASE STYLE, qv PIN CONNECTIONS, 75 Ω .

NOTES:

1. TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS. .025" $\pm$.002". COPPER: 1/2 OZ. EACH SIDE.
FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. 0603, 0402 SIZES CHIP FOOT PRINTS SHOWN FOR REFERENCE,
VALUES OF RESISTORS WILL VARY BASED ON APPLICATION.
3. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDERMASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

DRAWN

DK (RAVON)

08 MAR 05

TOLERANCES ON:

CHECKED

RZ (RAVON)

08 MAR 05

2 PL DECIMALS $\pm$.005

APPROVED

HH (RAVON)

08 MAR 05

ANGLES $\pm$ FRACTIONS $\pm$

Mini-Circuits®

THIS DOCUMENT AND ITS CONTENTS ARE THE PROPERTY OF MINI-CIRCUITS. EXCEPT FOR USE EXPRESSLY GRANTED, IN WRITING, TO ITS VENDORS, VENDEE AND THE UNITED STATES GOVERNMENT, MINI-CIRCUITS RESERVES ALL PROPRIETARY DESIGN, USE, MANUFACTURING AND REPRODUCTION RIGHTS THERETO. THESE CONTENTS SHALL NOT BE USED, DUPLICATED OR DISCLOSED TO ANY OUTSIDE PARTY, IN WHOLE OR IN PART, WITHOUT WRITTEN PERMISSION OF MINI-CIRCUITS.

ASHEETA1.DWG REV:A DATE:01/12/95



Mini-Circuits®

13 Neptune Avenue
Brooklyn NY 11235PL, qv, DG983-1
TB-337 (75 Ω)

SIZE

A

CODE IDENT

15542

DRAWING NO:

98-PL-201

REV:

B

FILE:

98PL201

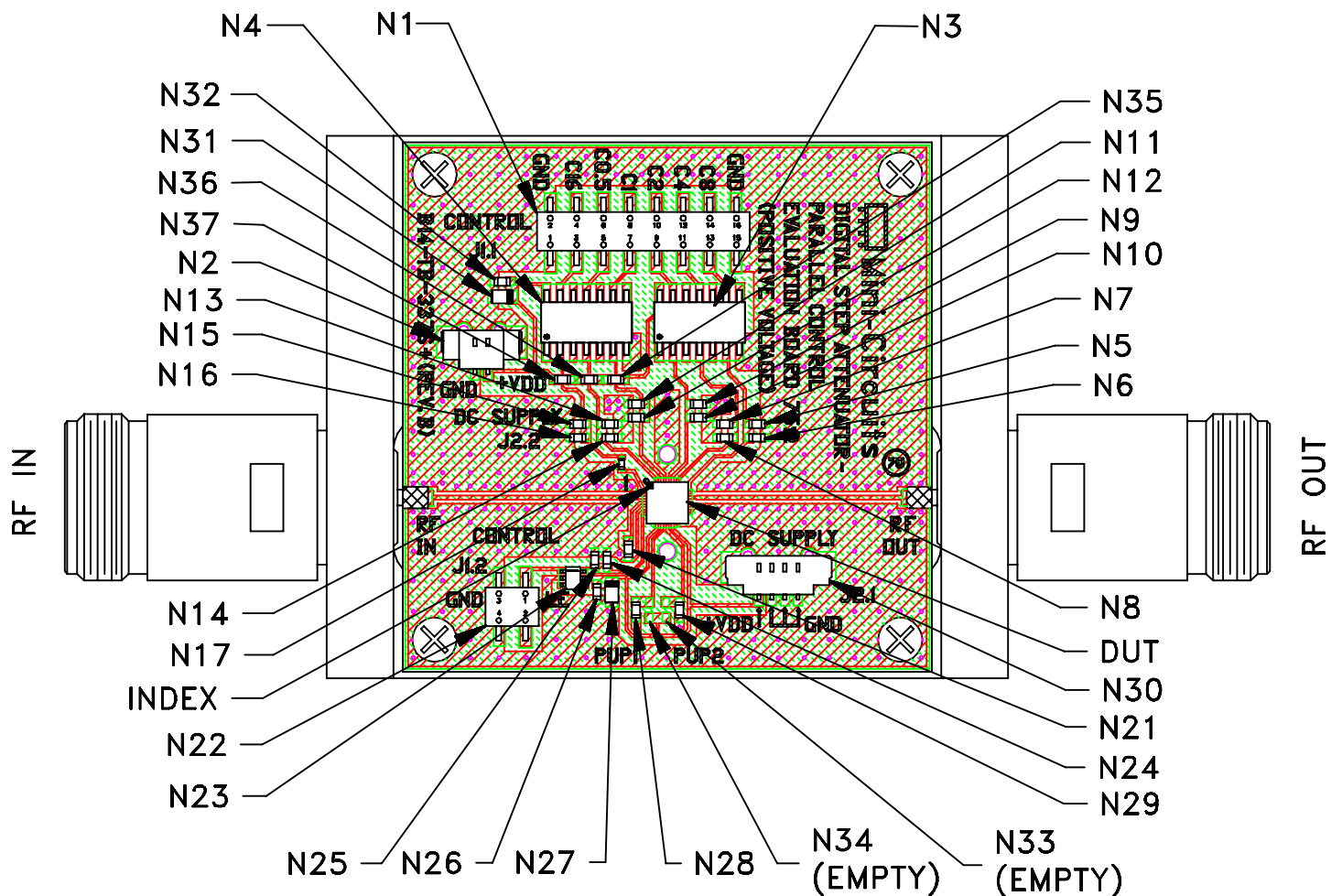
SCALE:

7:1

SHEET:

1 OF 1

Evaluation Board and Circuit

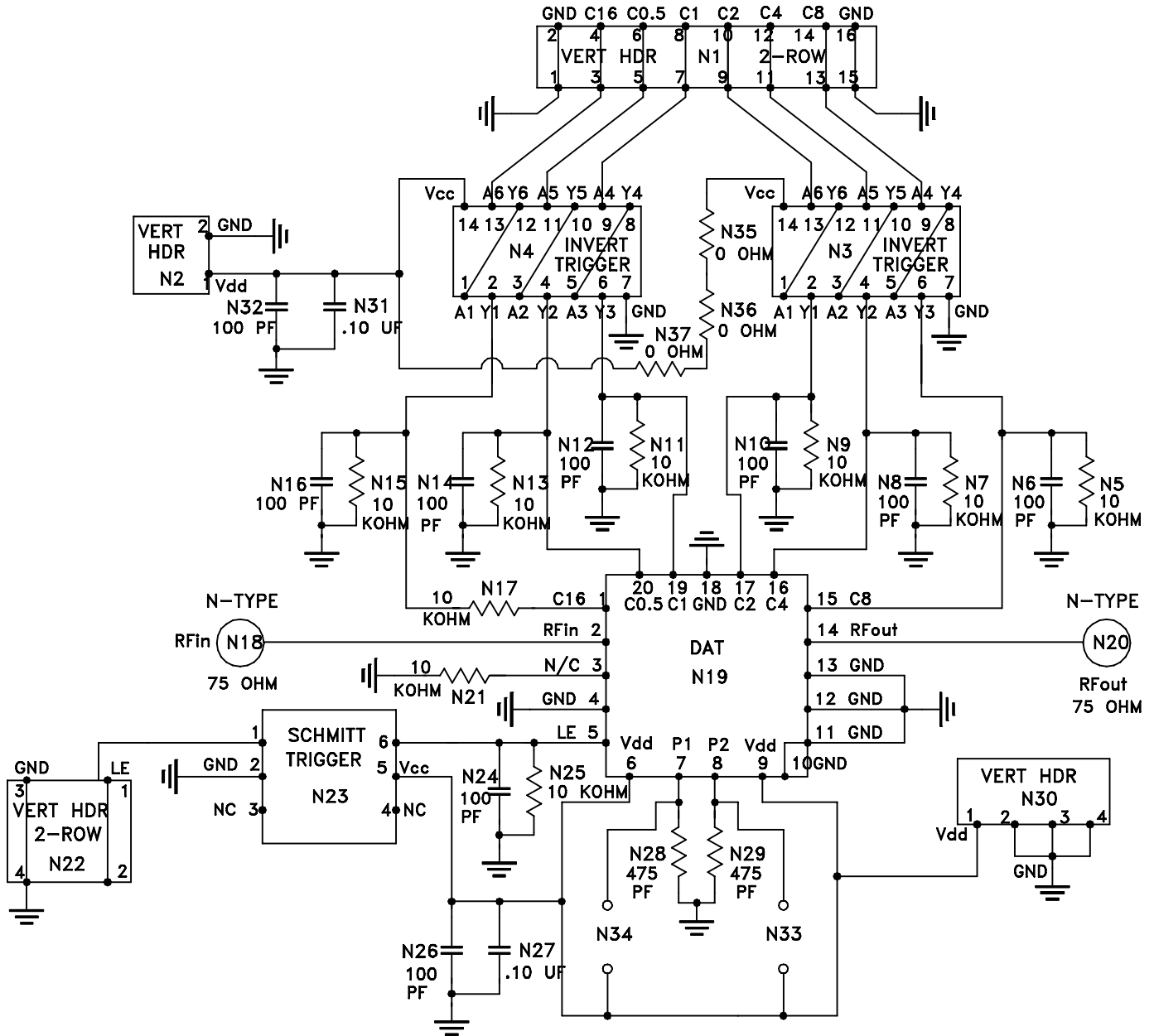


TB-337

Notes:

1. N-Type Female connectors.
2. PCB Material: FR4 Grade IT 180TC (ITEQ Corporation) or equivalent, Dielectric Constant=4.7, Thickness=.025 inch.

 **Mini-Circuits®**





All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C or -40° to 105° C Ambient Environment	Refer to Individual Model Data Sheet
Storage Temperature	-55° to 100° C or -65° to 150° C Ambient Environment	Refer to Individual Model Data Sheet
Temperature Humidity Bias	85°C, 85% RH, 96 hours	JESD22-A101B
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Pb-Free Process: 260°C peak	J-STD-020, Table 4-1, 4-2 and 5-2; Figure 5-1
Solderability	10X magnification, 95% coverage	JESD22-B102, Method 1: Dip and Look Test
Marking Resistance to Solvents	Laser marked, visual observation	Mini-Circuits D4-Q4T0-04