

Surface Mount Bias-Tee

JEBT-4R2GW

50Ω Wideband 0.1 to 4200 MHz

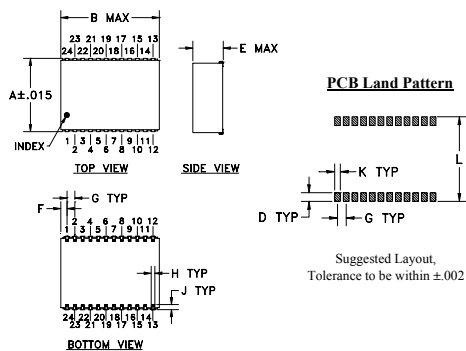
Maximum Ratings

Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
RF Power	30dBm max.
Voltage at DC port	25V max.
Input Current	500mA
DC resistance from DC to RF&DC port	4.5 ohm typ.
Permanent damage may occur if any of these limits are exceeded.	

Pin Connections

RF	15
RF&DC	10
DC	24
GROUND	all other pins

Outline Drawing

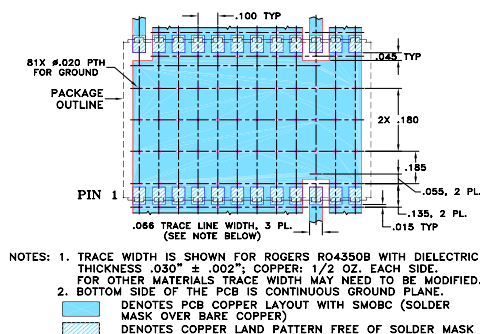


Outline Dimensions (inch)

A	B	C	D	E	F
.940	1.260	--	.100	.390	.080
23.88	32.00	--	2.54	9.91	2.03

G	H	J	K	L	wt
.100	.047	.065	.065	.970	grams
2.54	1.19	1.65	1.65	24.64	7.2

Demo Board MCL P/N: TB-359 Suggested PCB Layout (PL-216)



Features

- wideband, 0.1 to 4200 MHz
- low insertion loss, 0.6 dB typ.
- good isolation, 40 dB typ.

Applications

- biasing amplifiers, laser diodes, active antenna
- DC return
- DC blocking
- satellite communication



Generic photo used for illustration purposes only

CASE STYLE: BL301

Bias-Tee Electrical Specifications

FREQUENCY (MHz)	INSERTION LOSS* (dB)						ISOLATION* (dB) (RF port to DC port) (RF&DC port to DC port)					
	L		M		U		L		M		U	
f _L f _U	Typ.	Max.	Typ.	Max.	Typ.	Max.	Typ.	Min.	Typ.	Min.	Typ.	Min.
0.1 4200	0.15	0.8	0.6	1.2	0.6	1.6	25	15	40	20	40	20

L= low range (f_L to 10 f_L)

M= mid range (10 f_L to f_U/2)

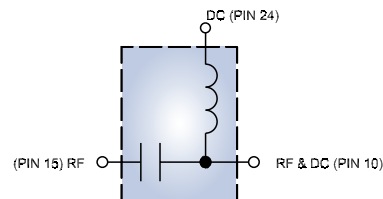
U= upper range (f_U/2 to f_U)

* Insertion Loss and Isolation are guaranteed up to 20 dBm-RF power and 200mA DC current.

Typical Performance Data

Freq. (MHz)	Pin (dBm)	INSERTION LOSS (dB) with Current						ISOLATION (dB) (Pin= -10dBm) with current						VSWR (:1)
		0mA	20mA	50mA	100mA	150mA	200mA	10mA	20mA	50mA	100mA	150mA	200mA	
0.10	19.80	0.17	0.17	0.16	0.17	0.20	0.24	19.46	19.04	17.83	14.58	12.66	11.75	1.16
0.27	19.80	0.13	0.13	0.13	0.14	0.14	0.15	25.86	25.53	24.52	21.43	19.31	18.16	1.07
0.53	19.80	0.12	0.12	0.12	0.11	0.11	0.11	29.17	28.98	28.36	26.18	24.40	23.37	1.04
1.06	19.80	0.13	0.13	0.12	0.11	0.12	0.12	30.81	30.74	30.56	29.62	28.62	27.92	1.02
10.00	18.50	0.16	0.17	0.17	0.16	0.16	0.16	30.06	30.07	30.07	30.20	30.38	30.56	1.04
114.75	19.50	0.22	0.25	0.24	0.22	0.22	0.22	34.45	34.49	34.27	33.99	33.83	33.59	1.07
324.25	19.70	0.50	0.55	0.53	0.52	0.53	0.56	44.65	44.61	44.25	43.90	43.91	43.34	1.06
743.25	18.70	0.28	0.31	0.30	0.29	0.29	0.29	51.19	50.50	50.16	50.65	51.69	52.47	1.06
952.75	18.20	0.31	0.33	0.33	0.31	0.32	0.33	40.75	40.80	40.97	40.97	40.93	40.95	1.11
1581.25	18.00	0.46	0.48	0.47	0.46	0.48	0.49	42.58	42.59	43.94	43.77	44.36	44.17	1.13
2000.25	17.10	0.46	0.48	0.47	0.46	0.46	0.47	45.46	45.57	45.73	45.48	46.14	45.28	1.12
2524.00	14.40	0.40	0.42	0.41	0.42	0.43	0.44	53.15	53.72	52.19	53.17	52.67	53.67	1.12
3047.75	14.20	0.45	0.48	0.47	0.46	0.46	0.49	52.46	52.25	51.55	51.33	51.46	50.99	1.09
3676.25	15.10	0.73	0.74	0.75	0.75	0.75	0.75	46.32	47.19	46.36	45.53	46.19	45.65	1.07
4200.00	17.90	1.04	1.07	1.07	1.06	1.05	1.06	28.42	28.36	28.24	28.14	28.01	27.92	1.09
4502.50	-0.60	1.17	1.19	1.18	1.19	1.17	1.16	28.15	28.10	28.05	27.96	27.84	27.87	1.14
4802.00	-0.70	1.26	1.26	1.27	1.25	1.22	1.20	37.95	38.01	38.19	37.93	37.58	37.51	1.12
5251.75	-1.10	1.19	1.17	1.16	1.13	1.11	1.09	49.68	51.04	49.12	49.37	49.13	48.19	1.11
5550.75	-2.00	1.65	1.63	1.60	1.56	1.54	1.51	38.44	38.56	38.36	38.07	37.85	38.19	1.10
6000.00	-2.40	1.70	1.71	1.65	1.59	1.54	1.50	34.37	34.36	34.23	34.40	34.49	34.48	1.12

Electrical Schematic



Notes

- Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
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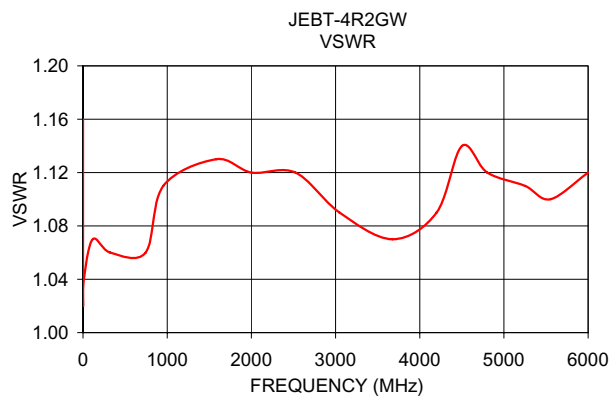
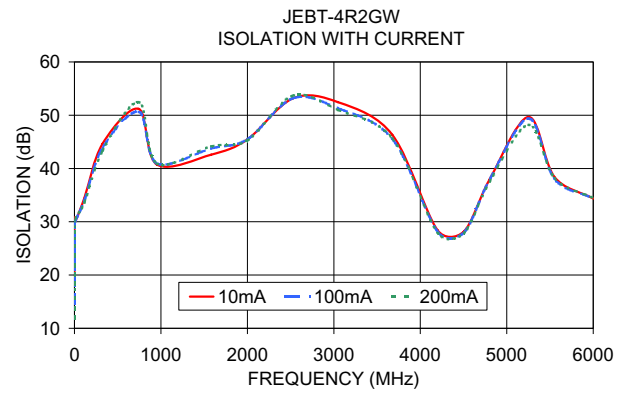
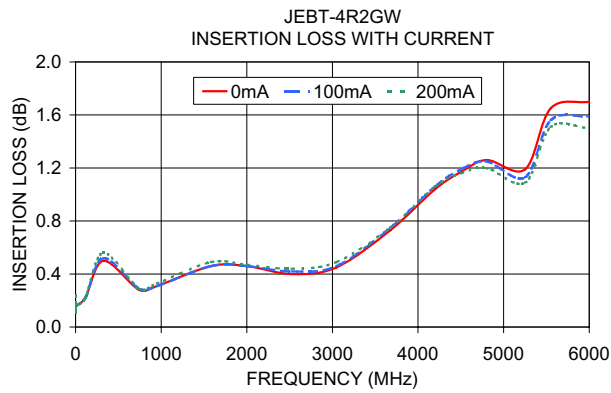


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REV. D
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DJ/RS/CP
210128

Performance Charts

JEBT-4R2GW



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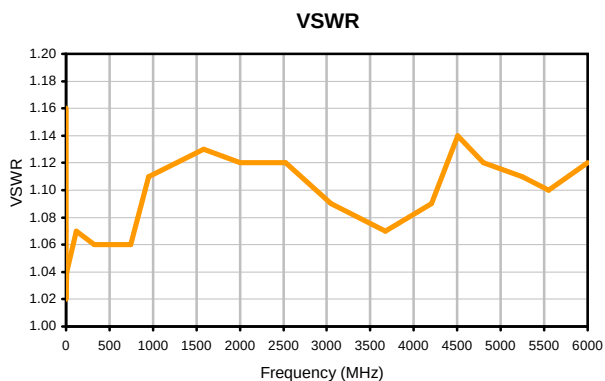
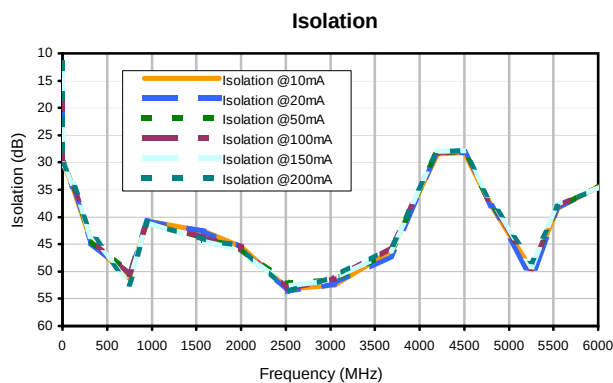
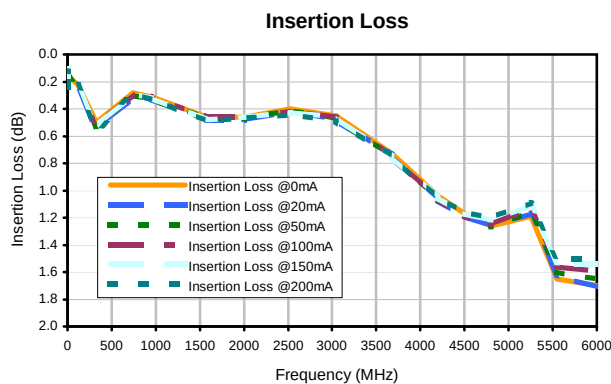
Typical Performance Data

FREQ. (MHz)	INSERTION LOSS with current (RF Port to RF&DC Port) (dB)						ISOLATION with current (RF Port to DC Port, RF&DC Port to DC Port) (dB)						VSWR (:1)
	0mA	20mA	50mA	100mA	150mA	200mA	10mA	20mA	50mA	100mA	150mA	200mA	
0.10	0.17	0.17	0.16	0.17	0.20	0.24	19.46	19.04	17.83	14.58	12.66	11.75	1.16
0.27	0.13	0.13	0.13	0.14	0.14	0.15	25.86	25.53	24.52	21.43	19.31	18.16	1.07
0.53	0.12	0.12	0.12	0.11	0.11	0.11	29.17	28.98	28.36	26.18	24.40	23.37	1.04
1.06	0.13	0.13	0.12	0.11	0.12	0.12	30.81	30.74	30.56	29.62	28.62	27.92	1.02
10.00	0.16	0.17	0.17	0.16	0.16	0.16	30.06	30.07	30.07	30.20	30.38	30.56	1.04
114.75	0.22	0.25	0.24	0.22	0.22	0.22	34.45	34.49	34.27	33.99	33.83	33.59	1.07
324.25	0.50	0.55	0.53	0.52	0.53	0.56	44.65	44.61	44.25	43.90	43.91	43.34	1.06
743.25	0.28	0.31	0.30	0.29	0.29	0.29	51.19	50.50	50.16	50.65	51.69	52.47	1.06
952.75	0.31	0.33	0.33	0.31	0.32	0.33	40.75	40.80	40.97	40.97	40.93	40.95	1.11
1581.25	0.46	0.48	0.47	0.46	0.48	0.49	42.58	42.59	43.94	43.77	44.36	44.17	1.13
2000.25	0.46	0.48	0.47	0.46	0.46	0.47	45.46	45.57	45.73	45.48	46.14	45.28	1.12
2524.00	0.40	0.42	0.41	0.42	0.43	0.44	53.15	53.72	52.19	53.17	52.67	53.67	1.12
3047.75	0.45	0.48	0.47	0.46	0.46	0.49	52.46	52.25	51.55	51.33	51.46	50.99	1.09
3676.25	0.73	0.74	0.75	0.75	0.75	0.75	46.32	47.19	46.36	45.53	46.19	45.65	1.07
4200.00	1.04	1.07	1.07	1.06	1.05	1.06	28.42	28.36	28.24	28.14	28.01	27.92	1.09
4502.50	1.17	1.19	1.18	1.19	1.17	1.16	28.15	28.10	28.05	27.96	27.84	27.87	1.14
4802.00	1.26	1.26	1.27	1.25	1.22	1.20	37.95	38.01	38.19	37.93	37.58	37.51	1.12
5251.75	1.19	1.17	1.16	1.13	1.11	1.09	49.68	51.04	49.12	49.37	49.13	48.19	1.11
5550.75	1.65	1.63	1.60	1.56	1.54	1.51	38.44	38.56	38.36	38.07	37.85	38.19	1.10
6000.00	1.70	1.71	1.65	1.59	1.54	1.50	34.37	34.36	34.23	34.40	34.49	34.48	1.12

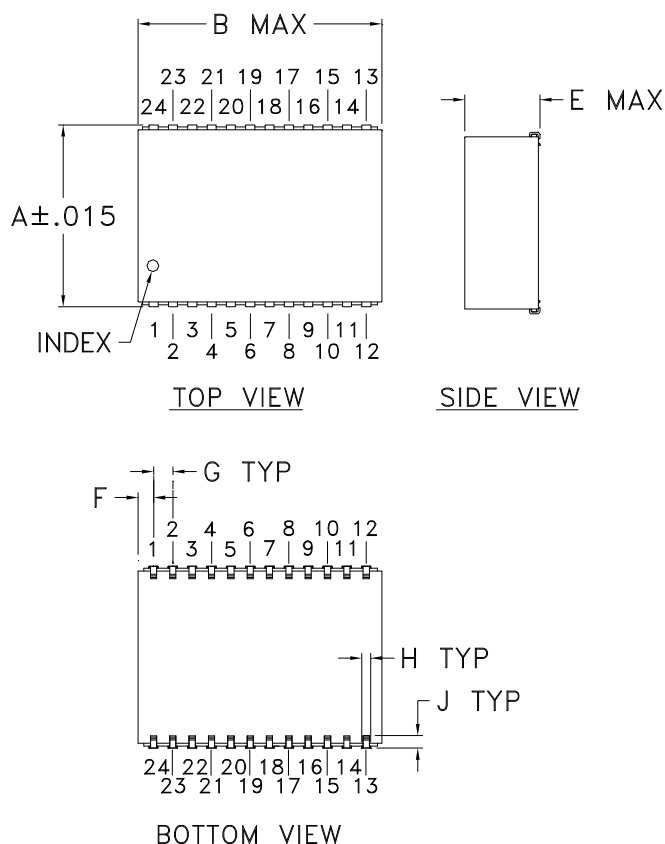
Bias Tee , Surface Mount

Typical Performance Curves

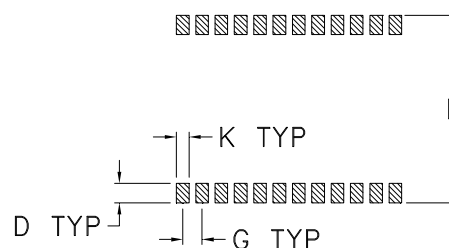
JEBT-4R2GW



Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

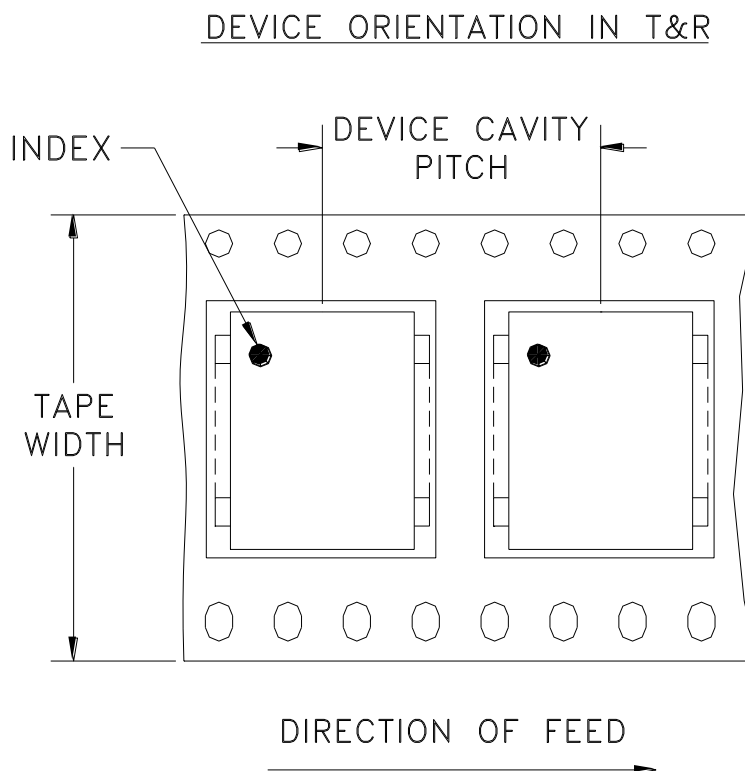
CASE #	A	B	C	D	E	F	G	H	J	K	L	WT. GRAM
BL301	.940 (23.88)	1.260 (32.00)	-- --	.100 (2.54)	.390 (9.91)	.080 (2.03)	.100 (2.54)	.047 (1.19)	.065 (1.65)	.065 (1.65)	.970 (24.64)	7.2

Dimensions are in inches (mm). Tolerances: 2Pl. ±.01; 3Pl. ±.005

Notes:

- Case material: Plastic.
- Base material: Printed wiring laminate.
- Termination finish:
 - For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
 - For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.

Tape & Reel Packaging TR-F25



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
44	32	13	Small quantity standards (see note)	20
				50
				100
			Standard	125

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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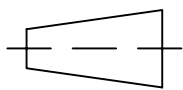
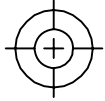
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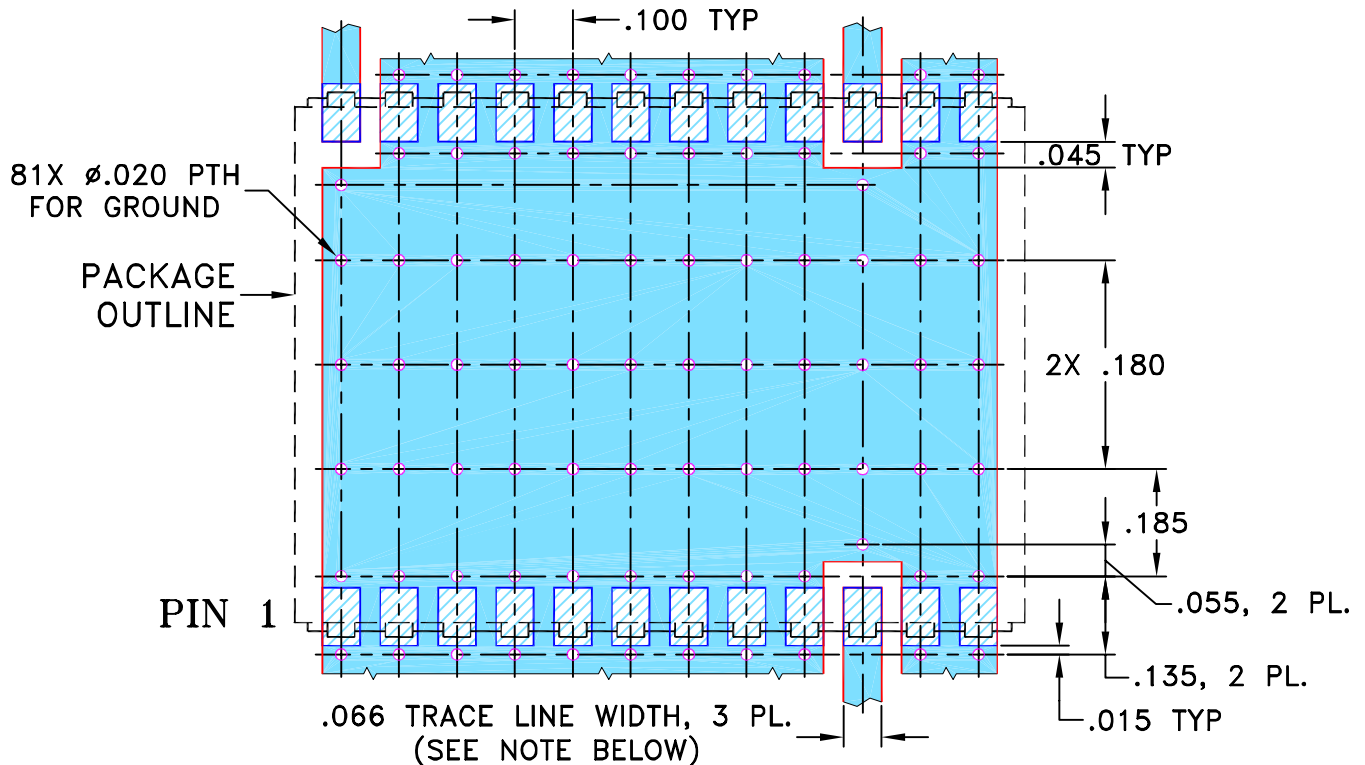
THIRD ANGLE PROJECTION



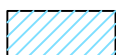
REVISIONS

REV OR	ECN No.	DESCRIPTION	DATE	DR	AUTH
	M100847	NEW RELEASE	02/03/06	MMG	DY

SUGGESTED MOUNTING CONFIGURATION FOR BL301 CASE STYLE, "hr" PIN CONNECTION



- NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS R04350B WITH DIELECTRIC THICKNESS $.030'' \pm .002''$; COPPER: 1/2 OZ. EACH SIDE.
FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

 DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
 DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

DRAWN

MMG

12/21/05

TOLERANCES ON:

CHECKED

AV

01/03/06

2 PL DECIMALS $\pm$
3 PL DECIMALS $\pm .005$

APPROVED

DY

02/03/06

ANGLES $\pm$
FRACTIONS $\pm$
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PL, hr, BL301, JEFT, TB-359

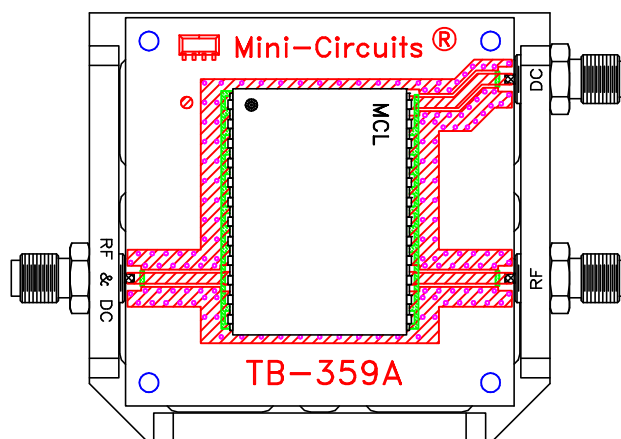
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98-PL-216REV:
OR

FILE: 98PL216

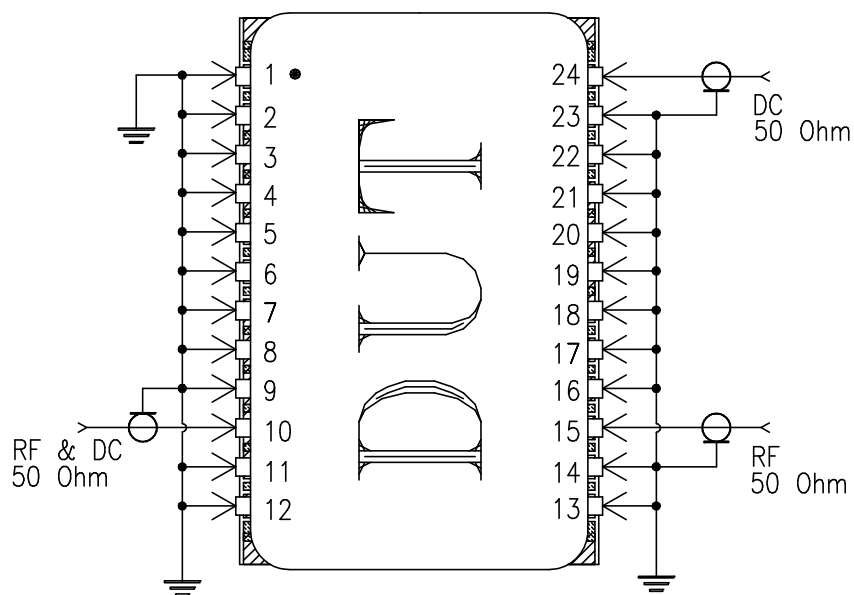
SCALE: 3:1

SHEET: 1 OF 1

Evaluation Board and Circuit



TB-359



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent,
Dielectric Constant=3.5, Thickness=.030 inch.

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All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215