

Surface Mount

Voltage Controlled Oscillator

JTOS-100P+

5V Tuning for PLL ICs 48 to 59 MHz

Features

- low phase noise
- 5V tuning voltage range
- linear tuning characteristics
- aqueous washable

Applications

- PLL circuitry
- measurement instrumentation
- frequency synthesizers



CASE STYLE: BK377

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Electrical Specifications

Frequency (MHz)		Power Output (dBm)	Tuning Voltage (V)		Phase Noise (dBc/Hz) SSB at offset frequencies: Typ.				Pulling pk-pk @ 12 dB (MHz)	Pushing (MHz/V)	Tuning Sensitivity (MHz/V)	Harmonics (dBc)		3 dB Modulation Bandwidth (MHz)	Power Supply	
Min.	Max.	Typ.	Min.	Max.	1 kHz	10 kHz	100 kHz	1 MHz	Typ.	Typ.	Typ.	Typ.	Max.	Typ.	Voltage (V)	Current (mA) Max.
48	59	+9	0.5	5.0	-83	-108	-128	-140	0.6	0.2	3.5-4	-30	-20	0.1	12	18

Pin Connections

RF OUT	13
VCC	2
V-TUNE	5
GROUND	1,3,4,6,7,8,9,10,11,12,14

Maximum Ratings

Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Max. Supply Voltage (Vcc)	16V
Absolute Max. Tuning Voltage (Vtune)	7V
All specifications	50 ohm system

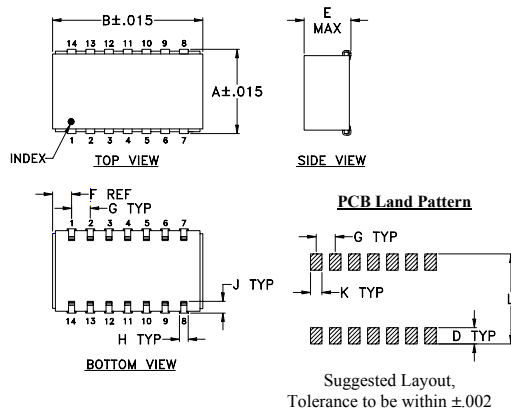
Permanent damage may occur if any of these limits are exceeded.

Tape & Reel: F107

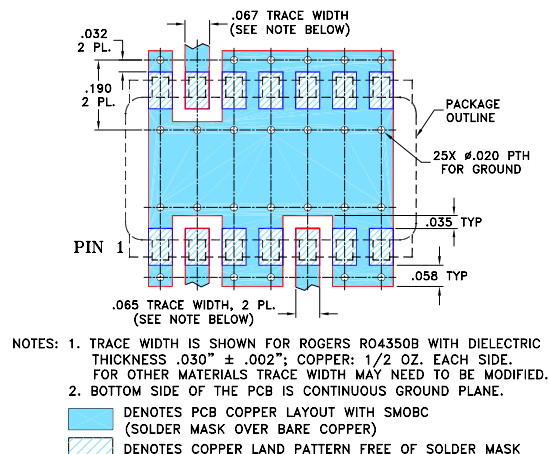
7" Reels with 10, 20, 50, 100 devices
13" Reels with 200 devices

Environmental Ratings: ENV65

Outline Drawing



Demo Board MCL P/N: TB-04 Suggested PCB Layout (PL-005)



Outline Dimensions (inch)

A	B	C	D	E	F	G	H	J	K	L	wt
.505	.800	--	.100	.250	.100	.100	.047	.065	.065	.525	grams
12.83	20.32	--	2.54	6.35	2.54	2.54	1.19	1.65	1.65	13.34	3.0

Notes

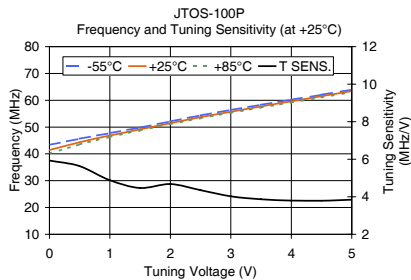
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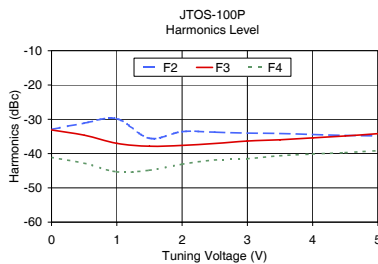
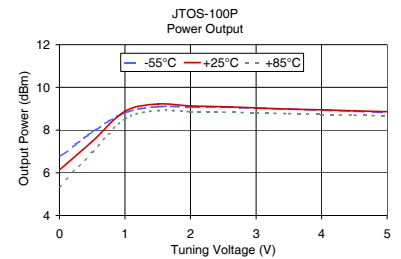
NON-CATALOG

Performance Data & Curves

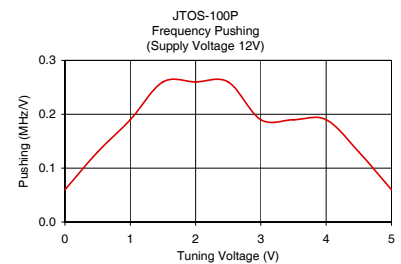
JTOS-100P+



V TUNE	TUNING SENS. (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)		
		-55°C	+25°C	+85°C	-55°C	+25°C	+85°C
0.00	5.93	43.34	41.46	40.21	6.74	6.13	5.35
0.50	5.63	45.64	44.35	43.52	7.91	7.47	6.96
1.00	4.88	47.75	46.87	46.32	8.81	8.88	8.52
1.50	4.47	49.80	49.15	48.75	9.09	9.22	8.91
2.00	4.68	52.13	51.45	51.10	9.08	9.13	8.86
2.50	4.35	54.35	53.69	53.32	9.08	9.09	8.84
3.00	4.02	56.37	55.73	55.34	9.02	9.04	8.80
3.50	3.87	58.30	57.68	57.28	8.98	8.98	8.77
4.00	3.80	60.19	59.59	59.18	8.93	8.95	8.73
4.50	3.79	62.07	61.49	61.06	8.88	8.90	8.70
5.00	3.84	63.98	63.41	62.97	8.82	8.86	8.66



V TUNE	HARMONICS (dBc)			FREQ. PUSHING (MHz/V)
	F2	F3	F4	
0.00	-32.96	-33.12	-41.12	0.06
0.50	-31.13	-34.63	-42.80	0.13
1.00	-29.85	-37.02	-45.35	0.19
1.50	-35.52	-37.86	-44.86	0.26
2.00	-33.60	-37.60	-43.10	0.26
2.50	-33.73	-37.06	-41.89	0.26
3.00	-34.02	-36.35	-41.52	0.19
3.50	-34.13	-35.96	-40.63	0.19
4.00	-34.43	-35.43	-40.10	0.19
4.50	-34.71	-34.88	-39.71	0.13
5.00	-34.84	-34.17	-39.17	0.06



Notes

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JTOS-100P+

Typical Performance Data

V TUNE	TUNE SENS (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)			HARMONICS (dBc)			FREQ. PUSH (MHz/V)	FREQ OFFSET (KHz)	PHASE NOISE (dBc/Hz)
		-55°C	+25°C	+85°C	-55°C	+25°C	+85°C	F2	F3	F4			
0.5	5.63	45.6	44.4	43.5	7.91	7.47	6.96	-31.1	-34.6	-42.8	0.02	1	-83
1.0	4.88	47.8	46.9	46.3	8.81	8.88	8.52	-29.9	-37.0	-45.4		10	-108
1.5	4.47	49.8	49.2	48.8	9.09	9.22	8.91	-35.5	-37.9	-44.9		100	-128
2.0	4.68	52.1	51.5	51.1	9.08	9.13	8.86	-33.6	-37.6	-43.1		1000	-140
2.5	4.35	54.4	53.7	53.3	9.08	9.09	8.84	-33.7	-37.1	-41.9	0.28		
3.0	4.02	56.4	55.7	55.3	9.02	9.04	8.80	-34.0	-36.4	-41.5			
3.5	3.87	58.3	57.7	57.3	8.98	8.98	8.77	-34.1	-36.0	-40.6			
4.0	3.80	60.2	59.6	59.2	8.93	8.95	8.73	-34.4	-35.4	-40.1	0.15		
4.5	3.79	62.1	61.5	61.1	8.88	8.90	8.70	-34.7	-34.9	-39.7			
5.0	3.84	64.0	63.4	63.0	8.82	8.86	8.66	-34.8	-34.2	-39.2	0.13		

REV. X1
JTOS-100P+
070131
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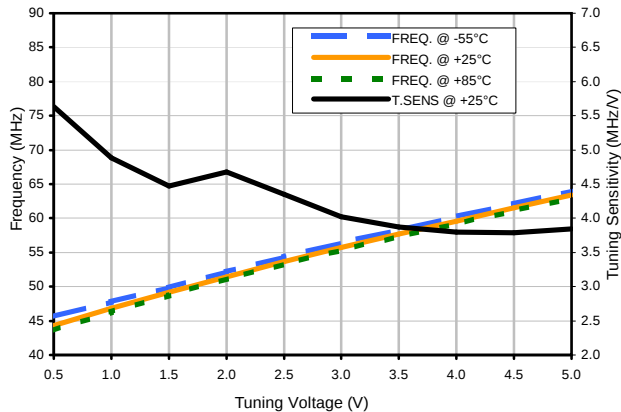


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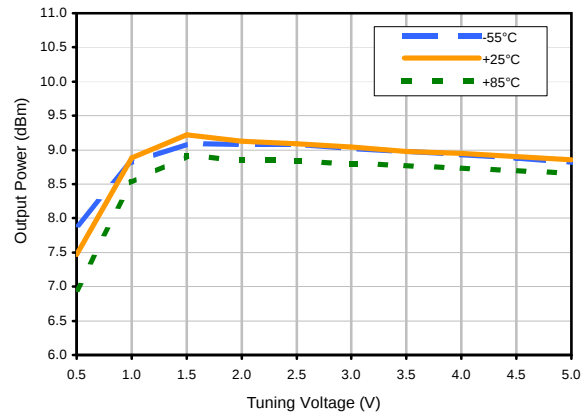
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Typical Performance Data

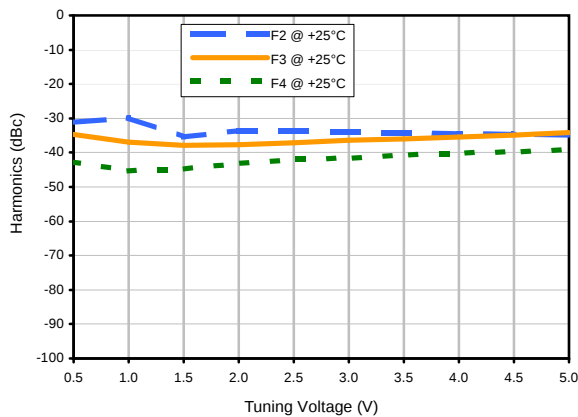
Frequency and Tuning Sensitivity



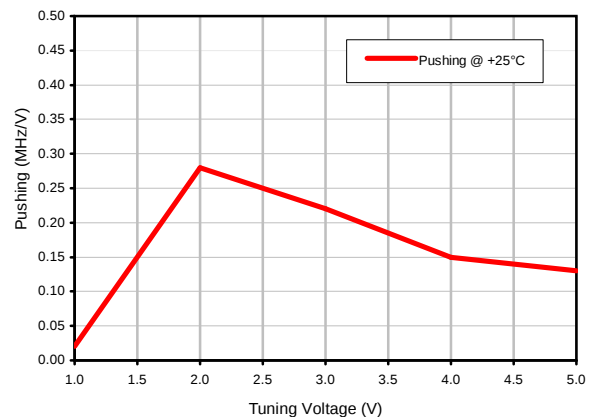
Power Output



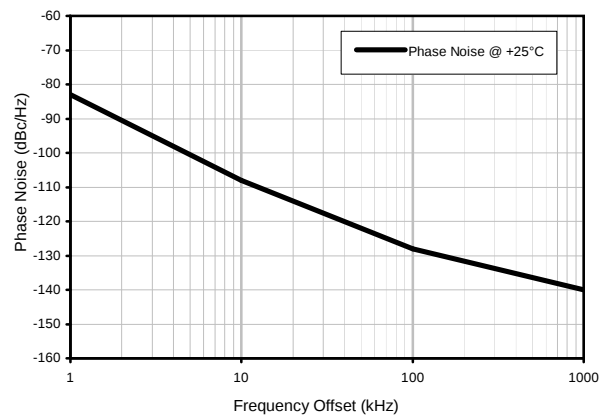
Harmonics Level



Frequency Pushing



Phase Noise



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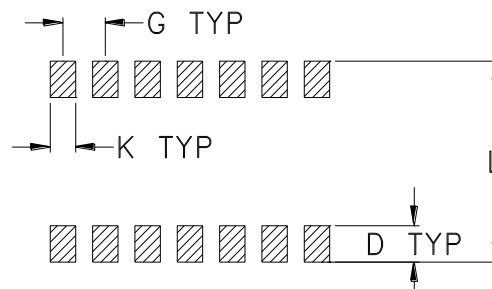
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Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	L	WT. GRAM
BK377	.505 (12.83)	.800 (20.32)	-- --	.100 (2.54)	.250 (6.35)	.100 (2.54)	.100 (2.54)	.047 (1.19)	.065 (1.65)	.065 (1.65)	.525 (13.34)	2.0 MAX.

Dimensions are in inches (mm). Tolerances: 2Pl. $\pm .01$; 3Pl. $\pm .005$

Notes:

- Case material: Copper Nickel alloy.
- Base material: Printed wiring laminate.
- Termination finish:
 - For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
 - For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



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Mini-Circuits ISO 9001 & ISO 14001 Certified

Tape & Reel Packaging TR-F107

DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
32	20	7	Small quantity standards (see note)	10
				20
				50
				100
		13	Standard	200

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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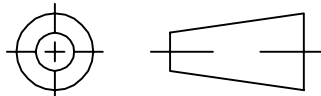
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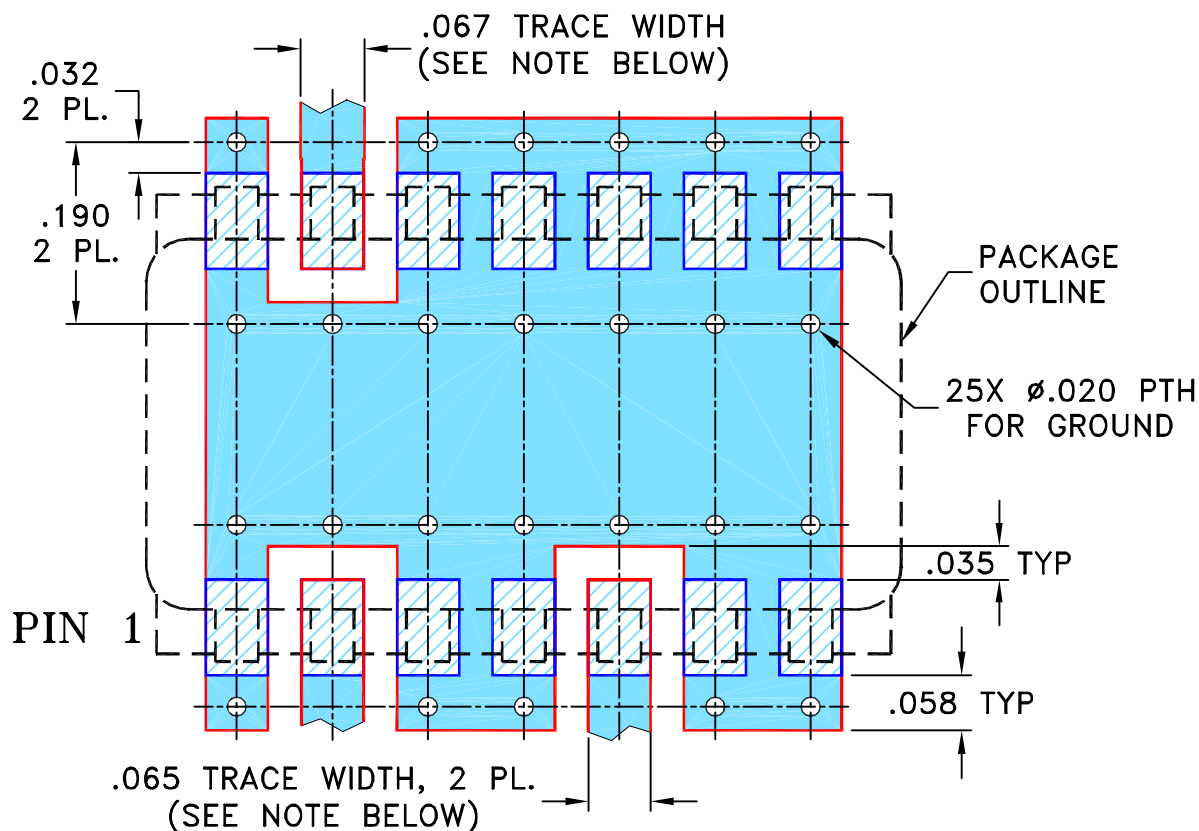
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
B	M76077	UPDATED DRAWING	04/01	GF	MM
C	M82575	UPDATED DRAWING	08/08/02	IL	MM
D	M102713	UPDATED DIMENSIONS & NOTES	01/17/06	MMG	IL
E	M115059	CORRECTED NOTE 2	12/18/07	MMG	IL

SUGGESTED MOUNTING CONFIGURATION FOR BK377 CASE STYLE, "jc" PIN CONNECTION



- NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .030" $\pm$.002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC
(SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005ANGLES $\pm$ FRACTIONS $\pm$

DRAWN

FB

05/20/00

CHECKED

MM

05/24/00

APPROVED

DB

05/24/00



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Brooklyn NY 11235

PL, jc, BK377, JTOS, TB-04

SIZE

A

CODE IDENT

15542

DRAWING NO:

98-PL-005

REV:

E

FILE:

98PL005

SCALE:

5:1

SHEET:

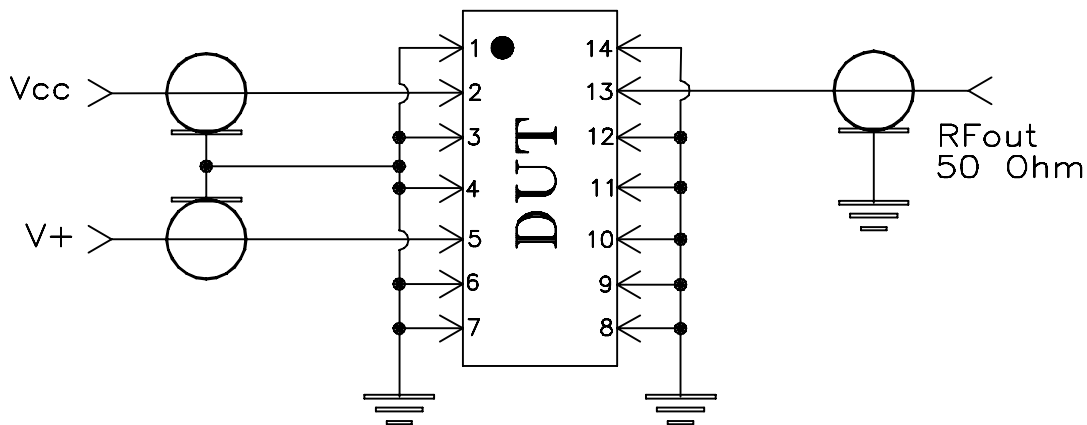
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ASHEETA1.DWG REV:A DATE:01/12/95

Evaluation Board and Circuit



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.

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Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215