

Power Splitter/Combiner

QCV-ED13423C/2

2 Way-90°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.



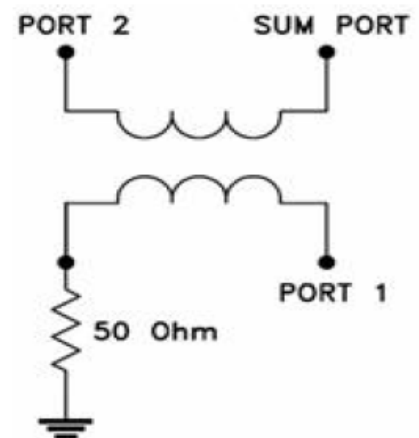
CASE STYLE : JV1210C-1

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter	Min.	Typ.	Max.	Units
Frequency	130		210	MHz
Isolation		21		dB
Insertion Loss Average of Coupled Outputs above 3.0 dB		0.50		dB
Phase Unbalance		2.00		deg.
Amplitude Unbalance		0.45		dB
VSWR	SUM Port	1.20		(:1)
	OUT Ports	1.23		(:1)

MAXIMUM RATINGS	
Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1 (0°)	4
PORT 2 (90°)	6
50 TERM. EXTERNAL	3
GND	2,5

Functional Diagram



2 Way-90° Power Splitter/Combiner

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Typical Performance Data

FREQ.	TOTAL LOSS ¹		AMP. UNBAL.	ISOLATION	PHASE UNBAL.	FREQ.	VSWR		
(MHz)	(dB)		(dB)	(dB)	(Deg.)	(MHz)	(:1)		
	S-1	S-2		1-2			S	1	2
100.0	2.33	4.72	2.39	25.00	87.50	100.0	1.10	1.10	1.13
105.0	2.46	4.54	2.08	24.85	87.45	105.0	1.10	1.11	1.14
110.0	2.57	4.37	1.79	24.70	87.43	110.0	1.10	1.11	1.14
115.0	2.68	4.22	1.53	24.58	87.40	115.0	1.10	1.11	1.15
120.0	2.79	4.08	1.30	24.45	87.39	120.0	1.10	1.11	1.15
125.0	2.89	3.97	1.08	24.30	87.38	125.0	1.11	1.12	1.16
130.0	2.98	3.87	0.89	24.13	87.37	130.0	1.11	1.12	1.16
135.0	3.06	3.78	0.72	23.95	87.36	135.0	1.11	1.12	1.17
140.0	3.14	3.71	0.57	23.72	87.35	140.0	1.12	1.13	1.17
145.0	3.21	3.65	0.44	23.46	87.36	145.0	1.12	1.13	1.18
150.0	3.27	3.60	0.34	23.20	87.40	150.0	1.13	1.14	1.19
155.0	3.32	3.57	0.25	22.91	87.45	155.0	1.14	1.14	1.20
160.0	3.37	3.55	0.18	22.63	87.47	160.0	1.15	1.15	1.21
165.0	3.40	3.55	0.14	22.31	87.52	165.0	1.16	1.16	1.23
170.0	3.43	3.56	0.13	21.96	87.60	170.0	1.17	1.16	1.24
175.0	3.44	3.58	0.14	21.56	87.72	175.0	1.19	1.17	1.26
180.0	3.45	3.62	0.17	21.12	87.87	180.0	1.21	1.19	1.28
185.0	3.44	3.68	0.24	20.63	88.06	185.0	1.23	1.20	1.30
190.0	3.43	3.76	0.33	20.10	88.32	190.0	1.26	1.22	1.33
195.0	3.41	3.86	0.45	19.52	88.64	195.0	1.28	1.24	1.36
200.0	3.38	3.99	0.61	18.90	89.04	200.0	1.32	1.26	1.40
205.0	3.35	4.15	0.80	18.25	89.56	205.0	1.35	1.29	1.44
210.0	3.31	4.34	1.04	17.57	90.21	210.0	1.40	1.32	1.48
215.0	3.26	4.58	1.32	16.87	91.04	215.0	1.45	1.36	1.54
220.0	3.21	4.87	1.65	16.14	92.12	220.0	1.50	1.40	1.60

¹Total Loss = Insertion Loss + 3dB Splitter Loss

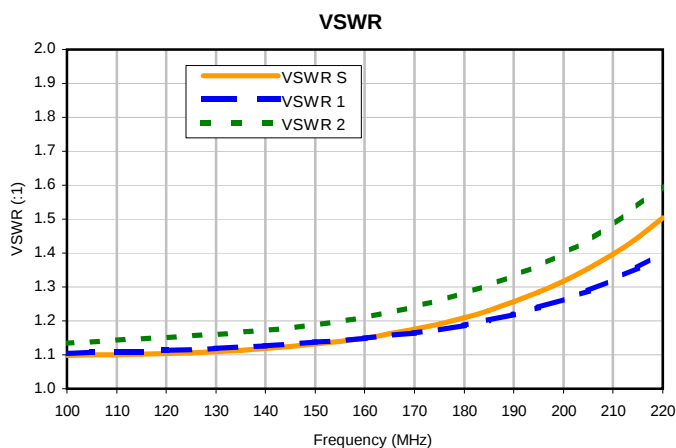
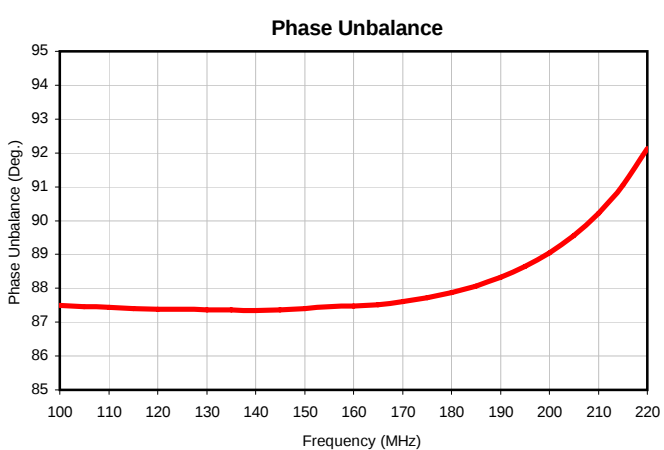
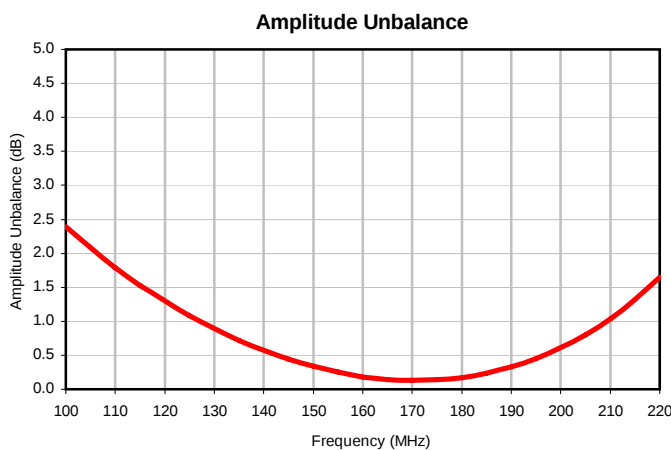
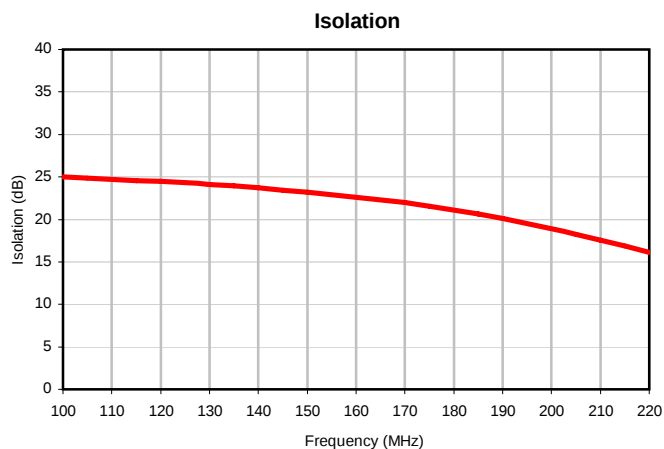
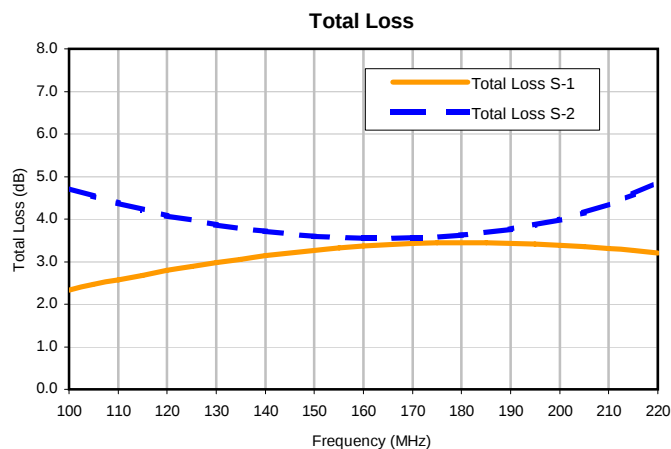


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IF/RF MICROWAVE COMPONENTS

REV. X1
 QCV-ED13423C/2
 2/7/2011
 Page 1 of 1

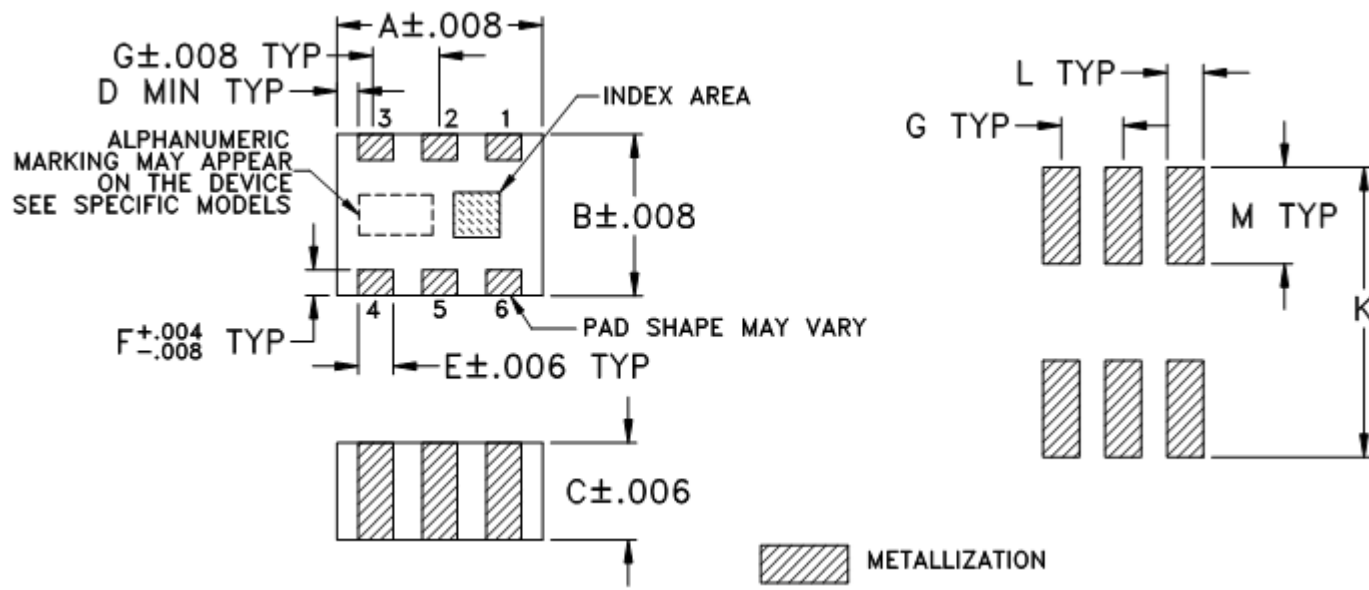
Typical Performance Curves



Outline Dimensions

JV1210C-1

PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm.002$

CASE #	A	B	C	D	E	F	G	H	J	K	L	M	WT. GRAM
JV1210C-1	.126 (3.2)	.098 (2.5)	.059 (1.50)	.004 (.1)	.022 (.56)	.016 (.4)	.039 (1.0)	- -	- -	.177 (4.5)	.024 (.6)	.059 (1.5)	.03

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01$; 3 Pl. $\pm .005$

Notes:

- Open style, ceramic base.
- Termination finish: **as shown below or indicated on Data Sheet.**
For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.
- Pad tolerance is non-cumulative. Minimum spacing between each pad is .004.



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RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F74

DEVICE ORIENTATION IN T&R

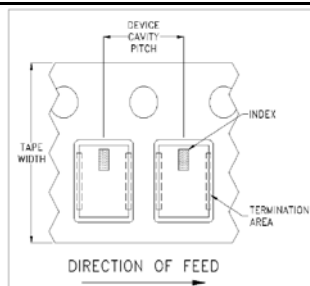


ILLUSTRATION 1

Applicable Case Styles

GE0805C-1
GE0805C-1AP
JV1210C-1
GU2939

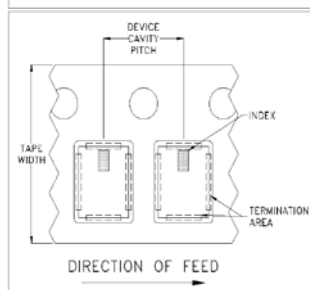


ILLUSTRATION 2

Applicable Case Styles

JV1210C
JV1210C-2
JV1210C-3
JV1210C-4
JV1210C-5
JV1210C-6
JV1210C-11

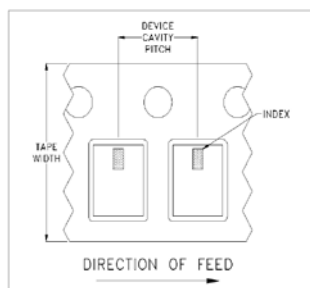


ILLUSTRATION 3

Applicable Case Styles

JC0603C-8
JV1210C-7
JV1210C-8
JV1210C-9
JV1210C-10
JV1210C-13
GE0805C-13

Tape Width, mm	Device Cavity Pitch, mm	Real Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	2000
				4000

Note: Small reel availability varies by model. Refer to pricing and availability on individual model dashboard.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



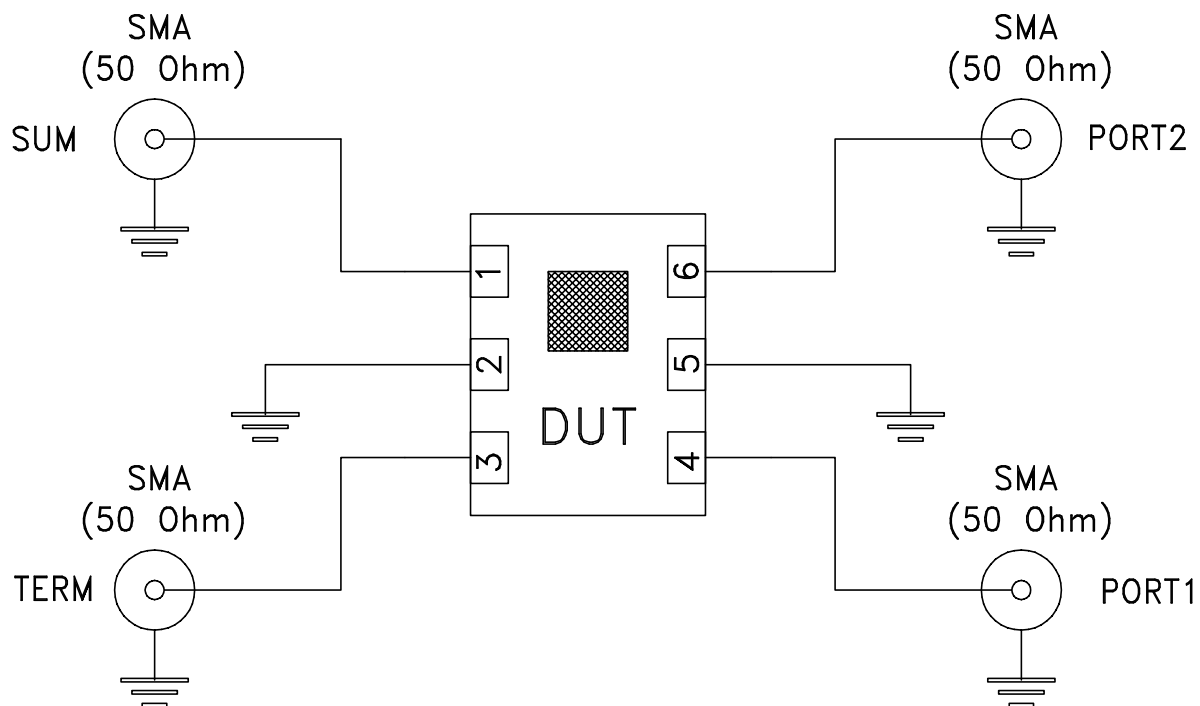
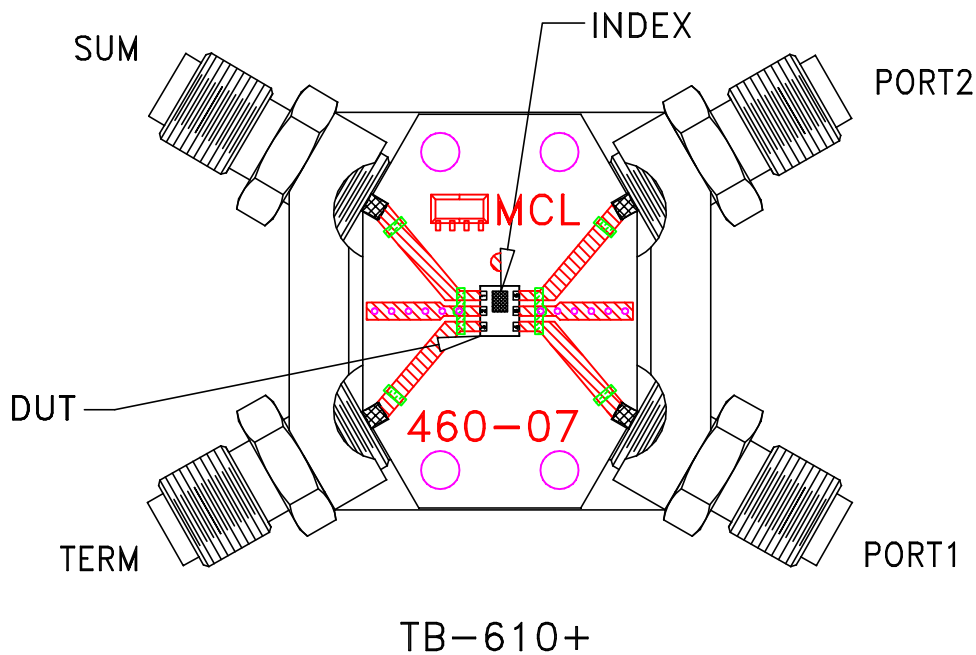
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Evaluation Board and Circuit



Schematic Diagram

NOTES:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent, Dielectric Constant=3.5, Thickness=.020 inch.

 **Mini-Circuits®**



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A