Wideband, Microwave **Monolithic Amplifier**

AVA-183+

6 to 18 GHz 50Ω



CASE STYLE: DQ849

The Big Deal

- Surface Mount Amplifier up to 18 GHz
- Integrated matching, DC Blocks and bias circuits
- High Reverse Isolation

Product Overview

The Mini-Circuits AVA-183+ is a surface mount, microwave amplifier fabricated using InGaAs PHEMT technology and fully integrated gain block up to 18 GHz. It is packaged in Mini-Circuits' industry standard 3x3 mm MCLP (QFN) package, which provides excellent RF and thermal performance. The AVA-183+ integrates the entire matching network with the majority of the bias circuit inside the package, reducing the need for complicated external circuits. This approach makes the AVA-183+ extremely flexible and enables simple, straightforward use.

NON-CATALOG

Key	Features

Feature	Advantages
Wideband, 6 to 18 GHz	Broad frequency range supports a wide array of applications from microwave radio and radar , to military communications and countermeasures.
Excellent Gain Flatness	Typical ±0.6 dB gain flatness across the entire frequency range minimizes the need for external equalizer networks making it a great fit for instrumentation and EW applications.
High Isolation	With reverse isolation of 38 dB (25 dB directivity), the AVA-183+ is an excellent choice for buffering broadband circuits. It is an ideal LO driver amplifier and provides designers system flexibility and margin when integrating cascaded RF components.
Manufacturability	MSL1 and ESD Class1A (HBM) ratings minimize special handling on production lines.

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Wideband, Microwave **Monolithic Amplifier**

6-18 GHz

Product Features

- Gain, 12.3 dB typ. & Flatness, ±0.6 dB
- Output Power, up to +18.4 dBm typ.
- Excellent isolation, 38 dB typ.
- Single Positive Supply Voltage, 5V
- Integrated DC blocks, Bias-Tee & Microwave bypass capacitor
- Unconditionally Stable
- Aqueous washable; 3mm x 3mm SMT package

Typical Applications

- Military EW and Radar
- DBS
- Wideband Isolation amplifier
- Microwave point-to-point radios
- Satellite systems



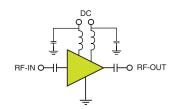
CASE STYLE: DQ849 PRICE: Contact Sales Dept.

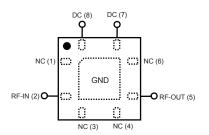
+RoHS Compliant The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

General Description

AVA-183+ is a wideband medium power monolithic amplifier fabricated using InGaAs PHEMT technology with outstanding gain flatness up to 18 GHz. It is manufactured using PHEMT* technology and is unconditionally stable. Its outstanding isolation enables it to be used as a wideband isolation amplifier or buffer amplifier in a variety of microwave systems.

simplified schematic and pad description





Pad Number	Description (See Application Circuit, Fig. 2)
2	RF input pad
5	RF output pad
8(V _{D1}), 7(V _{D2})	DC power supply
paddle in center of bottom	Connected to ground
1,3,4,6	No internal connection; recommended use: per PCB Layout PL-328
	Number 2 5 8(V _{D1}), 7(V _{D2}) paddle in center of bottom

*Pseudomorphic High Electron Mobility Transistor.

Note

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REV. A M132481 AVA-183+ ED-13817 MCL NY 160915 Page 2 of 5

Wideband Monolithic PHEMT MMIC Amplifier



Parameter	Condition (GHz)	Min.	Тур.	Max.	Units
Frequency Range		6.0		18.0	GHz
DC Voltage (V _{D1} , V _{D2})			5.0		V
DC Current (I _{D1} +I _{D2})			134	145	mA
	6.0		12.5		
	8.0	10.0	12.7		
	10.0	10.0	12.4		
Gain	12.0		12.0		dB
	14.0		11.7		
	16.0		12.6		
	18.0	10.0	12.6		
	6.0		15.6		1
	8.0		25.7		
	10.0		17.9		
Input Return Loss	12.0		12.4		dB
nput notum 2000	14.0		11.3		
	16.0		15.8		
	18.0		11.7		
	6.0		21.0		
	8.0		25.0		
	10.0		18.6		
Output Return Loss	12.0		14.3		dB
	14.0		13.6		
	16.0		20.3		
	18.0		19.0		ļ
	6.0		27.1		
	8.0		26.3		
Output IP3	$ON - C_{4.0}^{10.0}$	AL	26.5 26.3 26.0 25.4	7	dBm
	18.0		24.5		
	6.0		18.9		1
	8.0		18.7		
	10.0	16.0	18.6		
Output Power @ 1 dB compression	12.0		18.7		dBm
	14.0		18.5		
	16.0		18.0		
	18.0		17.4		
	6.0		7.0		
	8.0		6.3		
	10.0		5.6		
Noise Figure	12.0		6.5		dB
-	14.0		6.5		
	16.0		6.1		
	18.0		6.1		
Directivity (Isolation-Gain)			25.0		dB
DC Current Variation vs. Temperature (2)			-0.087		mA/°C
Thermal Resistance		1	47		°C/W

Electrical Specifications⁽¹⁾ at 25°C, Zo=50Ω, (refer to characterization circuit, Fig. 1)

Absolute Maximum Ratings⁽³⁾

Parameter	Ratings
Operating Temperature (4)	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Channel Temperature	160°C
DC Voltage (Pad 7,8)	5.5V
Voltage (Pads 2, 5)	10V
Power Dissipation	860 mW
DC Current (Pad 7+8)	160mA
Input Power	20 dBm

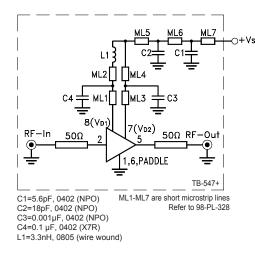
(1) Measured on Mini-Circuits Characterization test fixture TB-547+ See Characterization Test Circuit (Fig. 1) ⁽²⁾ (Current at 85°C - Current at -45°C)/130 ⁽³⁾ Permanent damage may occur if any of these limits are exceeded.

These maximum ratings are not intended for continuous normal operation. ⁽⁴⁾ Defined with reference to ground pad temperature.

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Characterization Test Circuit



Recommended Application Circuit

(refer to evaluation board for PCB Layout and component values)

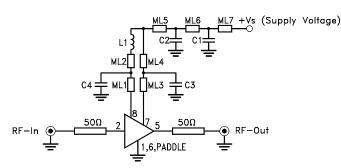


Fig 2. Recommended Application Circuit

Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization Test Board TB-547+) Gain, Output power at 1dB compression (P1dB), Noise Figure, Output IP3 (OIP3) are measured using Agilent's N5242A PNA-X microwave network analyzer.

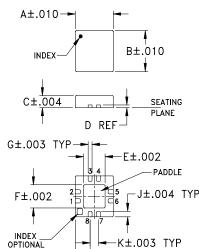
Conditions:

1. Gain: Pin=-25 dBm

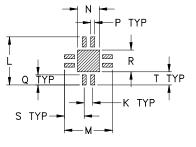
2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, -10 dBm/tone at output.

3. Vs adjusted for 5V at device (V $_{\rm D1}$ and V $_{\rm D2}$), compensating loss of bias lines.

Outline Drawing



PCB Land Pattern



Suggested Layout, Tolerance to be within $\pm .002$

Outline Dimensions (inch)

Α	В	С	D	E	F	G	н	J	
.118	.118	.035	.008	.067	.067	.012	.046	.016	
3.00	3.00	0.89	0.20	1.70	1.70	0.30	1.17	0.41	
K	L	Μ	N	Р	Q	R	S	Т	wt
	-			-	-		-	T .041 g	
.026	.148	.148	.067	.012	.031	.067	.061		grams

Notes

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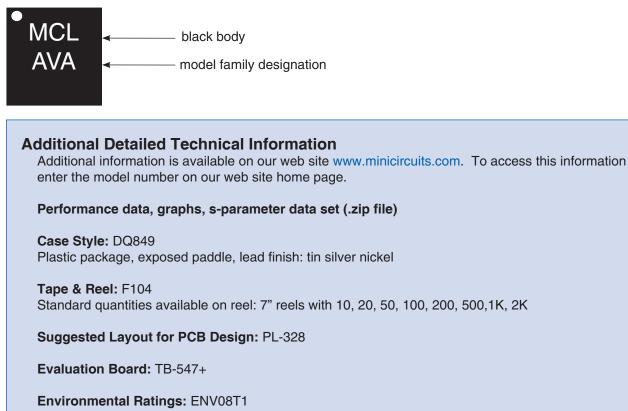
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Product Marking



ESD Rating

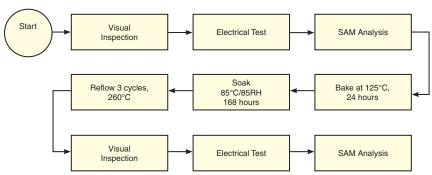
Human Body Model (HBM): 1A (250 to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): M1 (<100V) in accordance with ANSI/ESD STM5.2-1999; passes 25V

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

MSL Test Flow Chart



- Note

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