Wideband, Microwave, 0.5W Monolithic Amplifier

AVM-273HP+

50 Ω 13 to 26.5 GHz

The Big Deal

- Wideband 13 to 26.5 GHz
- Output power up to +27dBm
- Excellent directivity, 43 dB typ. @ 20 GHz
- Unconditionally stable
- Excellent gain flatness, ±1 dB
- Sequencing and DC Control module (VCM-1+) is available

Product Overview

Mini-Circuits AVM-273HP+ is a three stage balanced, wideband monolithic amplifier delivering up to 0.5W power and operating over 13 to 26.5 GHz. It is designed using PHEMT technology and is unconditionally stable. Its outstanding isolation enables it to be used as a wideband isolation amplifier or buffer amplifier in a variety of microwave systems including point to point radios, military EW and radar, DBS, and VSAT.

Key Features

Feature	Advantages
Wideband	Wide frequency coverage up to 26.5 GHz supports many microwave applications.
Pout up to +27 dBm	Can be used as a low-cost driver for high power amplifiers.
Excellent active directivity, 43 dB @ 20 GHz (directivity = isolation - gain)	Can be used as an inter-stage isolation amplifier, minimizing interaction of adjacent components.
Unconditionally stable	Eliminates the need for any compensating network to prevent unintended oscillation.
Small package	Small size for high power with low inductance, repeatable transitions, and excellent ther- mal contact to PCB.





CASE STYLE: DG1677-1

Wideband, Microwave, 0.5W Monolithic Amplifier

13-26.5 GHz

Product Features

- Gain, 13 dB typ.
- Output Power, up to +27 dBm typ.
- Excellent directivity, 43 dB typ. at 20 GHz
- Unconditionally Stable
- Aqueous washable; 5 mm x 5 mm SMT package

Typical Applications

- Point to Point Radio
- Military EW and Radar
- DBS
- VSAT
- Wideband Isolation amplifier



CASE STYLE: DG1677-1

AVM-273HP+

+RoHS Compliant The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

General Description

The AVM-273HP+ is a three stage balanced wideband monolithic amplifier delivering up to 0.5W power operating over 13 to 26.5 GHz. It is manufactured using PHEMT technology and is unconditionally stable. Its outstanding isolation enables it to be used as a wideband isolation amplifier or buffer amplifier in a variety of microwave systems.

Simplified Schematic VG1A VD3A VD1A VG2A VD2A VG3A RF-IN М М М н Н RF-OUT М VD1B VG1B VG2B VD2B VG3B VD3B

Notes: H - 90° Hybrid M - Matching Network



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Wideband Monolithic Amplifier



Electrical Specifications ⁽¹⁾ at 25°C, Zo=50Ω, (refer to characterization circuit, Fig.1)

Parameter	Condition (GHz)	Min.	Тур.	Max.	Units.
Frequency Range		13.0		26.5	GHz
DC Voltage (VD1 to VD3 (A)&(B))			5.0		V
DC Voltage (VG1 to VG3 (A)&(B))			-0.8		V
DC Current (ID1+ID2+ID3; (A)&(B))			559	590	mA
DC Current (IG1+IG2+IG3; (A)&(B))			0.5		mA
	13.0	_	12.8		
	14.0	_	12.8		
	15.0	_	13.0		
	16.0	_	13.6		
Gain	17.0	_	14.2		dB
Jan	18.0	12.0	15.1		UD UD
	20.0		14.7		
	24.0	_	13.8		
	26.5	_	13.2		
			16.9		
	13.0				
	14.0		17.1		
	15.0		17.6		
	16.0		17.0		
nput Return Loss	17.0		19.4		dB
	18.0		15.5		
	20.0		8.9		
	24.0		9.0		
	26.5		7.2		
	13.0		8.1		
	14.0		12.7		
	15.0		18.9		
	16.0		19.3		
Dutput Return Loss	17.0		16.6		dB
	18.0		13.2		
	20.0		8.0		
	24.0		10.6		
	26.5		8.8		
Directivity (Isolation- Gain)	20.0		43		dB
	13.0		22.6		
	14.0		24.3		
	15.0		25.6		
	16.0		26.3		
Dutput Power @ 1 dB compression	17.0		26.4		dBm
	18.0		26.9		UDIII
	20.0		26.6		
	24.0		26.5		
	26.5		25.7		
	13.0		28.7		
	14.0		30.6		
	15.0		32.2		
	16.0		32.4		
DIP3	17.0		33.2		dBm
	18.0		32.1		
	20.0		31.0		
	24.0		29.7		
	26.4		29.5		
	13.0		9.8		
	14.0		9.6		
	15.0		9.3		
	16.0		8.9		
loise Figure	17.0		8.8		dB
NOISE I IYUIE	18.0		8.4		
	20.0		8.5		
	24.0		7.5		
	26.5		8.5		
OC Current Variation vs. Temperature (2)			0.32		mA/°C
OC Current Variation vs. Voltage			0.145		mA/mV
Thermal Resistance			16.3	1	°C/W



Absolute Maximum Ratings⁽³⁾

Operating Temperature ⁽⁴⁾	-40°C to 85°C
Storage Temperature	-65°C to 150°C
Channel Temperature	136°C
DC Voltage-Drain (Pads 11,13,15,26,28,30) @ VG=-0.8V (5)	+6 V
DC Voltage-Gate (Pads 10,12,14,27,29,31) @ VD=+5V (6)	-6 V
Power Dissipation	3.1 W
DC Current (Pads 11,13,15,26,28,30)	620 mA
Input Power (CW)	16 dBm
DC Voltage on pins 19 & 20 (@VD=5V & VG=-0.8V)	+10V

Notes:

1. Measured on Mini-Circuits Test Board TB-715-5V. See Characterization Test Circuit (Fig. 1), See cautionary note 1 2. (Current at 85°C - Current at -45°C)/130

- 3. Permanent damage may occur if any of these limits are exceeded. These maximum ratings are not intended for continuous normal operation.
- Defined with reference to ground pad temperature.
 VG=VG1, VG2, VG3 (A&B)
 VD= VD1, VD2, VD3 (A&B)

Bias Sequence and Conditions

NOTE: to prevent damage to the AVM-273HP+, and to ensure proper operation, all bias voltages must be applied in the following sequence for both Power On and for Power Off:

Power ON

- Connect all Vg wires together (to ensure all stages are biased simultaneously).
 Connect all Vd wires together (to ensure all stages are biased simultaneously).
- 3. Set Vg to -2V, apply Vg 4. Set Vd to +5V, apply Vd
- 5. Adjust Vg until the desired I_{dd} is reached (the typical Vg is around -0.8V). 6. Apply RF signal.

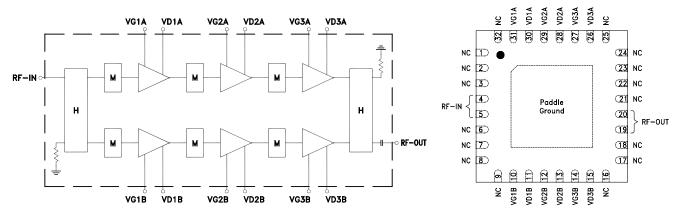
Power OFF

- 1. Turn OFF RF signal 2. Set Vg to -2.0V
- 3. Turn OFF Vd (Vd to 0V)
- 4. Turn OFF Vg (Vg to 0V)

ALTERNATELY - VCM-1+ module provide voltage sequencing and DC control automatically.

Following these requirements are required for proper operation and are a condition of Mini-Circuits Warranty.

Simplified Schematic and Pad Description



Notes:

H - 90° Hybrid

M - Matching Network

Function	Pad Number	Description (See Application Circuit)	Function	Pad Number	Description (See Application Circuit)
RF-In	4,5	RF Input	VG1 B	10	Gate Voltage of first stage amplifier (Bottom)
RF-Out	19,20	RF-Output (DC blocked)	VG2 B	12	Gate Voltage of second stage amplifier (Bottom)
VG1 A	31	Gate Voltage of first stage amplifier (Top)	VG3 B	14	Gate Voltage of third stage amplifier (Bottom)
VG2 A	29	Gate Voltage of second stage amplifier (Top)	VD1 B	11	Drain Voltage of first stage amplifier (Bottom)
VG3 A	27	Gate Voltage of third stage amplifier (Top)	VD2 B	13	Drain Voltage of second stage amplifier (Bottom)
VD1 A	30	Drain Voltage of first stage amplifier (Top)	VD3 B	15	Drain Voltage of third stage amplifier (Bottom)
VD2 A	28	Drain Voltage of second stage amplifier (Top)	NC	1-3, 6-9, 16-18,	No Connection, not used internally
VD3 A	26	Drain Voltage of third stage amplifier (Top)		21-25, 32	

Characterization Test Circuit

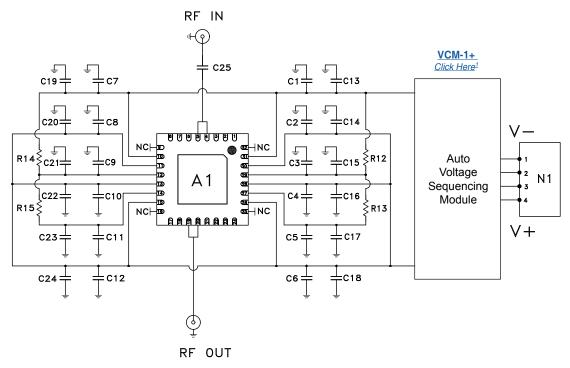


Fig. 1 Schematic of Test Board TBD (Test & Application)

TBD incorporates Current stabilization, automatic voltage sequencing, reverse voltage protection circuitry. Test board used for characterization. (DUT soldered on Mini-Circuits Characterization Test Board TBD) Gain, Output power at 1dB compression (P1dB), Noise Figure, Output IP3 (OIP3) are measured using Agilent's N5242A PNA-X microwave network analyzer. Conditions:

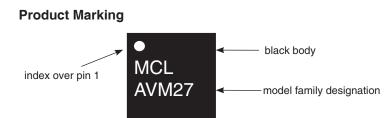
1. Gain: Pin=-25 dBm

2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, 0 dBm/tone at output.

3. V+ set to +6.1V to account for 1.1V drop in bias circuit resulting in +5V at drain (VD1 to VD3 (A&B))

4. V- is set to -5V to realize proper gate voltage (-0.8V typ.)

NOTE 1: The voltage sequencing and DC control circuit is available as a self-contained, surface mount module, see page 7





Additional Detailed Technical Information additional information is available on our dash board. To access this information <u>click here</u>				
	Data Table			
Performance Data	Swept Graphs			
S-Parameter (S2P Files) Data Set (.zip file)				
Case Style	DG1677-1 (SOT 89) Plastic package, exposed paddle, lead finish: tin-silver over nickel			
Tape & Reel	F68			
Standard quantities available on reel	7" reels with 10, 20, 50, 100, 200, 500 or 1K devices			
Suggested Layout for PCB Design	n PL-401			
Evaluation Board	TB-715-5V			

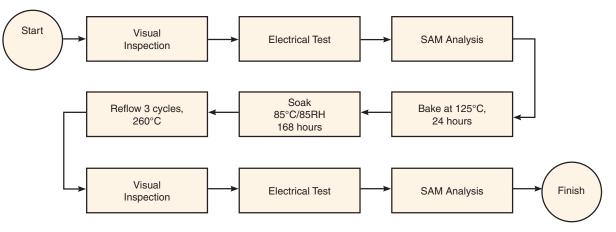
ESD Rating

Human Body Model (HBM): Class 1A in accordance with JESD22-A114F Machine Model (MM): Class A (pass 25V) in accordance JESD22-A115

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

MSL Test Flow Chart



Additional Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp



Surface Mount Sequencing & DC Control Module

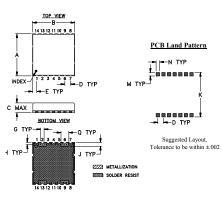
Maximum Ratings

Operating Temperature	-40°C to 85°C			
Operating Temperature	-40°C to 85°C			
Positve Input Voltage	+7.4V			
Negative Input Voltage	-6.0V			
Total DC	620 mA			
Permanent damage may occur if any of these limits are exceeded.				

Pad Description

NEGATIVE OUTPUT	2
POSITIVE OUTPUT	6
POSITIVE INPUT	9
NEGATIVE INPUT	13
GND	1,3,4,5,7,8,10,11,12,14

Outline Drawing



Outline Dimensions (inch)							
А	В	С	D	E	F	G	н
.750	.750	.220	.100	.075		.060	.040
19.05	19.05	5.59	2.54	1.91		1.52	1.02
J	к	L	М	Ν	Р	Q	wt
.070	.791		.063	.061		.140	grams
1.78	20.09		1.60	1.55		3.56	2.05

Features

- Provides correct voltage sequence and
- DC control for the PHEMT amplifier <u>AVM-273HP+</u> • Replaces over 20 discrete components
- Replaces over 20 discrete composition
 Protection from reverse polarity
- · Flotection nonn reverse polarity

Applications

AVM-273HP+ PHEMT Amplifier

VCM-1+

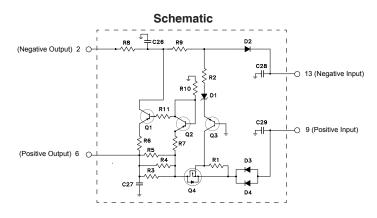


CASE STYLE: BG1482-1 PRICE: Contact Sales Dept.

+RoHS Compliant The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Electrical Specifications at 25°C

Parameter	Min.	Тур.	Max.	Units
Positive Input Voltage	+5.9	+6.1	+6.3	V
Negative Input Voltage	-5.5	-5.0	-4.5	V
Positive Output Voltage	—	+5.0	—	V
Negative Output Voltage	—	-0.8	_	V
Controlled DC from positive line	—	560	_	mA
Sinking DC to the negative line	—	-0.5	_	mA



BOM

SEQ	Manufacturer P/N	Description	Manufacturer
Q1,Q2	MMBT3906	PNP Transistor	Fairchild Semiconductor
Q3	MMBT3904	NPN Transistor	Fairchild Semiconductor
Q4	NTS4101PT1G	P-Channel MOSFET	ON Semiconductor
D1	MMSZ5228BT1G	Zener Diode	ON Semiconductor
D2-D4	MBR0520LT1G	Schottky Diode	ON Semiconductor
C26,C27	GRM1555C1H101GA01D	0402 Capacitor, 100 pF, 2%	Murata Electronics
C28,C29	GRM188R71C105KA12D	0603 Capacitor, 1µF	Murata Electronics
R1,R2,R9,R10	RK73H1JTTD1002F	0603 Resistor, 10kΩ	KOA Speer
R3-R5	RK73H2BTTD3R32F	1206 Resistor, 3.32 Ω	KOA Speer
R6	RK73H1JTTD1000F	0603 Resistor, 100 Ω	KOA Speer
R7	RK73H1JTTD1501F	0603 Resistor, 1.5 kΩ	KOA Speer
R8	RK73H1JTTD1001F	0603 Resistor, 1.0 kΩ	KOA Speer
R11	RK73Z2BTTD	1206 Resistor, 0 Ω	KOA Speer