## Engineering Development Model

## Diplexer Connectorized

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

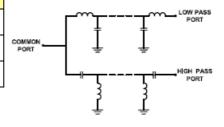
## DPLX-EDU1383+



CASE STYLE : FL905

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C							
Parameter		Port	Frequency (MHz)	Min.	Тур.	Max.	Units
Passband	Insertion Loss	Lowpass	9.8-10.2	-	0.5	1	dB
		Highpass	854-1870	-	0.3	1	dB
	Return Loss	Lowpass	9.8-10.2	12	18	-	dB
		Highpass	854-1870	12	20	-	dB
		Common	9.8-10.2	12	18	-	dB
			854-1870	12	20	-	dB
Stop band isolation		Lowpass	854-1870	40	60	-	dB
		Highpass	9.8-10.2	60	90	-	dB
Crossover isolation		Lowpass to Highpass	10.2-854	48	80	_	dB

MAXIMUM RATINGS				
Operating Temperature	-40°C to 85°C			
Storage Temperature	-55°C to 100°C			
RF Power Input	400mW			



PORT CONNECTIONS				
Highpass port	SMA (F)			
Lowpass port	SMA (F)			
Common port	SMA (F)			

