

Frequency Synthesizer

KSN-2101A-119+

50Ω 1970 to 2080 MHz

The Big Deal

- Low phase noise and spurious
- Robust design and construction
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK801

Product Overview

The KSN-2101A-119+ is a Frequency Synthesizer, designed to operate from 1970 to 2080 MHz for UMTS application. The KSN-2101A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: <ul style="list-style-type: none">• Phase Noise: -91 dBc/Hz typ. @ 10 kHz offset• Comparison Spurious: -68 dBc typ.• Reference Spurious: -93 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-2101A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.15"	The small size enables the KSN-2101A-119+ to be used in compact designs.

Notes

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Frequency Synthesizer

KSN-2101A-119+

50Ω 1970 to 2080 MHz

Features

- Integrated VCO + PLL
- Low phase noise and spurious
- Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+5V)
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK801

+RoHS Compliant
 The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

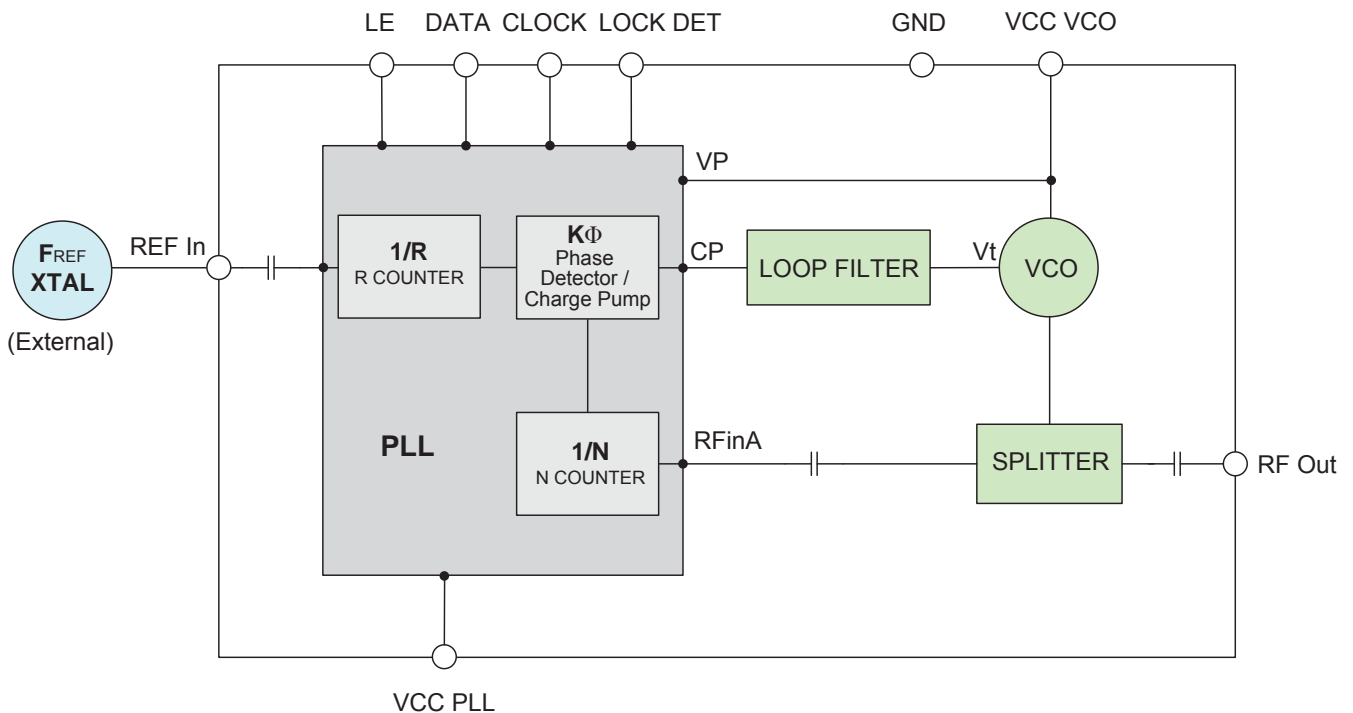
Applications

- UMTS

General Description

The KSN-2101A-119+ is a Frequency Synthesizer, designed to operate from 1970 to 2080 MHz for UMTS application. The KSN-2101A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise. To enhance the robustness of KSN-2101A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

Simplified Schematic



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Electrical Specifications (over operating temperature -35°C to +85°C)

Parameters		Test Conditions	Min.	Typ.	Max.	Units
Frequency Range		-	1970	-	2080	MHz
Step Size		-	-	40	-	kHz
Settling Time		Within ± 1 kHz	-	4	20	mSec
Output Power		-	-1.0	+1.7	+4.0	dBm
SSB Phase Noise		@ 100 Hz offset	-	-73	-	dBc/Hz
		@ 1 kHz offset	-	-68	-61	
		@ 10 kHz offset	-	-91	-85	
		@ 100 kHz offset	-	-122	-117	
		@ 1 MHz offset	-	-143	-138	
Integrated SSB Phase Noise		@ 50Hz - 5MHz	-	-35	-	dBc
Reference Spurious Suppression		Ref. Freq. 11.52 MHz	-	-93	-70	dBc
Comparison Spurious Suppression		Step Size 40 kHz	-	-68	-55	
Non - Harmonic Spurious Suppression		-	-	-90	-	
Harmonic Suppression		-	-	-35	-20	
VCO Supply Voltage		+5.00	+4.75	+5.00	+5.25	V
PLL Supply Voltage		+5.00	+4.75	+5.00	+5.25	
VCO Supply Current		-	-	29	36	mA
PLL Supply Current		-	-	17	23	
Reference Input (External)	Frequency	11.52 (square wave)	-	11.52	-	MHz
	Amplitude	1	-	1	-	V _{P-P}
	Input impedance	-	-	100	-	KΩ
	Phase Noise @ 1 kHz offset	-	-	-145	-	dBc/Hz
RF Output port Impedance		-	-	50	-	Ω
Input Logic Level	Input high voltage	-	2.80	-	-	V
	Input low voltage	-	-	-	0.60	V
Digital Lock Detect	Locked	-	2.70	-	3.50	V
	Unlocked	-	-	-	0.40	V
Frequency Synthesizer PLL		-	ADF4113			
PLL Programming		-	3-wire serial 3.3V CMOS			
Register Map @ 2080 MHz	F_Register	-	(MSB) 100111111000000010010011 (LSB)			
	N_Register	-	(MSB) 001001100101100100000001 (LSB)			
	R_Register	-	(MSB) 000100000000010010000000 (LSB)			

Absolute Maximum Ratings

Parameters	Ratings
VCO Supply Voltage	5.5V
PLL Supply Voltage	6.0V
VCO Supply Voltage to PLL Supply Voltage	N.A.
Reference Frequency Voltage	-0.3Vmin, +3.4Vmax
Data, Clock, LE Levels	-0.3Vmin, +3.4Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded

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Typical Performance Data

FREQUENCY (MHz)	POWER OUTPUT (dBm)			VCO CURRENT (mA)			PLL CURENT (mA)		
	-40°C	+25°C	+85°C	-40°C	+25°C	+85°C	-40°C	+25°C	+85°C
1970	1.17	1.44	1.84	27.86	29.68	31.00	15.19	16.77	18.27
1981	1.21	1.53	1.85	27.81	29.65	31.00	15.21	16.79	18.28
1992	1.26	1.61	1.81	27.76	29.62	31.00	15.22	16.80	18.28
2003	1.33	1.65	1.79	27.74	29.60	30.99	15.23	16.80	18.29
2014	1.39	1.65	1.80	27.70	29.59	30.97	15.24	16.81	18.29
2025	1.45	1.69	1.81	27.68	29.57	30.95	15.24	16.80	18.29
2036	1.44	1.75	1.80	27.67	29.55	30.94	15.24	16.82	18.29
2047	1.41	1.80	1.75	27.65	29.52	30.91	15.25	16.82	18.30
2058	1.43	1.79	1.75	27.63	29.50	30.89	15.26	16.82	18.31
2069	1.52	1.73	1.80	27.60	29.48	30.85	15.26	16.83	18.31
2080	1.66	1.66	1.91	27.56	29.45	30.81	15.26	16.83	18.31

FREQUENCY (MHz)	HARMONICS (dBc)					
	F2			F3		
	-40°C	+25°C	+85°C	-40°C	+25°C	+85°C
1970	-27.89	-32.44	-40.14	-35.74	-35.83	-36.22
1981	-26.94	-32.01	-39.32	-35.00	-35.93	-36.46
1992	-27.19	-32.73	-40.14	-33.54	-35.03	-35.22
2003	-28.44	-34.17	-43.46	-33.93	-33.87	-35.14
2014	-28.53	-35.94	-49.49	-33.08	-33.08	-34.93
2025	-29.53	-37.63	-53.33	-31.73	-33.33	-33.95
2036	-30.51	-38.50	-52.86	-31.26	-32.16	-32.48
2047	-31.69	-39.29	-50.36	-31.22	-32.05	-32.64
2058	-33.83	-40.12	-49.37	-30.15	-31.49	-31.69
2069	-34.83	-41.30	-49.20	-30.11	-30.16	-31.37
2080	-34.98	-43.70	-49.99	-30.42	-29.99	-31.64

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FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @OFFSETS				
	+25°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
1970	-73.50	-69.50	-92.61	-122.97	-143.38
1981	-74.52	-67.40	-92.78	-122.69	-141.61
1992	-76.21	-66.45	-92.86	-122.78	-143.01
2003	-76.05	-67.05	-93.01	-123.20	-144.11
2014	-75.49	-68.49	-92.79	-123.67	-144.43
2025	-75.30	-69.35	-91.40	-123.70	-144.96
2036	-73.37	-69.22	-92.02	-123.56	-144.76
2047	-73.74	-68.66	-91.61	-123.43	-144.56
2058	-74.44	-68.06	-91.56	-123.25	-144.19
2069	-74.14	-67.61	-92.36	-123.05	-143.71
2080	-71.02	-67.30	-92.94	-123.02	-143.61

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @OFFSETS				
	-40°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
1970	-73.68	-70.43	-91.43	-124.05	-144.69
1981	-73.83	-67.92	-92.35	-123.23	-144.24
1992	-74.18	-67.83	-92.25	-122.27	-143.16
2003	-73.67	-68.23	-92.20	-121.87	-142.63
2014	-72.59	-69.19	-92.90	-122.12	-143.25
2025	-73.62	-68.61	-92.34	-122.55	-143.75
2036	-74.22	-67.62	-91.53	-122.77	-144.02
2047	-74.53	-67.04	-91.20	-122.81	-144.01
2058	-73.61	-67.75	-92.08	-122.88	-143.71
2069	-73.47	-66.78	-91.64	-122.46	-143.55
2080	-76.74	-67.39	-92.34	-122.28	-143.41

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @OFFSETS				
	+85°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
1970	-74.84	-68.95	-93.03	-123.60	-144.66
1981	-73.12	-68.14	-92.27	-124.17	-144.77
1992	-74.21	-67.04	-92.07	-124.49	-145.78
2003	-75.41	-67.55	-91.78	-124.09	-145.63
2014	-76.00	-67.70	-91.00	-123.89	-145.77
2025	-73.83	-67.92	-90.48	-123.75	-145.59
2036	-75.18	-68.09	-90.62	-123.63	-144.99
2047	-75.67	-66.91	-91.42	-123.77	-144.72
2058	-74.83	-66.91	-91.62	-123.48	-144.70
2069	-73.14	-66.80	-92.42	-123.37	-144.35
2080	-74.22	-64.15	-93.42	-123.53	-144.00

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COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @Fcarrier 1970MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 2025MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 2080MHz+(n*Fcomparison) (dBc) note 1		
	n	-40°C	+25°C	+85°C	-40°C	+25°C	+85°C	-40°C	+25°C
-5	-107.12	-96.22	-107.08	-97.15	-94.66	-93.32	-96.01	-91.15	-93.20
-4	-103.28	-92.52	-100.03	-95.99	-91.92	-90.54	-94.24	-87.85	-88.93
-3	-98.38	-87.32	-95.22	-90.61	-86.74	-86.22	-89.47	-83.64	-84.82
-2	-82.16	-77.04	-82.68	-84.55	-79.50	-79.40	-84.78	-77.72	-77.86
-1	-77.01	-67.99	-74.40	-73.66	-68.35	-68.33	-73.47	-67.14	-67.02
0 ^{note 2}	-	-	-	-	-	-	-	-	-
+1	-70.38	-67.29	-72.44	-73.83	-68.69	-68.37	-74.77	-69.39	-67.18
+2	-76.03	-80.04	-79.90	-86.17	-80.35	-80.26	-86.96	-82.34	-79.29
+3	-86.05	-85.63	-89.63	-94.08	-87.33	-88.75	-96.83	-91.94	-86.32
+4	-89.04	-90.22	-94.31	-100.14	-91.76	-93.63	-102.71	-98.02	-91.24
+5	-92.80	-94.60	-95.53	-105.66	-96.67	-98.11	-106.02	-103.50	-96.61

Note 1: Comparison frequency 40 kHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @Fcarrier 1970MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @Fcarrier 2025MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @Fcarrier 2080MHz+(n*Freference) (dBc) note 3		
	n	-40°C	+25°C	+85°C	-40°C	+25°C	+85°C	-40°C	+25°C
-5	-121.62	-126.97	-126.93	-123.87	-121.81	-126.15	-123.01	-123.37	-121.61
-4	-121.35	-125.57	-125.42	-121.75	-127.14	-126.76	-119.96	-122.21	-125.13
-3	-119.11	-123.19	-128.20	-120.38	-126.62	-126.49	-114.16	-125.44	-125.65
-2	-105.23	-105.22	-104.61	-106.54	-107.01	-106.59	-105.57	-107.83	-108.41
-1	-99.96	-97.12	-95.02	-94.04	-92.47	-90.61	-88.62	-88.02	-85.77
0 ^{note 4}	-	-	-	-	-	-	-	-	-
+1	-94.01	-93.47	-96.92	-94.69	-93.87	-96.46	-95.65	-96.74	-96.23
+2	-112.67	-117.42	-117.53	-117.09	-116.97	-116.27	-114.38	-115.04	-112.19
+3	-115.85	-122.12	-127.43	-116.73	-123.75	-127.04	-112.63	-122.13	-126.37
+4	-117.34	-120.84	-119.10	-115.62	-119.67	-123.59	-113.93	-116.87	-117.89
+5	-126.06	-126.09	-127.17	-125.65	-126.04	-127.39	-124.43	-125.98	-126.01

Note 3: Reference frequency 11.52 MHz

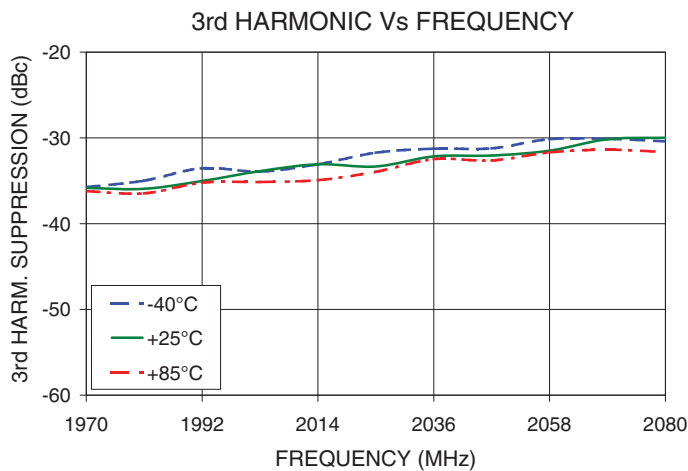
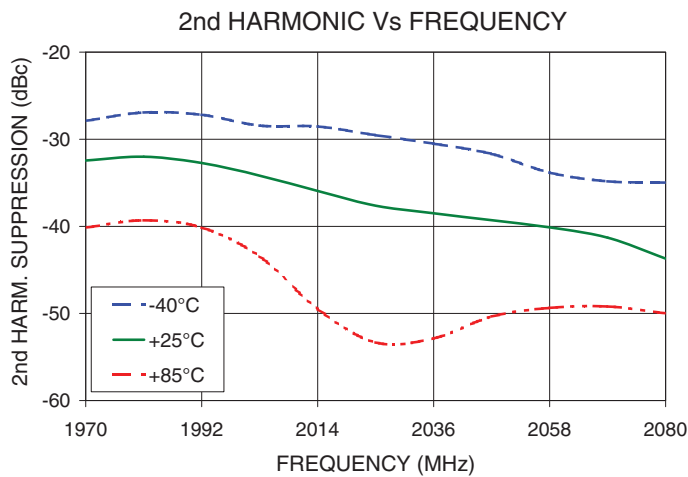
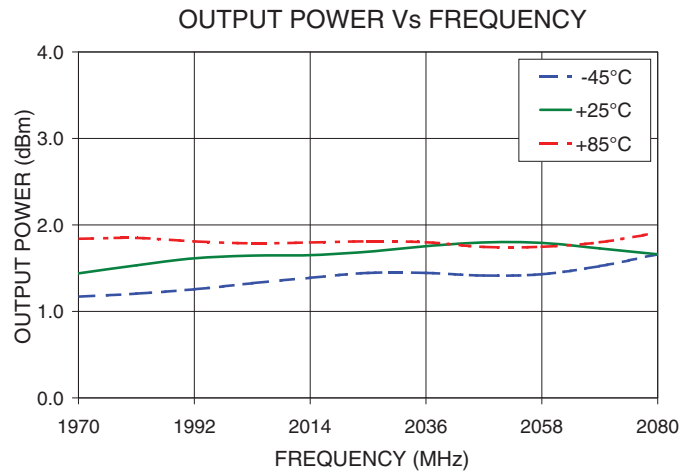
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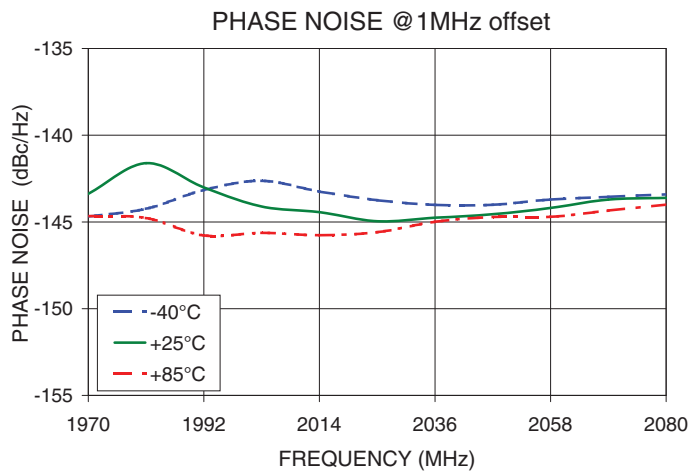
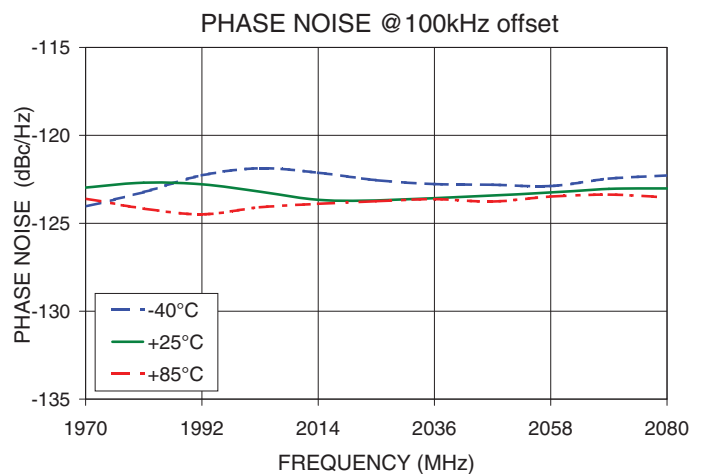
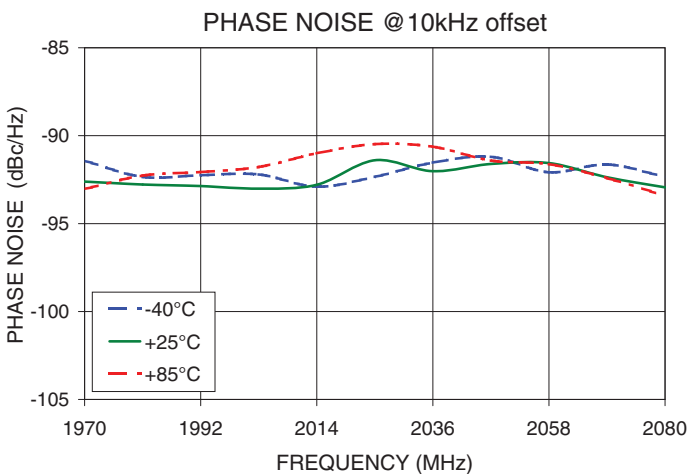
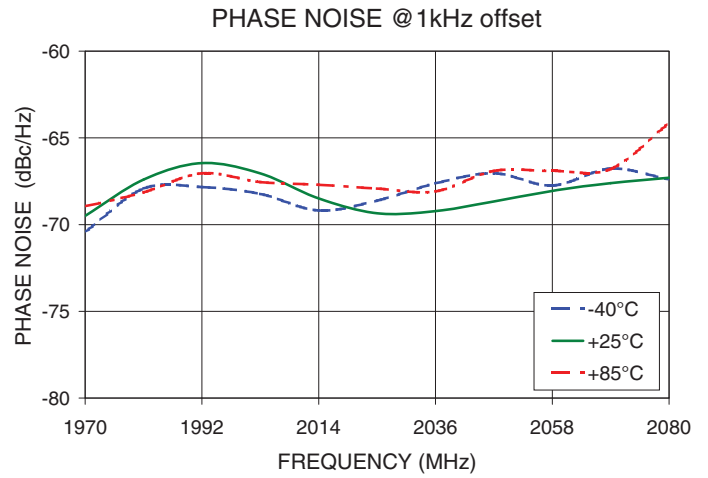
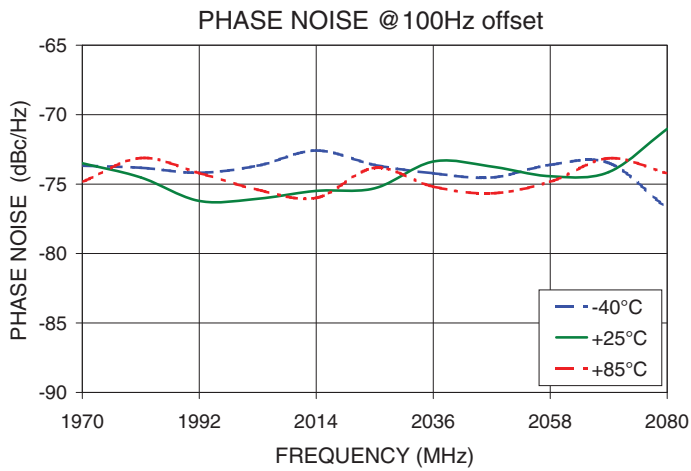
Typical Performance Curves



Notes

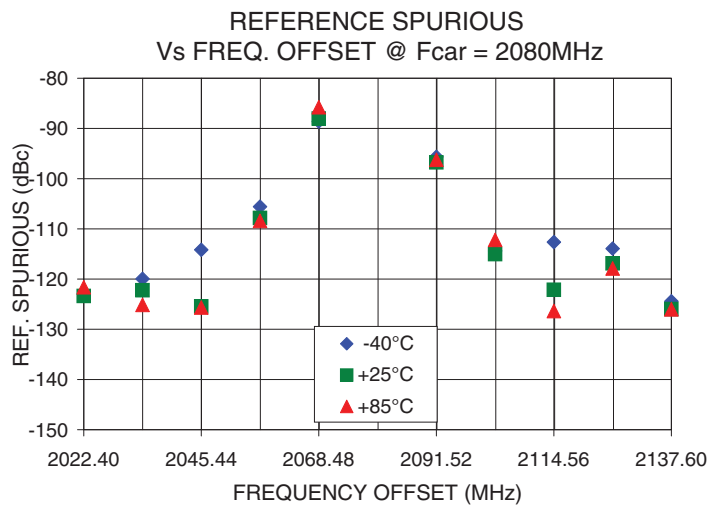
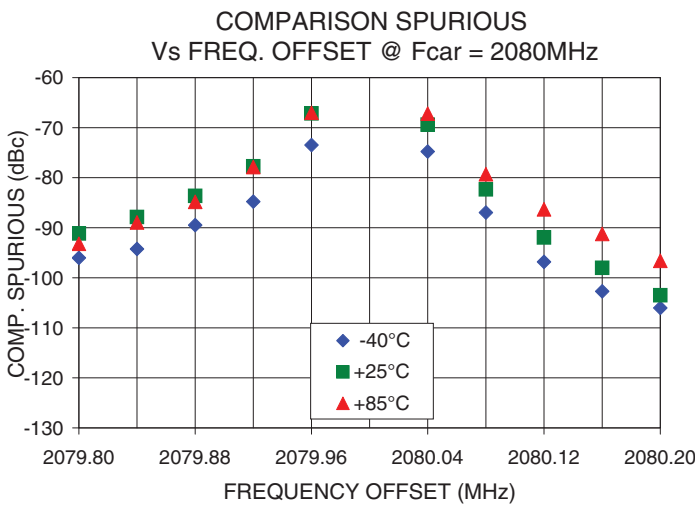
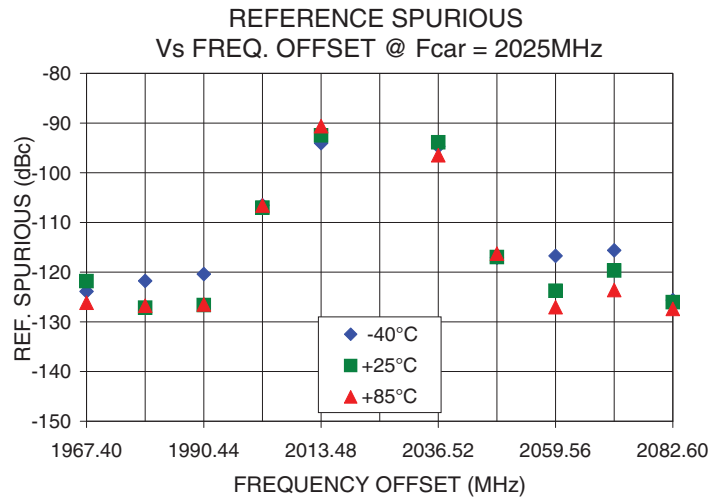
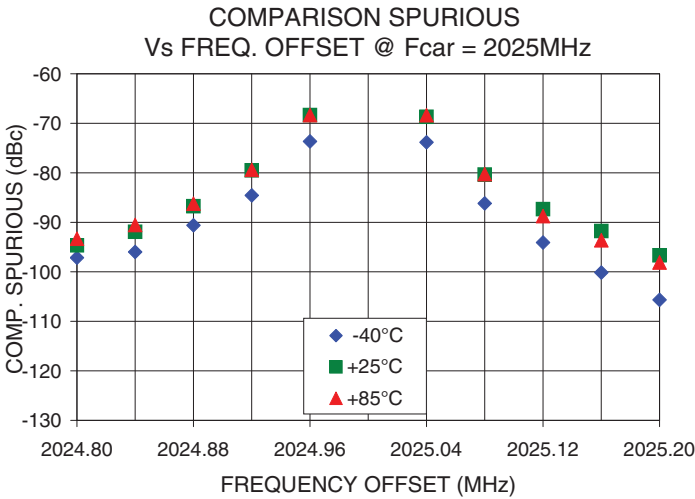
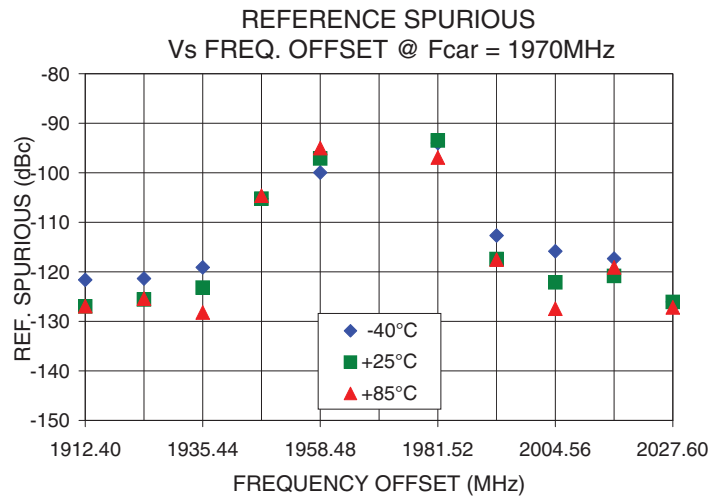
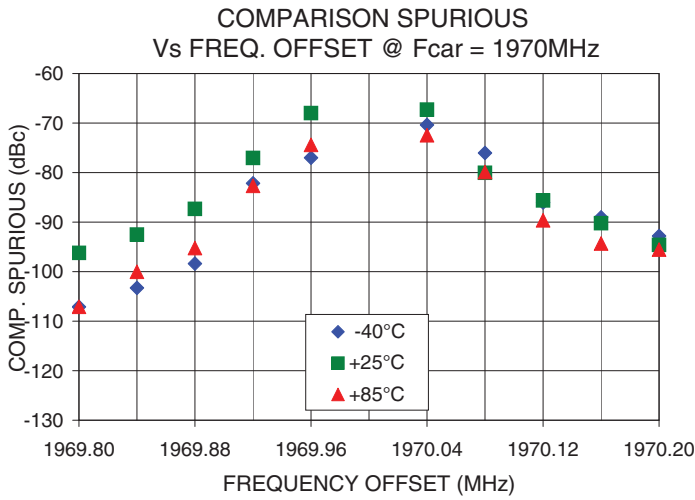
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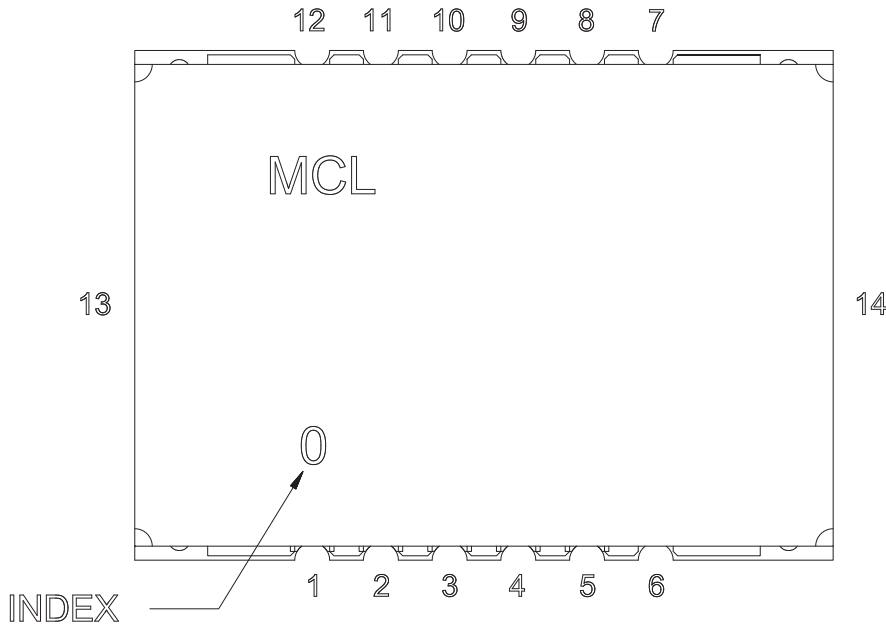


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Pin Configuration

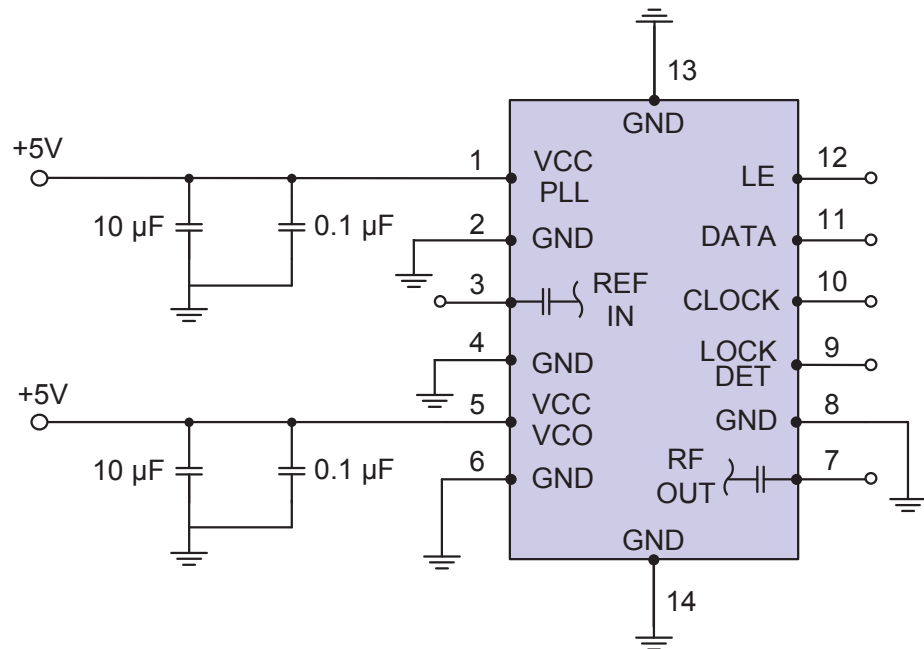


Pin Connection

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.

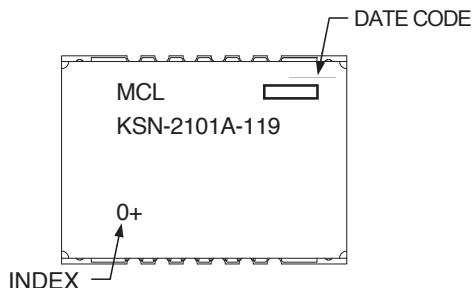


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Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK801

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

Evaluation Board: TB-567+

Environment Ratings: ENV03T2

Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

