

Frequency Synthesizer

KSN-2450A-119+

50 Ω **2350 to 2450 MHz**

The Big Deal

- Fractional N synthesizer
- · Low phase noise and spurious
- · Robust design and construction
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK1042

Product Overview

The KSN-2450A-119+ is a Frequency Synthesizer, designed to operate from 2350 to 2450 MHz for CDMA base station application. The KSN-2450A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -103 dBc/Hz typ. @ 10 kHz offset • Step Size Spurious: -93 dBc typ. • Comparison Spurious: -99 dBc typ. • Reference Spurious: -89 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-2450A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.15"	The small size enables the KSN-2450A-119+ to be used in compact designs.



Frequency Synthesizer

KSN-2450A-119+

2350 to 2450 MHz 50Ω

Features

- Fractional N synthesizer
- Integrated VCO + PLL
- Low phase noise and spurious
- Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+3V)
- Small size 0.80" x 0.58" x 0.15"

Applications

CDMA base station



CASE STYLE: DK1042

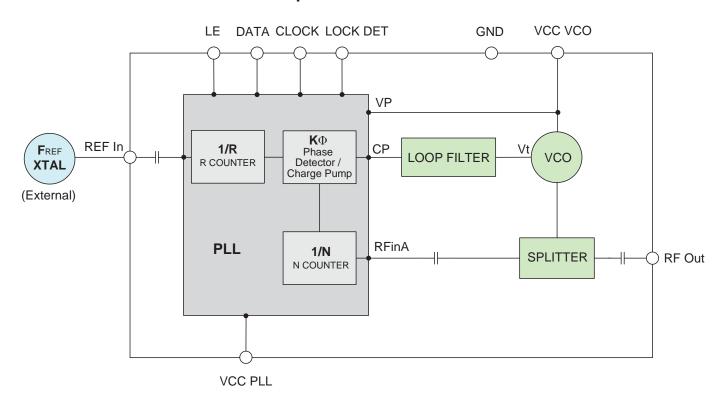
+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

General Description

The KSN-2450A-119+ is a Frequency Synthesizer, designed to operate from 2350 to 2450 MHz for CDMA base station application. The KSN-2450A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise. To enhance the robustness of KSN-2450A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

Simplified Schematic





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Frequency Synthesizer

KSN-2450A-119+

Electrical Specifications (over operating temperature -40°C to +85°C)

Parameters		Test Conditions	Min.	71			
Frequency Range		-	2350	-	2450	MHz	
Step Size	-	-	250	-	kHz		
Comparison Frequency		-	-	10	-	MHz	
Settling Time		Within ± 1 kHz	-	10	-	mSec	
Output Power		-	-2	+1	+4	dBm	
		@ 100 Hz offset	-	-86	-		
		@ 1 kHz offset	-	-89	-84]	
SSB Phase Noise		@ 10 kHz offset	-	-102	-96	dBc/Hz	
		@ 100 kHz offset	-	-125	-120]	
		@ 1 MHz offset	-	-145	-140		
Step Size Spurious Suppression	on	Step Size 250 kHz	-	-93	-75		
0.5 Step Size Spurious Suppre	ession	0.5 Step Size 125 kHz	-	-91	-74]	
Reference Spurious Suppress	ion	Ref. Freq. 30 MHz	-	-89	-70	dBc	
Comparison Spurious Suppres	ssion	Comp. Freq. 10 MHz	-	-99	-75		
Non - Harmonic Spurious Supp	pression	-	-	-90	-	İ	
Harmonic Suppression		-	-	-25	-14		
VCO Supply Voltage		+5.00	+4.75	+5.00	5.25	V	
PLL Supply Voltage		+3.00	+2.85	+3.00	3.15	V	
VCO Supply Current		-	-	35	41	」 _m ∧ │	
PLL Supply Current		-	-	14	23	— mA	
	Frequency	30 (square wave)	-	30	-	MHz	
Reference Input	Amplitude	1	-	1	-	V _{p-P}	
(External)	Input impedance	-	-	100	-	ΚΩ	
	Phase Noise @ 1 kHz offset	-	-	-145	-	dBc/Hz	
RF Output port Impedance		-	-	50	-	Ω	
Input Logic Level	Input high voltage	-	2.55	-	-	V	
Imput Logic Level	Input low voltage	-	-	-	0.55	V	
Digital Lock Detect	Locked	-	2.45	-	3.15	V	
Digital Lock Detect	Unlocked	-	-	-	0.40	V	
Frequency Synthesizer PLL	-	ADF4153					
PLL Programming		-	3-wire seria	3-wire serial 3V CMOS			
	R0_Register	-	(MSB) 001	11101010000	000000000000000000000000000000000000000	(LSB)	
Register Map @ 2450 MHz	R1_Register	-	(MSB) 000101001100000010100001 (LSB)				
	R2_Register	-	(MSB) 0000001111100010 (LSB)				
	R3_Register	-	(MSB) 011	11000111 (LS	SB)		

Absolute Maximum Ratings

Parameters	Ratings
VCO Supply Voltage	5.8V
PLL Supply Voltage	4.0V
VCO Supply Voltage to PLL Supply Voltage	-0.3V to +5.8V
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded



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Typical Performance Data

FREQUENCY	PO	POWER OUTPUT			O CURRE	NT	PLL CURENT		
(MHz)		(dBm)			(mA)			(mA)	
. ,	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
2350	0.74	0.95	0.95	33.54	34.99	35.77	11.77	13.47	16.79
2360	0.73	0.90	0.90	33.61	35.04	35.81	12.60	14.37	16.35
2370	0.77	0.92	0.85	33.68	35.09	35.86	12.53	14.42	16.85
2380	0.92	1.01	0.94	33.73	35.14	35.89	12.53	14.41	16.88
2390	1.08	1.17	1.04	33.77	35.17	35.92	12.55	14.36	16.89
2400	1.22	1.32	1.20	33.82	35.21	35.95	12.60	14.29	16.90
2410	1.27	1.47	1.34	33.87	35.26	35.98	12.30	14.23	16.91
2420	1.27	1.47	1.38	33.93	35.30	36.00	11.77	14.23	16.89
2430	1.23	1.46	1.40	33.97	35.33	36.03	12.31	14.25	16.88
2440	1.21	1.43	1.38	34.01	35.36	36.06	12.38	14.35	16.86
2450	1.20	1.41	1.36	34.02	35.37	36.06	11.77	13.47	15.95

FREQUENCY	HARMONICS (dBc)								
(MHz)		F2			F3				
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C			
2350	-20.82	-25.08	-28.92	-35.19	-38.22	-37.06			
2360	-22.99	-27.50	-31.24	-35.62	-38.52	-37.40			
2370	-25.55	-30.71	-33.79	-37.14	-39.36	-37.90			
2380	-24.93	-31.18	-35.79	-35.57	-38.53	-37.84			
2390	-23.34	-28.63	-37.01	-33.56	-35.86	-37.64			
2400	-21.03	-26.37	-31.47	-32.89	-35.02	-36.41			
2410	-20.87	-24.23	-27.75	-33.19	-34.97	-35.88			
2420	-22.14	-25.86	-29.53	-34.15	-35.52	-37.42			
2430	-23.05	-27.62	-31.51	-34.06	-36.14	-39.15			
2440	-23.21	-28.45	-33.82	-34.10	-36.96	-41.16			
2450	-22.25	-28.07	-34.06	-34.32	-37.20	-41.47			







FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS									
(MHz)		+25°C								
. ,	100Hz	1kHz	10kHz	100kHz	1MHz					
2350	-87.59	-89.20	-103.82	-126.63	-146.62					
2360	-89.14	-88.90	-103.71	-126.65	-146.75					
2370	-89.00	-89.67	-103.40	-126.51	-146.03					
2380	-88.41	-89.85	-103.14	-126.34	-145.76					
2390	-87.30	-89.40	-102.92	-126.14	-145.98					
2400	-87.27	-89.16	-102.63	-125.81	-145.49					
2410	-87.71	-89.01	-102.30	-125.42	-144.68					
2420	-87.91	-88.49	-101.92	-125.26	-144.65					
2430	-87.91	-88.12	-101.59	-125.11	-144.67					
2440	-86.87	-88.72	-101.68	-124.93	-144.62					
2450	-88.55	-89.09	-100.92	-124.78	-144.96					

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS								
(MHz)	-45°C								
	100Hz	1kHz	10kHz	100kHz	1MHz				
2350	-87.11	-89.68	-104.22	-127.81	-147.80				
2360	-88.34	-89.90	-103.95	-127.36	-147.73				
2370	-87.34	-89.49	-104.07	-127.09	-147.16				
2380	-88.65	-88.79	-103.56	-126.94	-147.18				
2390	-89.75	-88.31	-102.95	-126.76	-147.27				
2400	-87.74	-89.05	-102.62	-126.43	-147.04				
2410	-87.47	-88.45	-102.39	-126.08	-146.68				
2420	-88.38	-86.96	-102.23	-125.73	-146.24				
2430	-87.99	-87.23	-101.91	-125.57	-146.02				
2440	-88.49	-87.73	-101.54	-125.36	-144.80				
2450	-90.30	-88.58	-101.09	-125.08	-145.13				

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS								
(MHz)	+85°C								
, ,	100Hz	1kHz	10kHz	100kHz	1MHz				
2350	-87.31	-90.76	-102.56	-125.11	-145.38				
2360	-88.45	-88.33	-102.38	-124.93	-145.12				
2370	-89.44	-88.38	-101.85	-124.82	-145.01				
2380	-88.45	-88.37	-101.69	-124.64	-144.71				
2390	-87.41	-88.32	-101.51	-124.44	-144.38				
2400	-86.86	-87.94	-100.97	-124.15	-143.84				
2410	-86.78	-87.84	-100.49	-123.88	-143.43				
2420	-88.15	-88.62	-100.23	-123.64	-143.41				
2430	-88.28	-88.57	-100.05	-123.47	-143.37				
2440	-86.56	-87.27	-100.00	-123.38	-143.29				
2450	-86.99	-88.29	-99.70	-122.99	-143.27				







COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @Fcarrier 2350MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 2400MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 2450MHz+(n*Fcomparison) (dBc) note 1		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-99.17	-112.43	-97.96	-104.53	-107.34	-99.93	-97.84	-106.37	-95.11
-4	-97.39	-104.38	-95.31	-104.40	-102.29	-98.88	-97.79	-115.36	-97.29
-3	-87.31	-87.89	-87.53	-89.94	-87.19	-86.75	-98.54	-89.48	-94.32
-2	-99.27	-102.73	-107.06	-101.24	-102.14	-98.60	-96.67	-106.07	-108.91
-1	-92.93	-98.79	-93.84	-88.26	-96.34	-91.70	-84.86	-98.26	-95.29
o ^{note 2}	-	-	-	-	-	-	-	-	-
+1	-92.16	-99.65	-94.00	-86.69	-96.19	-90.64	-86.22	-96.30	-97.01
+2	-94.98	-101.86	-95.95	-97.93	-101.34	-101.53	-100.16	-100.54	-104.65
+3	-84.89	-88.61	-83.49	-85.77	-87.88	-85.27	-93.62	-86.27	-88.37
+4	-95.56	-105.27	-97.50	-99.60	-104.59	-106.21	-105.16	-100.19	-103.56
+5	-95.52	-107.10	-95.15	-106.27	-99.30	-118.63	-103.28	-101.09	-111.46

Note 1: Comparison frequency 10 MHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @Fcarrier 2350MHz+(n*Freference) (dBc) note 3			@Fcarrier			REFERENCE SPURIOUS @Fcarrier 2450MHz+(n*Freference) (dBc) note 3		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-95.33	-103.58	-102.69	-100.98	-105.13	-98.66	-99.19	-106.77	-100.82
-4	-94.61	-105.00	-103.98	-95.76	-106.71	-102.94	-94.61	-103.34	-109.71
-3	-93.37	-112.69	-108.42	-97.70	-111.62	-99.16	-103.19	-105.67	-101.30
-2	-97.33	-100.49	-97.96	-91.72	-105.98	-97.25	-96.71	-107.79	-95.85
-1	-90.69	-87.86	-87.47	-88.06	-87.15	-87.66	-91.71	-89.35	-94.14
o ^{note 4}	-	-	-	-	-	-	-	-	-
+1	-87.02	-88.64	-83.68	-85.44	-87.69	-84.99	-85.60	-86.51	-88.15
+2	-101.81	-102.88	-100.97	-96.89	-96.86	-104.74	-104.46	-101.21	-111.86
+3	-107.05	-108.93	-104.14	-99.34	-102.67	-109.52	-97.07	-105.80	-108.75
+4	-104.08	-102.45	-108.67	-98.14	-102.07	-107.38	-98.62	-109.94	-104.77
+5	-101.87	-101.21	-105.23	-109.14	-112.67	-100.10	-98.81	-104.50	-106.42

Note 3: Reference frequency 30 MHz

Note 4: All spurs are referenced to carrier signal (n=0).





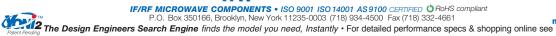


STEP SIZE SPURIOUS ORDER	0.5 STEP SIZE & STEP SIZE SPURIOUS @Fcarrier 2352MHz+(n*Fstep size) (dBc) note 5			0.5 STEP SIZE & STEP SIZE SPURIOUS @Fcarrier 2402MHz+(n*Fstep size) (dBc) note 5			0.5 STEP SIZE & STEP SIZE SPURIOUS @Fcarrier 2448MHz+(n*Fstep size) (dBc) note 5		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5.0	-117.25	-120.68	-118.56	-113.08	-116.43	-113.66	-105.48	-108.68	-107.62
-4.5	-109.09	-112.81	-110.50	-114.02	-114.79	-118.22	-102.81	-109.13	-109.09
-4.0	-127.84	-127.41	-119.71	-123.60	-131.03	-119.46	-114.47	-123.45	-124.78
-3.5	-110.77	-107.71	-110.17	-118.47	-120.76	-108.08	-106.18	-109.84	-113.54
-3.0	-107.89	-106.71	-110.90	-112.75	-114.47	-112.94	-112.04	-114.48	-117.58
-2.5	-102.17	-105.31	-102.51	-100.62	-102.68	-102.86	-102.70	-100.86	-104.41
-2.0	-119.99	-113.75	-109.91	-111.32	-112.77	-102.76	-99.96	-101.01	-102.46
-1.5	-97.59	-99.85	-96.85	-94.89	-97.41	-96.39	-100.48	-101.50	-104.42
-1.0	-91.78	-93.60	-94.52	-95.77	-98.90	-98.22	-88.69	-96.88	-95.97
-0.5	-99.73	-100.99	-92.12	-99.65	-98.97	-97.00	-86.30	-90.34	-89.54
o ^{note 6}	-	-	-	-	-	-	-	-	-
+0.5	-101.90	-100.02	-92.89	-98.59	-94.45	-95.92	-85.81	-90.06	-88.09
+1.0	-92.14	-93.58	-94.34	-94.81	-99.91	-95.31	-88.56	-97.36	-93.88
+1.5	-95.03	-100.45	-96.87	-94.96	-96.65	-96.95	-100.08	-102.46	-103.45
+2.0	-115.25	-113.71	-108.07	-111.13	-115.74	-103.31	-98.98	-100.77	-102.42
+2.5	-101.23	-105.63	-101.53	-100.77	-103.12	-103.28	-102.12	-100.02	-102.96
+3.0	-107.26	-105.83	-109.00	-112.93	-120.60	-113.86	-110.75	-116.34	-112.85
+3.5	-110.67	-107.07	-110.78	-116.74	-118.52	-106.84	-106.20	-111.75	-113.80
+4.0	-130.16	-130.16	-115.98	-121.63	-126.92	-118.14	-114.43	-122.39	-127.84
+4.5	-108.44	-111.96	-108.72	-114.87	-114.78	-122.29	-102.89	-108.61	-109.78
+5.0	-118.86	-120.66	-120.47	-113.48	-113.56	-114.24	-104.87	-108.56	-108.30

Note 5: Step size 250 kHz

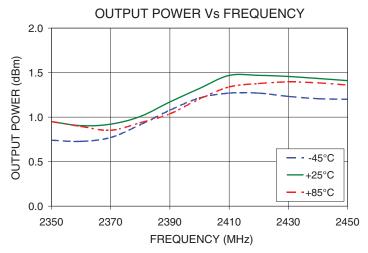
Note 6: All spurs are referenced to carrier signal (n=0).

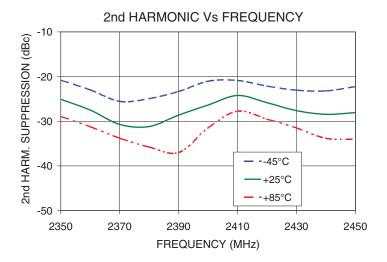


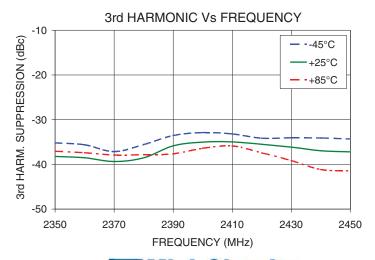




Typical Performance Curves





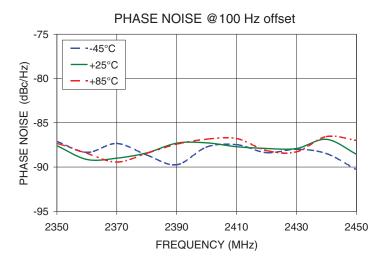


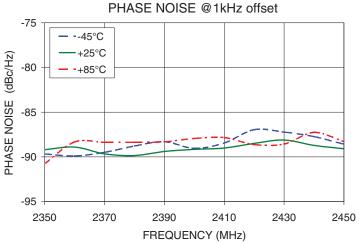
Mini-Circuits

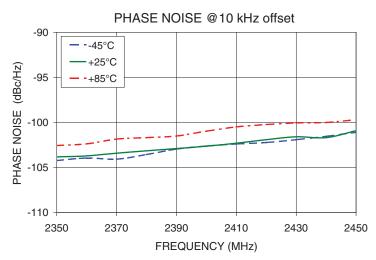
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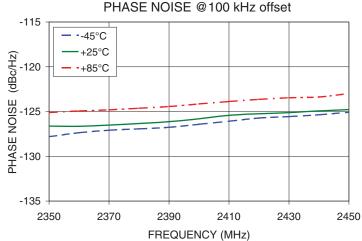
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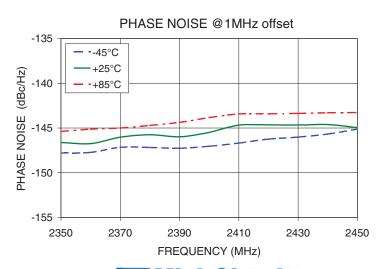












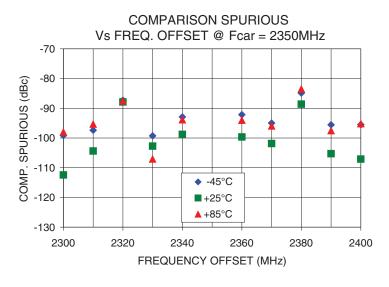
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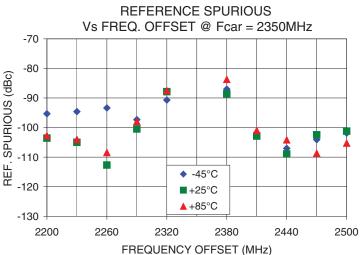
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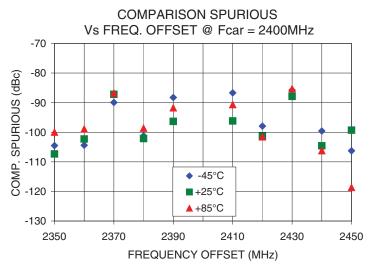
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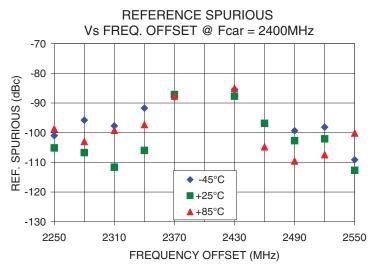
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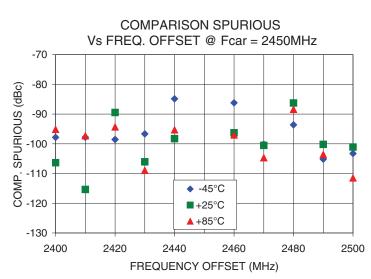


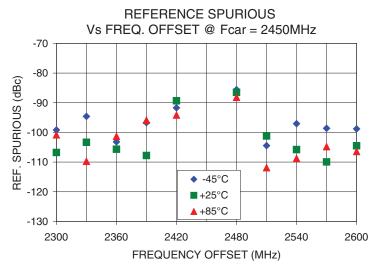








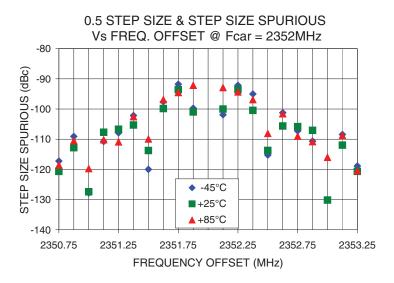


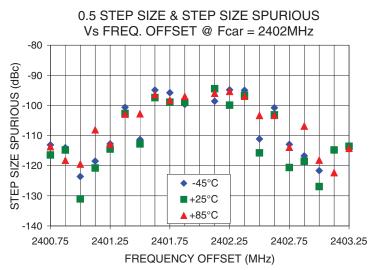


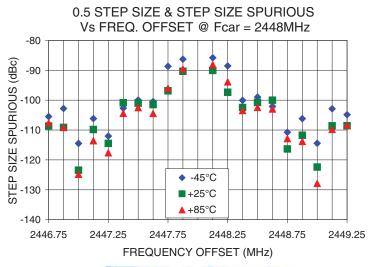
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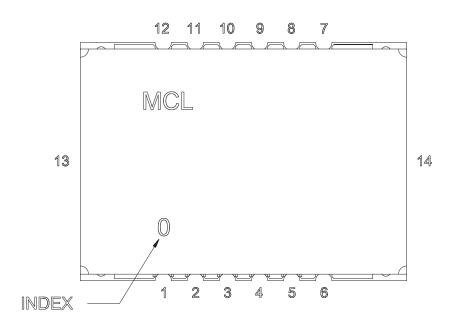




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Pin Configuration

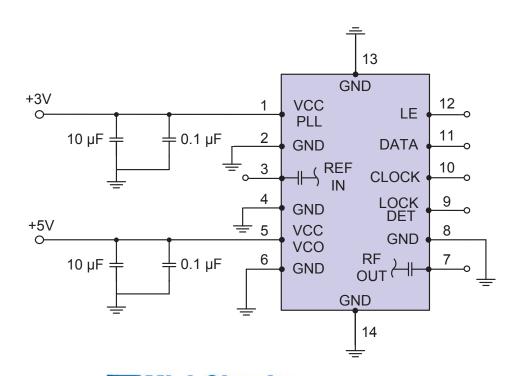


Pin Connection

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.

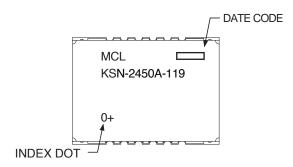




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Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK1042

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

Evaluation Board: TB-567-2+

Environment Ratings: ENV03T2





