# **NON-CATALOG**

# Frequency Synthesizer

KSN-409A-219+

50 $\Omega$  409.6 MHz (fixed)

## The Big Deal

- · Low phase noise and spurious
- Robust design and construction
- Small size 0.800" x 0.584" x 0.154"



CASE STYLE: DK1042

## **Product Overview**

The KSN-409A-219+ is a Frequency Synthesizer, designed to operate 409.6MHz for military lab application. The KSN-409A-219+ is packaged in a metal case (size of 0.800" x 0.584" x 0.154") to shield against unwanted signals and noise.

## **Key Features**

Feature	Advantages
Low phase noise and spurious: • Phase noise: -109 dBc/Hz typ. @ 10 kHz offset • Comparison spurious: -80 dBc typ. • Reference spurious: -105 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-409A-219+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.800" x 0.584" x 0.154"	The small size enables the KSN-409A-219+ to be used in compact designs.



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KSN-409A-219+

 $50\Omega$  409.6 MHz (fixed)

#### **Features**

- Integrated VCO + PLL
- · Low phase noise and spurious
- · Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+3.3V)
- Small size 0.800" x 0.584" x 0.154"



CASE STYLE: DK1042 PRICE: Contact Sales Dept.

+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

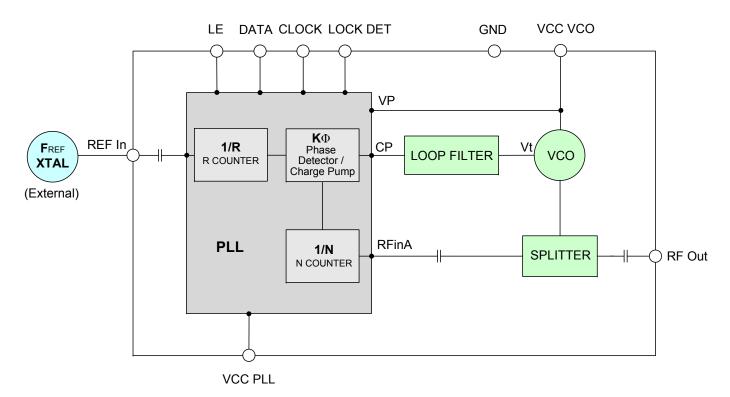
#### **Applications**

· Military lab

#### **General Description**

The KSN-409A-219+ is a Frequency Synthesizer, designed to operate 409.6MHz for military lab application. The KSN-409A-219+ is packaged in a metal case (size of 0.800" x 0.584" x 0.154") to shield against unwanted signals and noise. To enhance the robustness of KSN-409A-219+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

#### **Simplified Schematic**



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### Electrical Specifications (over operating temperature -20°C to +85°C)

Parame	Parameters						Tes	st Condit	ions	Mi	Min.			Мах.	Uı	nits	
Frequency	y Range								-		409	9.6	-		409.6	N	ИHz
Step size								-			-		400		-	k	ιHz
Settling Ti	ime						W	Within ± 1 kHz			-		2		-	m	Sec
Output Po	ower								-		+3	.0	+5.3		+8.0	d	Bm
					@	100	Hz offset		-		-92		-				
							@	1 kF	Iz offset		-		-97		-90		1
SSB Phas	se Noise						@	10 k	KHz offset		-		-109		-105	dB	c/Hz
							@	100	kHz offset		-		-135		-130		
							@	2 1 M	Hz offset		-		-156		-150		
Reference	Spurious Suppre	ession					Re	ef. Fr	req. 10 MHz		-		-105		-90		
Comparis	on Spurious Supp	ression					St	tep S	ize 400 kHz		-		-80		-70	c	dBc
Non - Har	monic Spurious S	Suppress	ion						-		-		-90		-		
Harmonic	Suppression								-		-		-35		-29	C	dBc
	ply Voltage								+5.00		+4.	75	+5.00		+5.25		
PLL Supp	ly Voltage							+3.30 +3.15			15	+3.30		+3.45		V	
VCO Sup	ply Current							-		-		35		41		0	
PLL Supp	ly Current							-		-		10		17 mA		nA	
		F	requ	ency					10 (sine wav	ve) - 10			-	N	ИHz		
Reference	e Input	7	Ampli	tude				1.0		-	-			- V <sub>p.p</sub>		/ <sub>p.p</sub>	
(External)		Ī	Input impedance				-		1 -		100		-		ΚΩ		
		F	hase	hase Noise @ 1 kHz offset				-		-		-145		-	dB	Bc/Hz	
RF Outpu	t port Impedance							-		1 -		50		-		Ω	
		I	nput	high vol	tage		-		2.8	30	-		-		V		
Input Logi	ic Level			low volt				-		1 -		-		0.60		V	
		L	ocke	ed				-		2.7	75	-		3.45		V	
Digital Lo	ck Detect	ī	Jnloc	ked					-		1 -		-		0.40		V
Frequency	y Synthesizer PLI								-		ADF4	113					
PLL Progr									-		3-wire	e serial 3	3.3V CM	os			
		Prescaler	Value	Power- Down 2	Current S	Setting 2	Currer Setting		Timer Counter Control	Fastlock Mode	Fastlock Enable	CP Three-	PD Polarity	Muxout Control	Power- Down 1	Counter Reset	Control Bits
	F_Register NOTE 2	01		0	11	1	111	-	0000	0	0	0	1	001	0	0	10
Register Map NOTE 1	N_Register	Reserv					13-Bit B Counter						6-Bit A Counter			Control	
		00						000001000000					000000			Bits 01	
				1	Lock Detect	Test Mode			0000001						000		Control
	R_Register	Reserved		SYNC	Precision	Bits	Width	h					Counter, R	· · · · · · · · · · · · · · · · · · ·			Bits
	0		0	0	1	00	00				000	0000000	11001				00

Note 1: Registers Load Sequence: Initialization Register, F Register, R Register, N Register.

Note 2: For the Initialization Register use Register F with Control Bits 11.

## **Absolute Maximum Ratings**

Parameters	Ratings
VCO Supply Voltage NOTE 3	6V
PLL Supply Voltage NOTE 3	6V
VCO Supply Voltage to PLL Power Supply NOTE 3	-0.3V to +5.5V
Reference Frequency Voltage	-0.3Vmin,VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin,VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Note 3: Power on/off Sequence: Power on: VCO Supply Voltage, followed by PLL Supply Voltage. Power off: PLL Supply Voltage, followed by VCO Supply Voltage.

Permanent damage may occur if any of these limits are exceeded



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## Typical Performance Data

FREQUENCY	POWER OUTPUT			T VCO CURRENT			PLL CURRENT			
(MHz)		(dBm)			(mA)			(mA)		
	-25°C	+25°C	+85°C	-25°C	+25°C	+85°C	-25°C	+25°C	+85°C	
409.6	5.18	5.27	5.10	33.44	34.67	35.71	8.22	9.81	11.64	

FREQUENCY	HARMONICS (dBc)					
(MHz)		F2			F3	
	-25°C	+25°C	+85°C	-25°C	+25°C	+85°C
409.6	-33.77	-35.60	-38.68	-46.56	-47.66	-50.52

			PHASE	E NOISE (d	Bc/Hz)				
FREQUENCY	@ТЕМР.	@ OFFSETS							
		100Hz	1kHz	10kHz	100kHz	1MHz			
	-25°C	-93.63	-96.20	-107.98	-134.40	-154.90			
409.6	+25°C	-94.13	-97.91	-109.72	-135.63	-156.15			
	+85°C	-95.30	-97.11	-108.79	-135.65	-155.85			

COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS  @Fcarrier  409.6MHz+(n*Fcomparison) (dBc) note 1					
n	-25°C	+25°C	+85°C			
-5	-93.66	-96.13	-103.35			
-4	-91.31	-93.16	-99.37			
-3	-89.56	-91.68	-97.98			
-2	-85.19	-88.86	-95.86			
-1	-79.89	-80.03	-76.78			
0 <sup>note 2</sup>	-	-	-			
+1	-79.81	-79.35	-76.67			
+2	-85.46	-89.25	-96.21			
+3	-89.73	-91.37	-98.54			
+4	-91.93	-93.67	-99.98			
+5	-94.29	-97.35	-105.09			

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Note 1:	Comr	narison	frequency	400 kHz

Note 2: All spurs are referenced to carrier signal (n=0).

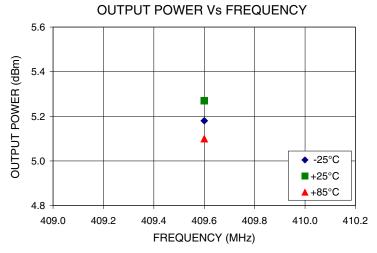
REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS  @Fcarrier  409.6MHz+(n*Freference)  (dBc) note 3						
n	-25°C	+25°C	+85°C				
-5	-104.66	-110.78	-105.32				
-4	-110.18	-108.61	-108.86				
-3	-107.15	-111.44	-110.82				
-2	-112.30	-115.92	-111.15				
-1	-110.42	-113.51	-119.58				
o <sup>note 4</sup>	-	-	-				
+1	-111.38	-112.44	-115.94				
+2	-110.61	-116.05	-120.16				
+3	-110.22	-111.55	-112.88				
+4	-111.18	-112.16	-116.67				
+5	-104.93	-104.60	-110.74				

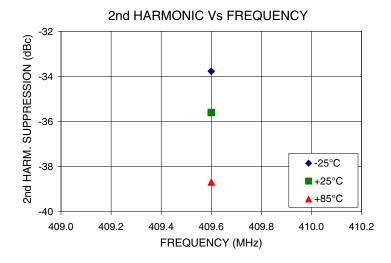
Note 3: Reference frequency 10 MHz

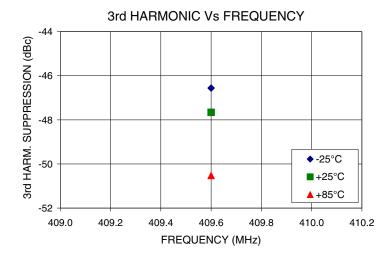
Note 4: All spurs are referenced to carrier signal (n=0).



## **Typical Performance Curves**

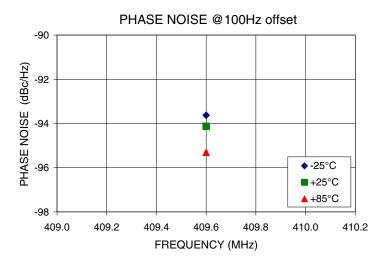


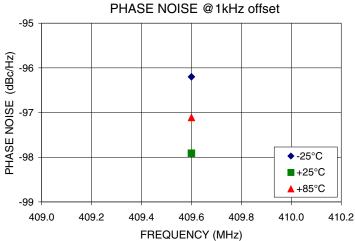


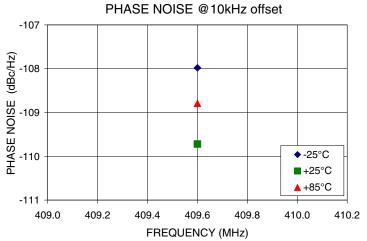


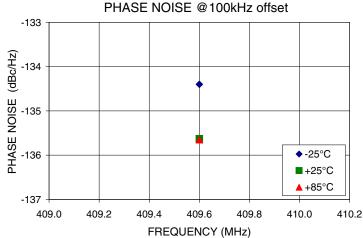
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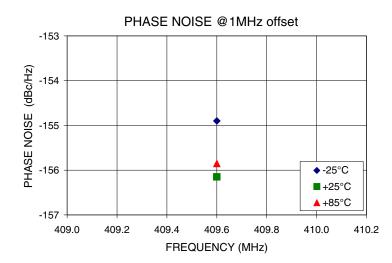
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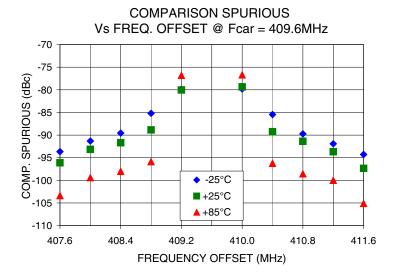






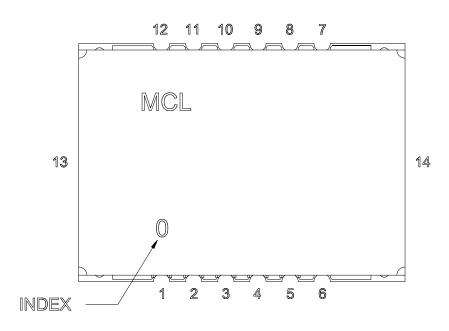


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#### REFERENCE SPURIOUS Vs FREQ. OFFSET @ Fcar = 409.6MHz -100 -105 -105 -110 (dBc) -115 -120 REF. -25°C ■+25°C ▲ +85°C -130 359.6 379.6 399.6 419.6 439.6 459.6 FREQUENCY OFFSET (MHz)

## **Pin Configuration**

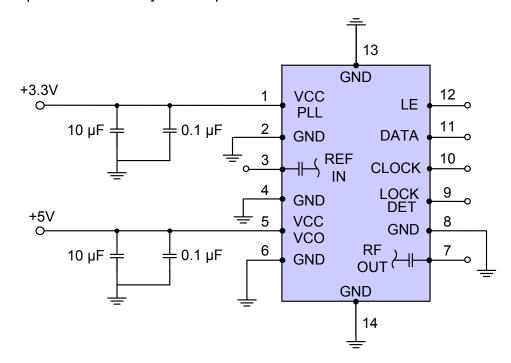


#### **Pin Connection**

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

### **Recommended Application Circuit**

Note: REF IN and RF OUT ports are internally AC coupled.

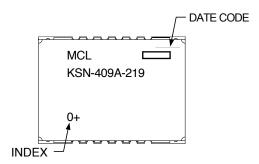


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### **Device Marking**



#### **Additional Detailed Technical Information**

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK1042

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

**Evaluation Board:** TB-567-1+

**Environment Ratings:** ENV03T2

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