NON-CATALOG

Frequency Synthesizer KSN-529A-119+

50Ω 456 to 530 MHz

The Big Deal

- Low phase noise and spurious
- Robust design and construction
- Small size 0.80" x 0.58" x 0.15"

CASE STYLE: DK801

Product Overview

The KSN-529A-119+ is a Frequency Synthesizer, designed to operate from 456 to 530 MHz for TD-SCDMA applications. The KSN-529A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -113 dBc/Hz typ. @ 10 kHz offset • Comparison Spurious: -68 dBc typ. • Reference Spurious: -117 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-529A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.15"	The small size enables the KSN-529A-119+ to be used in compact designs.

Surface Mount

NON-CATALOG

Frequency Synthesizer

KSN-529A-119+

50Ω 456 to 530 MHz

CASE STYLE: DK801

+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

Features

- Integrated VCO + PLL
- Low phase noise and spurious
- Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+3.3V)
- Small size 0.80" x 0.58" x 0.15"

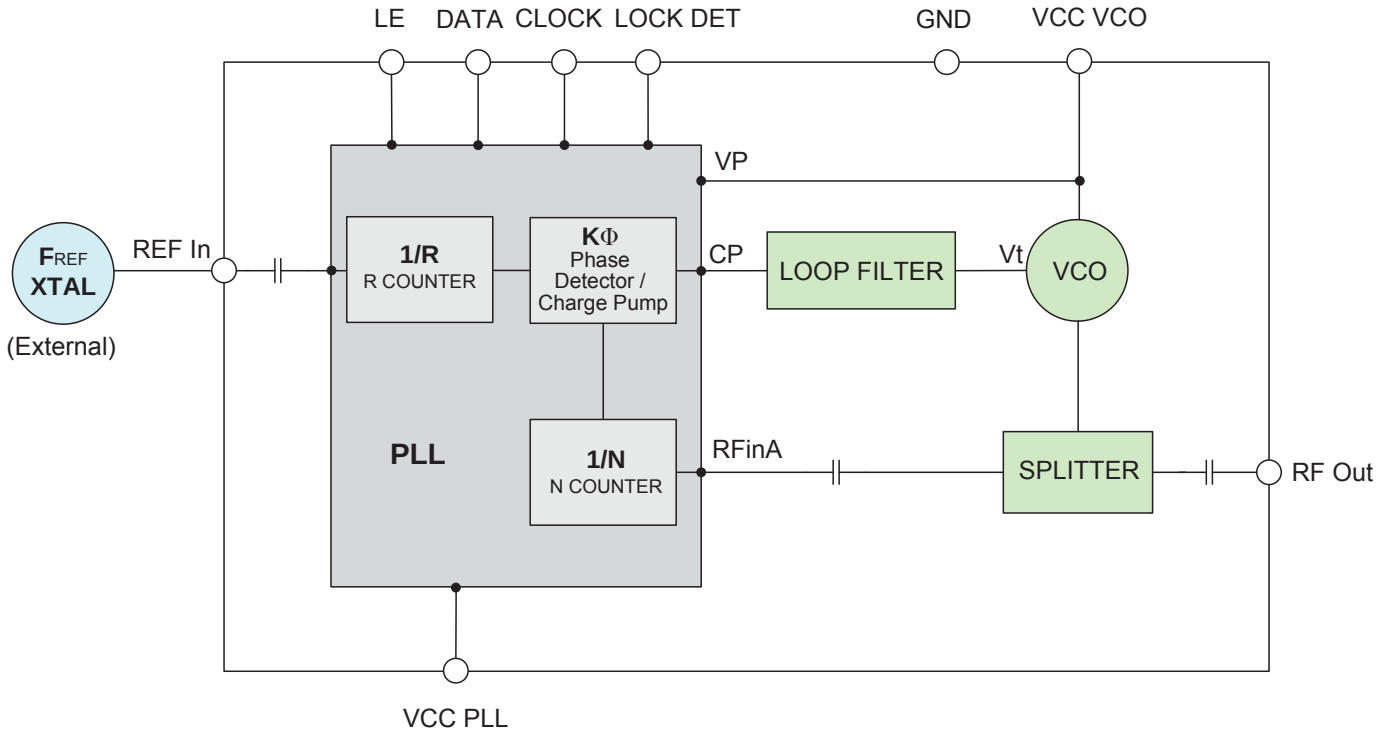
Applications

- TD-SCDMA

General Description

The KSN-529A-119+ is a Frequency Synthesizer, designed to operate from 456 to 530 MHz for TD-SCDMA applications. The KSN-529A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise. To enhance the robustness of KSN-529A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tomb-stoning during subsequent solder reflow operations by the customer.

Simplified Schematic



Electrical Specifications *(over operating temperature 0°C to +85°C)*

Parameters		Test Conditions	Min.	Typ.	Max.	Units
Frequency Range		-	456	-	530	MHz
Step Size		-	-	5	-	kHz
Settling Time		Within ± 1 kHz	-	30	-	mSec
Output Power		-	-1.5	+1.5	+4.5	dBm
SSB Phase Noise		@ 100 Hz offset	-	-73	-	dBc/Hz
		@ 1 kHz offset	-	-77	-69	
		@ 10 kHz offset	-	-113	-107	
		@ 100 kHz offset	-	-138	-132	
		@ 1 MHz offset	-	-159	-155	
Integrated SSB Phase Noise		@ 275kHz - 1.5MHz	-	-95	-	dBc
Reference Spurious Suppression		Ref. Freq.15 MHz	-	-117	-83	dBc
Comparison Spurious Suppression		Step Size 5 kHz	-	-68	-45	
Non - Harmonic Spurious Suppression		-	-	-90	-	
Harmonic Suppression		-	-	-32	-25	
VCO Supply Voltage		+5.00	+4.75	+5.00	+5.25	V
PLL Supply Voltage		+3.30	+3.15	+3.30	+3.45	
VCO Supply Current		-	-	17	23	mA
PLL Supply Current		-	-	9	15	
Reference Input (External)	Frequency	15 (square wave)	-	15	-	MHz
	Amplitude	1	-	1	-	V_{P-P}
	Input impedance	-	-	100	-	KΩ
	Phase Noise @ 1 kHz offset	-	-	-140	-	dBc/Hz
RF Output port Impedance		-	-	50	-	Ω
Input Logic Level	Input high voltage	-	2.80	-	-	V
	Input low voltage	-	-	-	0.60	V
Digital Lock Detect	Locked	-	2.75	-	3.45	V
	Unlocked	-	-	-	0.40	V
Frequency Synthesizer PLL		-	ADF4113			
PLL Programming		-	3-wire serial 3.3V CMOS			

Register Map NOTE 1

F_Register NOTE 2	*Prescaler Value*	*Power-Down 2*	*Current Setting 2*	*Current Setting 1*	*Timer Counter Control*	*Fastlock Mode*	*Fastlock Enable*	*CP Three-State*	*PD Polarity*	*Muxout Control*	*Power-Down 1*	*Counter Reset*	*Control Bits*
	01	0	111	111	0000	0	0	0	1	001	0	0	10
N_Register @ 530 MHz	*Reserved*	*CP Gain*	*13-Bit B Counter*						*6-Bit A Counter*				*Control Bits*
	00	1	1100111100001						000000				01
R_Register	*Reserved*	*DLY*	*SYNC*	*Lock Detect Precision*	*Test Mode Bits*	*Anti-Backlash Width*	*14-BIT Reference Counter, R*						*Control Bits*
	0	0	0	1	00	00	00101110111000						00

Note 1: Registers Load Sequence: Initialization Register, F Register, R Register, N Register.
Note 2: For the Initialization Register use Register F with Control Bits 11.

Absolute Maximum Ratings

Parameters	Ratings
VCO Supply Voltage NOTE 3	6V
PLL Supply Voltage NOTE 3	6V
VCO Supply Voltage to PLL Supply Voltage NOTE 3	-0.3V to +5.5V
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded

Note 3: Power on/off Sequence:
Power on: VCO Supply Voltage, followed by PLL Supply Voltage.
Power off: PLL Supply Voltage, followed by VCO Supply Voltage.

Typical Performance Data

FREQUENCY (MHz)	POWER OUTPUT (dBm)			VCO CURRENT (mA)			PLL CURRENT (mA)		
	-5°C	+25°C	+85°C	-5°C	+25°C	+85°C	-5°C	+25°C	+85°C
456	1.78	1.82	1.64	16.84	17.24	17.93	8.48	9.30	10.86
460	1.76	1.79	1.61	16.84	17.26	17.95	8.48	9.31	10.88
465	1.73	1.76	1.58	16.86	17.27	17.96	8.48	9.32	10.88
470	1.70	1.72	1.54	16.88	17.28	17.97	8.48	9.32	10.89
475	1.67	1.69	1.51	16.89	17.30	17.97	8.49	9.32	10.90
480	1.64	1.66	1.48	16.90	17.30	17.97	8.49	9.33	10.91
485	1.62	1.63	1.45	16.91	17.31	17.98	8.49	9.33	10.91
490	1.60	1.60	1.42	16.93	17.32	17.99	8.50	9.33	10.92
495	1.58	1.58	1.40	16.94	17.34	17.99	8.50	9.34	10.92
500	1.58	1.57	1.39	16.95	17.35	17.99	8.50	9.34	10.93
505	1.56	1.55	1.37	16.98	17.36	18.00	8.51	9.35	10.93
510	1.55	1.55	1.35	16.99	17.38	18.01	8.50	9.35	10.94
515	1.54	1.53	1.33	17.01	17.39	18.01	8.51	9.35	10.94
520	1.53	1.53	1.32	17.03	17.40	18.02	8.51	9.35	10.95
525	1.53	1.53	1.31	17.06	17.42	18.03	8.51	9.36	10.95
530	1.52	1.52	1.29	17.07	17.44	18.04	8.52	9.36	10.96

FREQUENCY (MHz)	HARMONICS (dBc)					
	F2			F3		
	-5°C	+25°C	+85°C	-5°C	+25°C	+85°C
456	-30.30	-31.03	-30.63	-37.63	-37.59	-38.53
460	-29.58	-30.40	-30.27	-33.39	-33.36	-34.06
465	-29.74	-30.61	-30.60	-36.04	-36.02	-36.54
470	-31.14	-31.93	-31.82	-35.61	-35.76	-36.52
475	-31.61	-32.29	-31.88	-33.63	-33.72	-34.22
480	-30.83	-31.26	-30.42	-38.29	-38.47	-38.98
485	-31.55	-31.69	-30.57	-36.16	-36.40	-37.15
490	-33.19	-33.37	-32.32	-33.70	-33.73	-34.30
495	-33.25	-33.58	-32.83	-38.71	-38.88	-39.50
500	-31.79	-32.20	-31.61	-35.79	-35.94	-36.60
505	-31.62	-31.92	-31.18	-35.44	-35.60	-35.90
510	-34.06	-34.23	-33.36	-39.54	-39.91	-40.35
515	-35.22	-35.27	-34.31	-36.77	-37.01	-37.61
520	-33.86	-33.88	-32.88	-38.50	-38.69	-38.94
525	-32.07	-32.08	-31.08	-41.24	-41.57	-42.11
530	-34.01	-34.09	-33.31	-38.33	-38.57	-39.07

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @OFFSETS +25°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
456	-75.94	-76.37	-113.22	-140.41	-160.48
460	-75.72	-77.30	-112.83	-139.64	-160.22
465	-74.84	-75.98	-112.62	-139.59	-160.13
470	-72.96	-75.51	-112.55	-139.15	-160.23
475	-76.06	-74.88	-112.31	-139.04	-159.55
480	-74.91	-74.24	-112.37	-138.69	-159.34
485	-75.86	-73.59	-112.47	-138.46	-158.79
490	-75.19	-75.99	-112.73	-138.29	-158.87
495	-73.42	-76.22	-112.96	-138.15	-159.96
500	-75.49	-76.75	-113.39	-138.12	-159.20
505	-73.54	-78.45	-113.81	-137.83	-158.89
510	-72.51	-78.29	-114.25	-137.69	-159.03
515	-72.53	-79.75	-114.77	-137.68	-159.36
520	-68.98	-81.54	-115.17	-137.64	-158.59
525	-70.01	-82.88	-115.59	-137.71	-159.15
530	-70.01	-83.92	-115.97	-137.53	-158.78

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @OFFSETS -5°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
456	-75.28	-77.23	-113.83	-139.42	-160.64
460	-77.62	-74.27	-113.66	-140.19	-160.99
465	-76.58	-77.22	-113.64	-139.65	-160.72
470	-76.18	-76.59	-113.55	-138.98	-160.11
475	-75.13	-75.54	-113.36	-138.67	-160.05
480	-75.70	-76.83	-113.43	-138.37	-159.25
485	-76.07	-74.66	-113.86	-138.11	-159.34
490	-75.88	-79.45	-113.67	-138.15	-159.53
495	-74.42	-78.33	-114.18	-137.84	-159.10
500	-75.93	-78.54	-114.39	-137.66	-159.06
505	-75.35	-78.57	-114.72	-137.64	-159.35
510	-75.57	-81.85	-115.10	-137.53	-159.48
515	-73.46	-81.81	-115.45	-137.42	-159.31
520	-74.31	-82.11	-115.84	-137.65	-159.04
525	-71.75	-84.22	-116.05	-137.61	-159.45
530	-72.13	-83.28	-116.50	-137.57	-159.83

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @OFFSETS +85°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
456	-76.67	-75.51	-113.15	-139.65	-159.56
460	-73.78	-74.55	-112.06	-138.29	-159.62
465	-75.25	-75.78	-111.85	-139.33	-159.81
470	-74.29	-73.70	-111.34	-138.76	-159.07
475	-73.72	-75.00	-111.58	-137.97	-158.88
480	-74.31	-75.07	-111.87	-138.80	-158.87
485	-71.34	-74.90	-111.84	-138.57	-159.28
490	-74.23	-75.02	-112.25	-138.29	-158.35
495	-70.42	-76.72	-112.62	-137.92	-158.60
500	-70.72	-76.63	-112.91	-137.91	-158.74
505	-69.20	-77.64	-113.34	-137.67	-158.55
510	-68.28	-77.70	-113.84	-137.37	-158.53
515	-67.56	-80.47	-114.25	-137.49	-158.34
520	-64.83	-80.79	-114.66	-137.11	-158.57
525	-65.50	-83.38	-115.07	-137.33	-158.79
530	-71.39	-83.50	-115.53	-137.28	-158.78

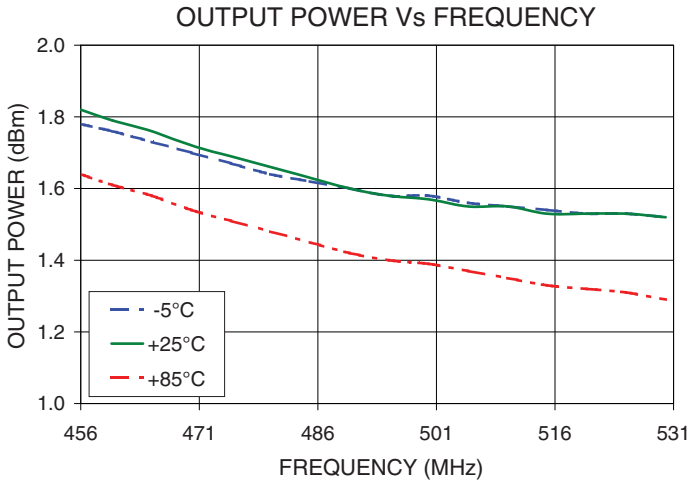
COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @Fcarrier 456MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 490MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @Fcarrier 530MHz+(n*Fcomparison) (dBc) note 1		
n	-5°C	+25°C	+85°C	-5°C	+25°C	+85°C	-5°C	+25°C	+85°C
-5	-94.43	-97.21	-84.00	-93.93	-97.37	-87.80	-94.23	-95.41	-85.71
-4	-92.98	-93.24	-80.74	-91.18	-92.66	-82.43	-91.85	-93.70	-80.47
-3	-88.03	-89.49	-75.20	-86.98	-89.36	-77.95	-86.08	-86.34	-75.86
-2	-84.78	-86.13	-68.36	-80.74	-83.82	-71.16	-81.94	-80.29	-69.56
-1	-74.77	-70.69	-56.15	-66.86	-66.95	-56.75	-62.18	-63.41	-55.59
0 note 2	-	-	-	-	-	-	-	-	-
+1	-67.46	-72.62	-55.65	-64.62	-66.93	-57.59	-61.94	-63.58	-55.48
+2	-84.83	-86.11	-68.01	-80.89	-82.81	-71.71	-80.03	-81.38	-69.25
+3	-89.17	-90.68	-75.53	-87.58	-90.15	-78.27	-85.78	-89.59	-76.81
+4	-93.02	-92.78	-80.81	-92.92	-93.47	-83.67	-93.56	-92.24	-82.35
+5	-95.13	-96.22	-84.67	-92.61	-96.35	-86.49	-94.56	-95.66	-86.54

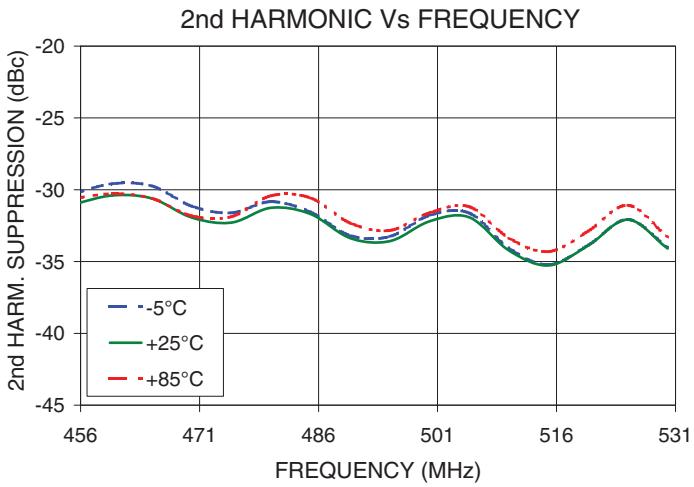
Note 1: Comparison frequency 5 kHz
Note 2: All spurs are referenced to carrier signal (n=0).

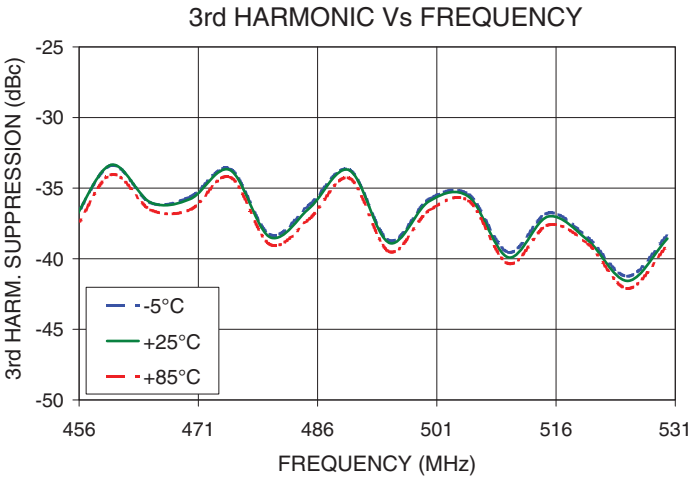
REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @Fcarrier 456MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @Fcarrier 490MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @Fcarrier 530MHz+(n*Freference) (dBc) note 3		
n	-5°C	+25°C	+85°C	-5°C	+25°C	+85°C	-5°C	+25°C	+85°C
-5	-120.82	-123.95	-126.53	-121.40	-124.40	-126.59	-124.32	-125.38	-127.51
-4	-125.82	-125.41	-126.76	-130.42	-127.77	-129.88	-119.89	-119.74	-120.72
-3	-129.26	-126.61	-128.83	-122.62	-123.46	-124.74	-121.46	-121.82	-124.11
-2	-98.19	-98.97	-99.45	-113.87	-114.36	-114.45	-121.40	-121.64	-119.21
-1	-119.17	-120.63	-121.54	-124.11	-123.38	-124.71	-127.00	-126.84	-127.54
0 note 4	-	-	-	-	-	-	-	-	-
+1	-112.77	-112.97	-113.49	-115.55	-116.08	-113.44	-118.63	-119.51	-118.83
+2	-98.58	-99.83	-99.97	-117.63	-118.90	-118.25	-126.49	-128.48	-128.25
+3	-126.22	-127.85	-128.19	-120.35	-119.99	-119.87	-128.30	-127.43	-127.76
+4	-123.85	-122.77	-124.21	-120.04	-119.43	-119.42	-114.57	-115.46	-113.13
+5	-128.35	-125.92	-127.40	-124.17	-123.38	-125.35	-122.84	-121.87	-123.28

Note 3: Reference frequency 15 MHz
Note 4: All spurs are referenced to carrier signal (n=0).

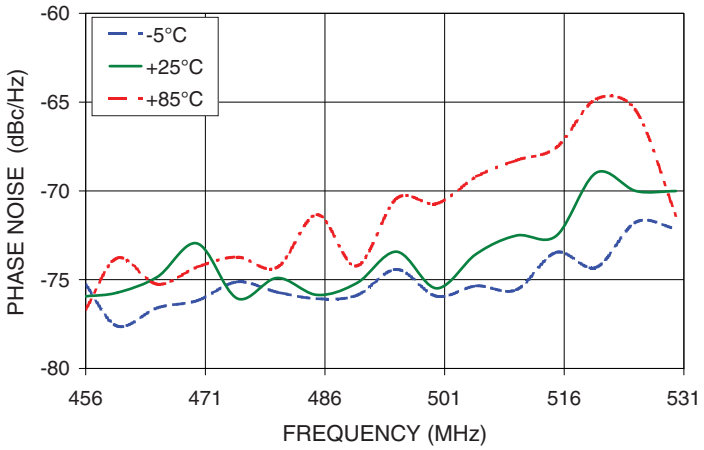
Typical Performance Curves



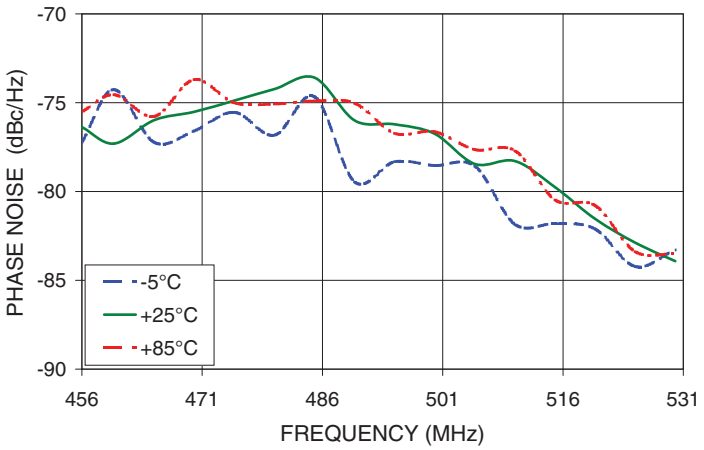




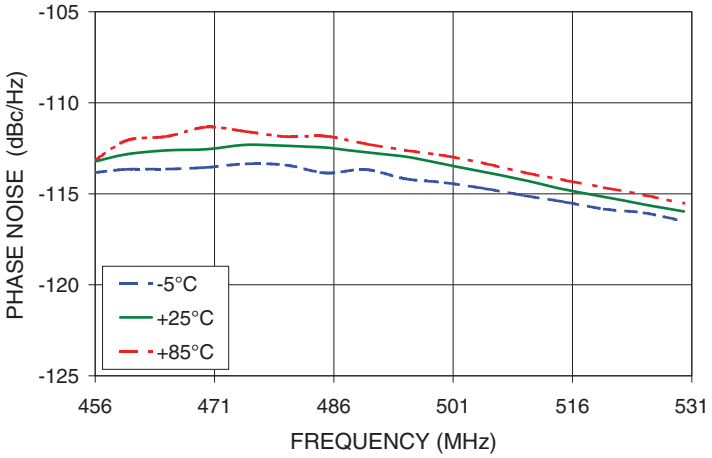
PHASE NOISE @100Hz offset



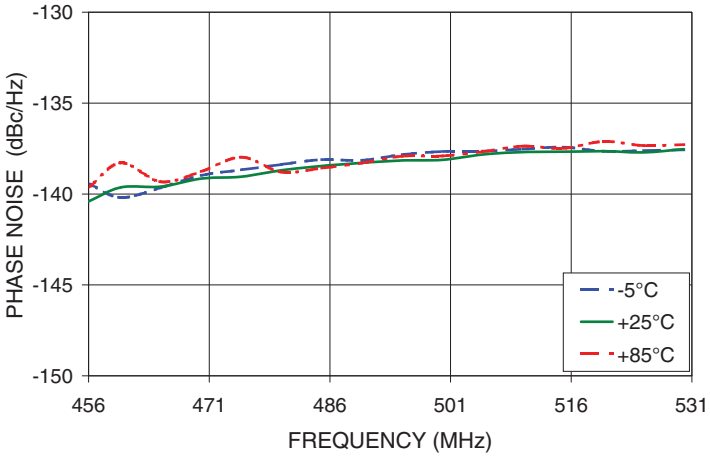
PHASE NOISE @1kHz offset



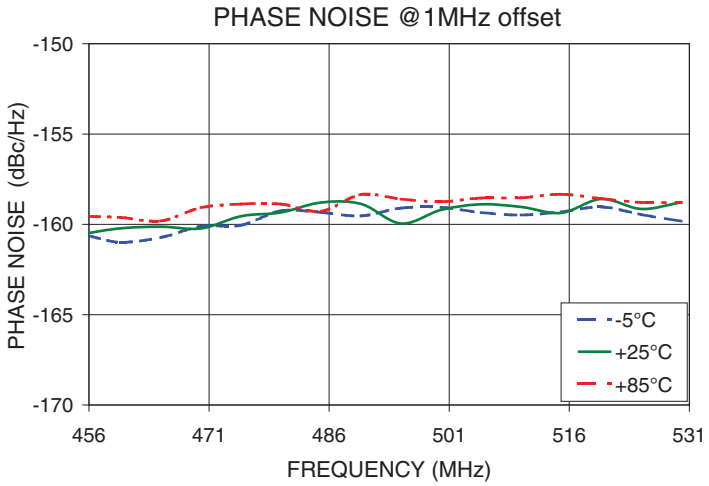
PHASE NOISE @10 kHz offset



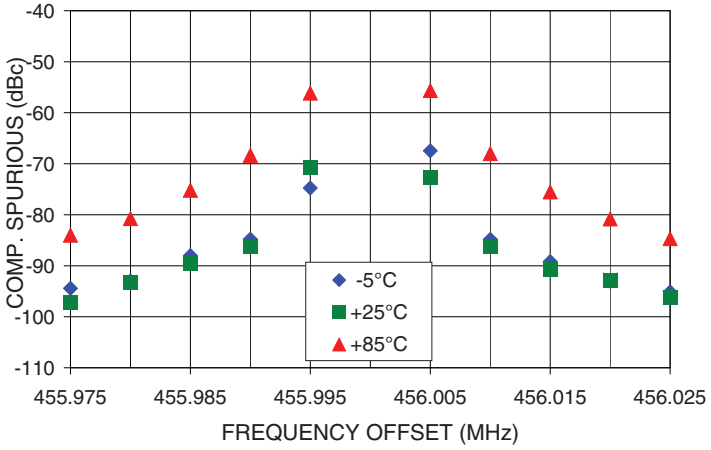
PHASE NOISE @100 kHz offset



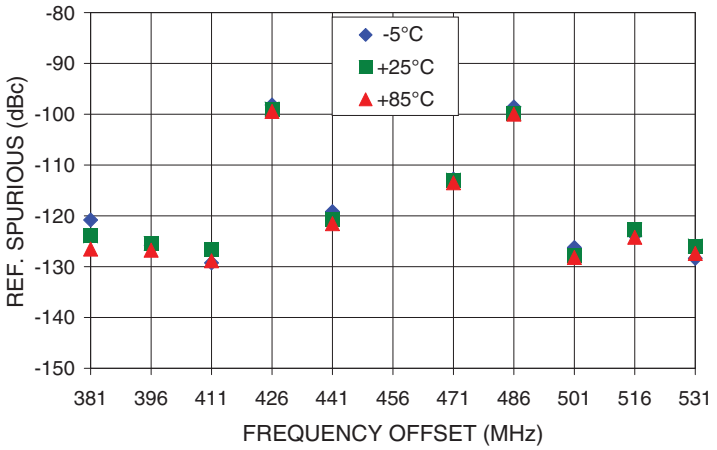
PHASE NOISE @1MHz offset



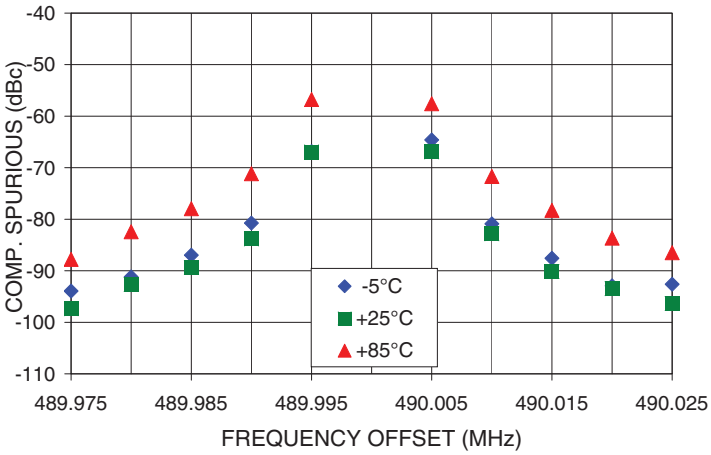
COMPARISON SPURIOUS
Vs FREQ. OFFSET @ Fcar = 456MHz



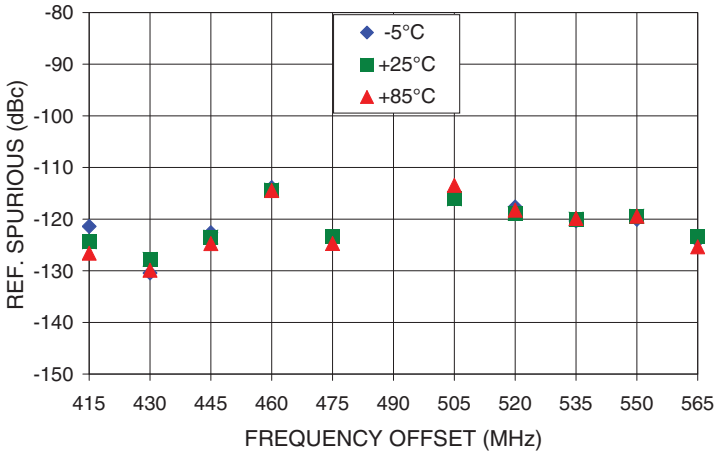
REFERENCE SPURIOUS
Vs FREQ. OFFSET @ Fcar =456MHz



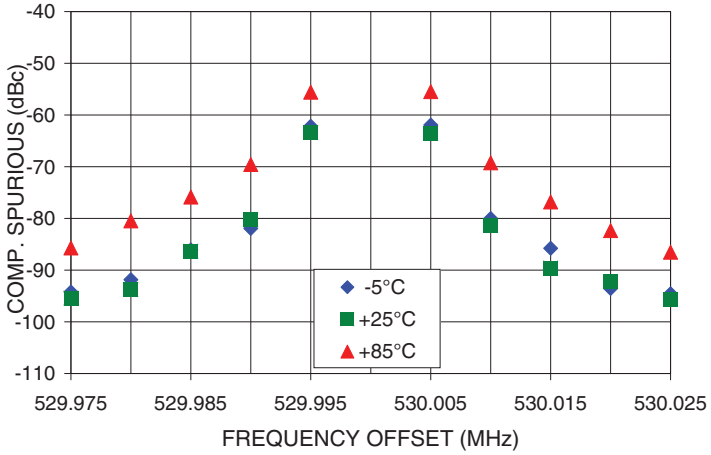
COMPARISON SPURIOUS
Vs FREQ. OFFSET @ Fcar = 490MHz



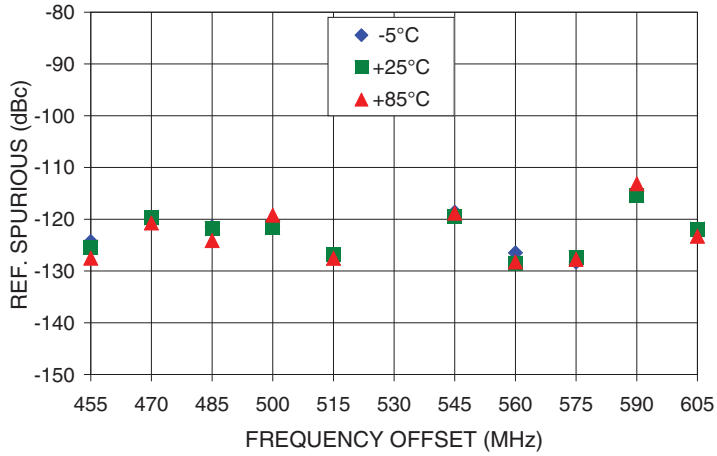
REFERENCE SPURIOUS
Vs FREQ. OFFSET @ Fcar =490MHz



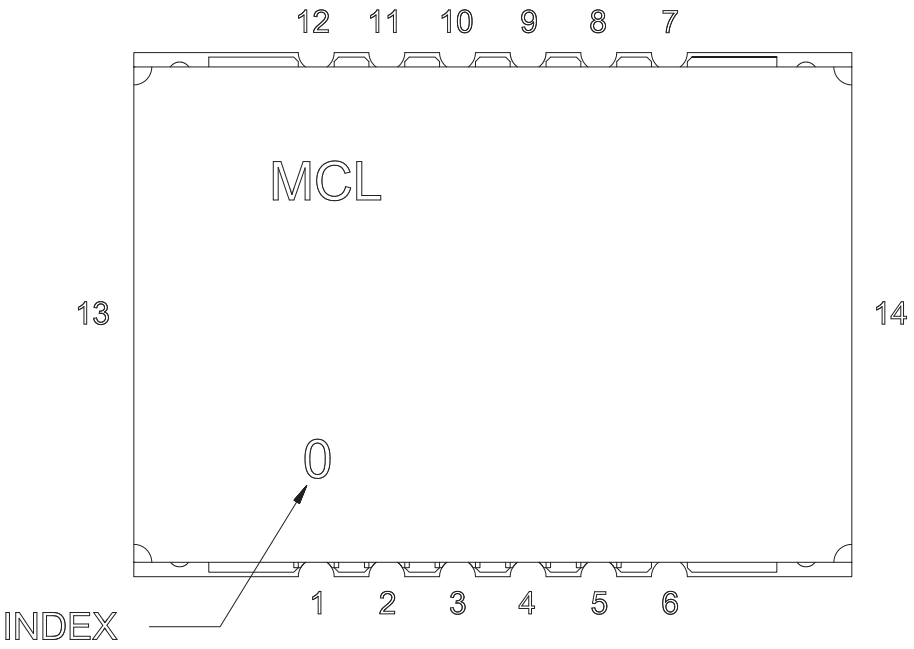
COMPARISON SPURIOUS
Vs FREQ. OFFSET @ Fcar = 530MHz



REFERENCE SPURIOUS
Vs FREQ. OFFSET @ Fcar =530MHz



Pin Configuration

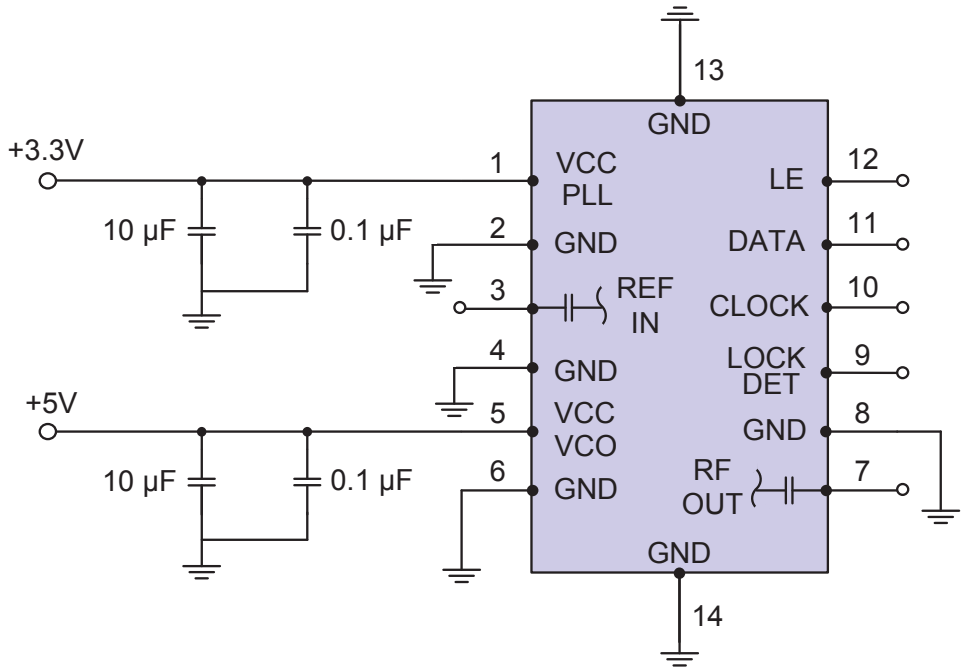


Pin Connection

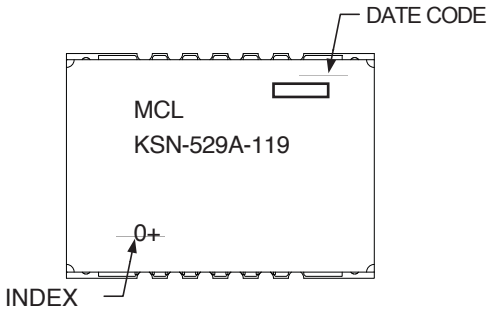
Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.



Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK801

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

Evaluation Board: TB-567-1+

Environment Ratings: ENV03T2