

# Monolithic Amplifier

RAM-8A+

50Ω DC to 1 GHz

#### **FEATURES**

- Exact Footprint Compatible for RAM-8+
- Benefits:
  - Lower Device Voltage, +3.7 V Typ.
  - Lower Power Dissipation in the MMIC
  - May Eliminate Need for RFC
- Wideband, DC to 1 GHz
- Cascadable Ceramic Package
- Internally Matched to 50Ω
- Low Noise Figure, 2.6 dB Typ.
- Excellent Repeatability



Generic photo used for illustration purposes only

CASE STYLE: AF190

### +RoHS Compliant The +Suffix identifies RoHS Compliance. ur website for methodologies and qualifications

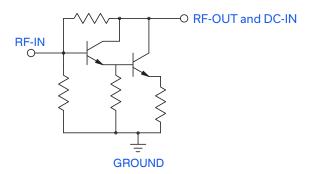
#### **APPLICATIONS**

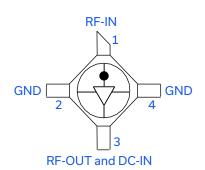
- Cellular
- UHF/VHF
- Communication Systems
- Transmission Receivers

#### **PRODUCT OVERVIEW**

RAM-8A+ (RoHS compliant) is a wideband amplifier offering high dynamic range. It has repeatable performance from lot to lot. It is enclosed in a ceramic surface-mount package. RAM-8A+ uses Darlington configuration and is fabricated using InGaP HBT technology.

#### SIMPLIFIED SCHEMATIC AND PIN DESCRIPTION





Function	Pin Number	Description
RF-IN	1	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
RF-OUT and DC-IN	3	RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit".
GND	2,4	Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.



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#### **ELECTRICAL SPECIFICATIONS AT +25°C AND 36 mA UNLESS NOTED OTHERWISE**

Parameter	Conditions (GHz)	Min.	Тур.	Max.	Units
Frequency Range <sup>1</sup>		DC		1	GHz
Gain	0.1 1	22 <sup>2</sup>	31.5 24.4		dB
Input Return Loss	0.1 - 1		13		dB
Output Return Loss	0.1 - 1		11		dB
Output Power @ 1 dB Compression	1		+12.6		dBm
Output IP3	1		+24.4		dBm
Noise Figure	1		2.6		dB
Recommended Device Operating Current			36		mA
Device Operating Voltage			+3.7		V
Thermal Resistance, Junction-to-Case <sup>3</sup>			145		°C/W

<sup>1.</sup> Guaranteed specification DC-1 GHz. Low frequency cut off determined by external coupling capacitors.

#### **ABSOLUTE MAXIMUM RATINGS**

Parameter	Ratings		
Operating Temperature	-54°C to +100°C		
Storage Temperature	-65°C to +150°C		
Operating Current	65 mA		
Power Dissipation	310 mW		
Input Power	+13 dBm		

Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation.

<sup>2.</sup> Full temperature range.

<sup>3.</sup> Case is defined as ground leads.

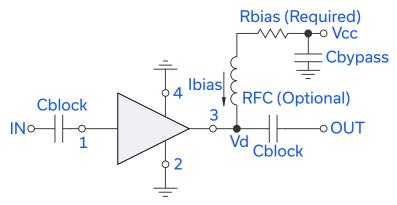


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#### RECOMMENDED APPLICATION CIRCUIT

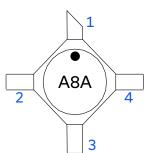


Test Board includes case, connectors, and components (in bold) soldered to PCB

R BIAS⁴				
Vcc	Bias Resistor Value <sup>5</sup>			
7	88.7			
8	118			
9	143			
10	174			
11	200			
12	226			
13	255			
14	280			
15	309			

- 4. When being used as a substitute for MAR-8SM or MSA-0866, the bias resistor values must be changed to the values in this table.
- 5. 1% Resistor values (Ohms) for optimum bias.

#### **PRODUCT MARKING**



 $Markings\ in\ addition\ to\ model\ number\ designation\ may\ appear\ for\ internal\ quality\ control\ purposes.$ 



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DC to 1 GHz 50Ω

### ADDITIONAL DETAILED TECHNICAL INFORMATION IS AVAILABLE ON OUR DASHBOARD. CLICK HERE

	Data Table
Performance Data & Graphs	Swept Graphs
	S-Parameter Data Set (.zip file)
Case Style	AF190 Ceramic surface-mount, 0.083 body diameter
Suggested Layout for PCB Design	PL-254
<b>Evaluation Board</b>	TB-414-8A+
Environmental Ratings	ENV08T6

#### **ESD RATING**

Human Body Model (HBM): Class 2 (2000 V to < 4000 V) in accordance with ANSI/ESD STM 5.1 - 2001 Machine Model (MM): Class M1 (< 500 V) in accordance with ANSI/ESD STM 5.2 - 1999

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
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