Frequency Synthesizer

RSN-795AF-119+

760.6 to 795.4 MHz 50Ω

The Big Deal

- · Fast settling time, 0.03 msec max
- Low phase noise and spurious
- High reliability over temperature changes



CASE STYLE: JG1228

Product Overview

The RSN-795AF-119+ is a Frequency Synthesizer, designed to operate from 760.6 to 795.4 MHz for GSM application. The RSN-795AF-119+ is packaged in a metal case (size of 0.910" x 0.910" x 0.252") to shield against unwanted signals and noise. The RSN-795AF-119+ Frequency Synthesizer can be used as local oscillators in the upconversion and down-conversion sections of wireless receivers and transmitters, with very high reliability over temperature changes due to use of high quality components which are secured to substrate with chip adhesive in addition to solder

Key Features

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -103 dBc/Hz typ. @ 10 kHz offset • Step Size Spurious: -77 dBc typ. • Comparison Spurious: -113 dBc typ. • Reference Spurious: -124 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Fast settling time	Less than 0.03 msec Max within 5.4deg can be used for fast settling applications.
Small size, 0.910" x 0.910" x 0.252"	The small size enables the RSN-795AF-119+ to be used in compact designs.

Notes

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Frequency Synthesizer

RSN-795AF-119+

760.6 to 795.4 MHz 50Ω

Features

- Fractional N synthesizer
- Fast settling time, 0.03 msec max
- · Low phase noise and spurious
- High reliability over temperature changes
- Low operating voltage (VCC VCO=+5.5V, VCC PLL=+3.3V VCC CP=+5.0V)
- Small size 0.910" x 0.910" x 0.252"



CASE STYLE: JG1228

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

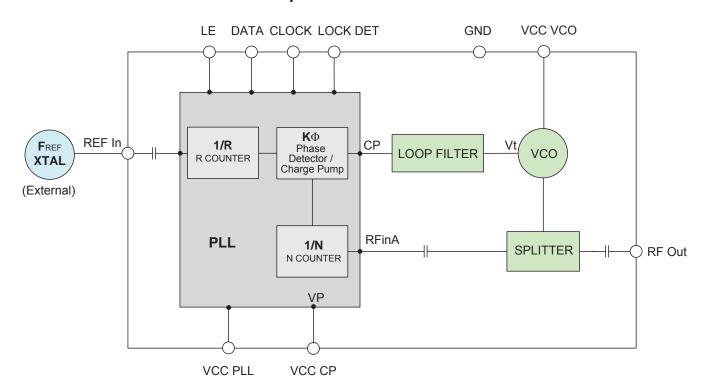
Applications

GSM

General Description

The RSN-795AF-119+ is a Frequency Synthesizer, designed to operate from 760.6 to 795.4 MHz for GSM application. The RSN-795AF-119+ is packaged in a metal case (size of 0.910" x 0.910" x 0.252") to shield against unwanted signals and noise. The RSN-795AF-119+ Frequency Synthesizer can be used as local oscillators in the upconversion and down-conversion sections of wireless receivers and transmitters. with very high reliability over temperature changes due to use of high quality components which are secured to substrate with chip adhesive in addition to solder.

Simplified Schematic



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REV. B M151108 EDR-8430/7F1 RSN-795AF-119+ Category-F8 RAV 151013 Page 2 of 13

Frequency Synthesizer

Electrical Specifications (over operating temperature -40°C to +85°C)

Parameters		Test Conditions	Min.	Тур.	Max.	Units			
Frequency Range		-	760.6	-	795.4	MHz			
Step Size		-	-	200	-	kHz			
Comparison Frequency	-	-	13	-	MHz				
Settling Time		Within ± 5.4 deg	ithin ± 5.4 deg - 0.02		0.03	mSec			
Output Power		-	+2	+5	+8	dBm			
		@ 100 Hz offset							
		@ 1 kHz offset	-	-103	-96	1			
SSB Phase Noise		@ 10 kHz offset	-	-103	-96	dBc/Hz			
		@ 100 kHz offset	-	-108	-104	1			
I		@ 1 MHz offset	-	-154	-148	-			
Step Size Spurious Suppression	1	Step Size 200 kHz	-	-77	-55				
0.5 Step Size Spurious Suppres	sion	0.5 Step Size 100 kHz	-	-85	-65	1			
Reference Spurious Suppressio	n	Ref. Freq. 52 MHz	-	-124	-90	40-			
Comparison Spurious Suppress	ion	Comp. Freq. 13 MHz	-	-113	-90	dBc			
Non - Harmonic Spurious Suppr	ession	· · · -	-	-90	-	1			
Harmonic Suppression		-	-	-37	-31	1			
VCO Supply Voltage		+5.50	+5.20	+5.50	+5.80				
PLL Supply Voltage		+3.30	+3.15	+3.30	+3.45	l v			
CP Supply Voltage	+5.00	+4.80	+5.00	+5.20	1				
VCO Supply Current	-	-	51	65					
PLL Supply Current	-	-	23	30	mA mA				
CP Supply Current		-	41		50				
	Frequency	52 (square wave)	-	52	-	MHz			
Reference Input	Amplitude	1	-	1	-	V _{p-P}			
(External)	Input impedance	-	-	100	-	ΚΩ			
1	Phase Noise @ 1 kHz offset	-	-	-135	-	dBc/Hz			
RF Output port Impedance	·	-	-	50	-	Ω			
	Input high voltage	-	2.80	-	-	V			
Input Logic Level	Input low voltage	-	-	-	0.60	V			
Birital Land Barray	Locked	-	2.75	-	3.45	V			
Digital Lock Detect	Unlocked	-	-	-	0.40	V			
Frequency Synthesizer PLL	•	-	ADF4193			•			
PLL Programming (Note*)		-	3-wire serial	3.3V CMOS					
	R0_Register	-		01000000001	100000 (LSB	5)			
Register Map @ 795.4 MHz	R1_Register	-		00100000010					
	R2_Register	-	(MSB) 1110			,			
	R3_Register	-	(MSB) 1111						
	R4_Register	-		01110010100	(LSB)				
	R5_Register	-	(MSB) 101 (,,				
	R6_Register	-			0 (LSB)				
	R7_Register	_	(MSB) 1001000000001110 (LSB) (MSB) 111 (LSB)						

Note*: Tested with GSM900RX_13M_PHASE CODE (GSM900/GSM850 RX, version 1.0) from "Analog Devices" recommendation for ADF4193 PLL.

Download Phase Code file

Absolute Maximum Ratings

•	
Parameters	Ratings
VCO Supply Voltage	+6.3V
PLL Supply Voltage	+3.6V
CP Supply Voltage	+5.8V
CP Supply Voltage to PLL Supply Voltage	-0.3V to 5.8V
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded

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Typical Performance Data

FREQUENCY	POV	POWER OUTPUT			/CO CURRENT PLL CUF			L CURE	ENT CP CURENT			
(MHz)		(dBm)			(mA)		(mA)			(mA)		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
760.6	6.12	5.80	5.20	48.69	51.66	53.19	20.74	22.94	25.95	40.86	41.34	41.93
765.0	6.01	5.68	5.12	48.65	51.66	53.19	20.78	23.16	25.97	40.65	41.15	41.75
770.0	5.86	5.53	5.02	48.60	51.66	53.20	20.71	22.98	26.02	40.42	40.93	41.55
775.0	5.70	5.38	4.90	48.57	51.65	53.19	20.75	22.91	26.13	40.20	40.72	41.35
780.0	5.47	5.22	4.77	49.18	51.64	53.18	20.75	23.56	25.97	39.96	40.49	41.13
785.0	5.27	5.06	4.63	48.83	51.62	53.18	20.88	23.17	25.95	39.73	40.28	40.93
790.0	5.07	4.91	4.49	48.71	51.61	53.17	20.48	23.01	25.95	39.49	40.06	40.73
795.0	4.88	4.76	4.34	48.64	51.59	53.17	20.85	23.19	26.05	39.26	39.84	40.53
795.4	4.88	4.75	4.33	48.62	51.60	53.17	20.85	23.11	26.11	39.24	39.82	40.51

FREQUENCY	HARMONICS (dBc)						
(MHz)		F2		F3			
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	
760.6	-35.09	-36.14	-36.80	-42.82	-47.12	-51.89	
765.0	-35.52	-36.75	-37.26	-42.84	-47.37	-51.98	
770.0	-36.27	-37.76	-38.33	-42.41	-46.85	-51.73	
775.0	-36.66	-38.51	-39.33	-42.90	-47.34	-52.39	
780.0	-36.38	-38.49	-39.67	-43.88	-48.50	-53.42	
785.0	-36.25	-38.28	-39.60	-44.65	-49.37	-53.97	
790.0	-36.46	-38.43	-39.77	-45.32	-50.17	-54.32	
795.0	-36.85	-38.79	-40.10	-45.06	-49.60	-53.14	
795.4	-36.90	-38.77	-40.13	-45.04	-49.49	-53.05	

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FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS								
(MHz)			+25°C						
. ,	100Hz	1kHz	10kHz	100kHz	1MHz				
760.6	-88.42	-103.06	-103.97	-108.27	-155.16				
765.0	-87.91	-104.10	-104.19	-108.28	-155.12				
770.0	-87.47	-103.44	-103.98	-108.48	-155.08				
775.0	-87.17	-104.44	-103.98	-108.60	-154.91				
780.0	-88.32	-102.45	-103.62	-108.96	-155.16				
785.0	-87.85	-104.32	-103.70	-108.86	-154.57				
790.0	-88.11	-103.14	-103.71	-109.07	-154.61				
795.0	-87.11	-103.02	-103.44	-109.13	-152.98				
795.4	-88.63	-103.58	-103.71	-109.09	-152.50				

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS								
(MHz)	-45°C								
, ,	100Hz	1kHz	10kHz	100kHz	1MHz				
760.6	-85.94	-103.21	-104.01	-109.11	-152.04				
765.0	-86.49	-103.54	-104.24	-109.08	-153.40				
770.0	-87.22	-103.52	-104.07	-109.36	-155.27				
775.0	-86.87	-102.45	-104.06	-109.51	-156.13				
780.0	-85.55	-103.06	-103.75	-109.61	-156.84				
785.0	-86.05	-102.59	-104.09	-109.63	-156.59				
790.0	-87.40	-103.65	-104.00	-109.78	-155.23				
795.0	-87.08	-102.70	-103.81	-109.90	-155.54				
795.4	-87.83	-102.60	-103.85	-109.86	-155.23				

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS						
(MHz)			+85°C				
, ,	100Hz	1kHz	10kHz	100kHz	1MHz		
760.6	-82.80	-105.53	-103.72	-108.04	-152.73		
765.0	-81.65	-104.04	-103.47	-108.00	-151.50		
770.0	-83.22	-104.53	-103.56	-108.13	-152.87		
775.0	-83.31	-104.63	-103.46	-108.21	-153.17		
780.0	-81.20	-103.50	-103.40	-108.48	-153.54		
785.0	-82.03	-104.89	-103.24	-108.42	-153.38		
790.0	-82.17	-103.19	-103.25	-108.67	-153.28		
795.0	-82.05	-103.91	-103.34	-108.71	-153.29		
795.4	-82.11	-104.97	-103.00	-108.68	-153.27		

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COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @Fcarrier 760.6MHz+(n*Fcomparison) (dBc) note 1				ARISON SPU @Fcarrier z+(n*Fcomp (dBc) no	arison)	COMPARISON SPURIOUS @Fcarrier 795.4MHz+(n*Fcomparison) (dBc) note 1		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-133.22	-125.90	-132.22	-132.98	-122.00	-124.09	-121.55	-131.28	-125.16
-4	-126.73	-119.73	-126.25	-126.42	-120.44	-123.75	-122.04	-125.76	-127.47
-3	-124.72	-116.38	-126.86	-123.23	-119.58	-125.79	-117.45	-120.96	-125.44
-2	-124.48	-113.96	-123.97	-121.05	-119.42	-118.92	-114.19	-121.30	-118.83
-1	-116.28	-109.12	-117.42	-112.21	-114.86	-110.78	-110.01	-116.40	-112.77
o ^{note 2}	-	-	-	-	-	-	-	-	-
+1	-114.67	-109.91	-117.58	-111.43	-115.72	-113.02	-110.53	-114.52	-112.69
+2	-123.77	-116.65	-126.61	-119.55	-120.31	-122.41	-117.10	-120.16	-121.64
+3	-130.74	-122.37	-131.72	-124.84	-124.50	-126.47	-119.91	-124.23	-122.41
+4	-129.63	-130.50	-133.13	-127.25	-126.31	-130.95	-122.90	-126.14	-125.27
+5	-135.29	-133.94	-134.43	-135.73	-131.04	-132.01	-131.35	-127.27	-129.09

Note 1: Comparison frequency 13 MHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @ Fcarrier 760.6MHz+(n*Freference) (dBc) note 3				RENCE SPU @Fcarrier Hz+(n*Frefe (dBc) no	rence)	REFERENCE SPURIOUS @Fcarrier 795.4MHz+(n*Freference) (dBc) note 3		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-131.15	-132.08	-127.61	-128.24	-123.64	-130.66	-128.92	-125.45	-125.88
-4	-129.49	-123.98	-129.35	-124.53	-125.19	-129.59	-120.49	-123.08	-129.58
-3	-123.03	-132.54	-129.58	-122.04	-122.88	-125.09	-121.75	-122.47	-131.23
-2	-122.16	-130.91	-129.54	-119.09	-131.36	-131.11	-120.01	-122.13	-127.11
-1	-127.47	-118.94	-126.30	-124.07	-120.55	-123.12	-119.81	-124.48	-126.82
o ^{note 4}	-	-	-	-	-	-	-	-	-
+1	-129.88	-127.69	-137.97	-127.36	-128.68	-130.97	-121.16	-125.65	-126.57
+2	-123.01	-120.05	-123.14	-122.78	-120.79	-120.43	-124.49	-119.26	-123.28
+3	-136.44	-132.57	-128.71	-131.52	-133.82	-131.97	-133.15	-132.49	-131.19
+4	-128.63	-125.76	-134.01	-131.18	-125.71	-133.02	-128.46	-123.73	-131.17
+5	-126.07	-123.19	-130.42	-127.10	-126.16	-128.83	-126.65	-124.29	-128.96

Note 3: Reference frequency 52 MHz

Note 4: All spurs are referenced to carrier signal (n=0).

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STEP SIZE SPURIOUS ORDER	0.5 STEP SIZE & STEP SIZE SPURIOUS @Fcarrier 760.6MHz+(n*Fstep size) (dBc) note 5			SPUF	P SIZE & ST RIOUS @Fc Hz+(n*Fster (dBc) no	arrier o size)	0.5 STEP SIZE & STEP SIZE SPURIOUS @ Fcarrier 795.4MHz+(n*Fstep size) (dBc) note 5		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5.0	-120.41	-105.68	-104.96	-110.84	-113.41	-102.76	-111.53	-107.39	-110.29
-4.5	-119.70	-119.12	-118.35	-119.97	-119.54	-120.94	-119.54	-121.54	-119.99
-4.0	-97.52	-97.55	-115.96	-112.43	-102.64	-106.51	-113.00	-109.24	-108.43
-3.5	-119.71	-118.63	-119.00	-117.56	-119.60	-119.92	-117.28	-117.99	-121.77
-3.0	-118.14	-103.87	-93.87	-92.33	-89.77	-102.50	-100.17	-96.06	-98.76
-2.5	-113.99	-113.67	-115.68	-113.29	-114.79	-113.52	-115.38	-111.99	-115.51
-2.0	-85.40	-93.99	-85.19	-92.53	-87.55	-102.81	-84.23	-92.60	-93.33
-1.5	-105.05	-105.22	-103.84	-101.31	-108.82	-108.19	-110.94	-104.17	-107.64
-1.0	-70.19	-84.39	-81.22	-79.46	-80.93	-82.63	-70.27	-76.37	-76.71
-0.5 0 ^{note} 6	-87.76 -	-83.58 -	-84.92 -	-83.27 -	-85.47 -	-87.39 -	-85.60 -	-84.13 -	-86.44 -
+0.5	-86.05	-88.14	-88.90	-87.27	-87.24	-84.12	-88.88	-88.13	-85.29
+1.0	-70.39	-85.10	-82.54	-79.67	-80.67	-82.28	-70.07	-76.22	-76.77
+1.5	-107.12	-105.81	-105.24	-108.22	-106.63	-107.01	-103.85	-105.71	-107.02
+2.0	-85.49	-94.22	-85.01	-93.33	-87.64	-101.97	-84.11	-92.70	-93.86
+2.5	-113.70	-116.46	-114.62	-108.51	-105.91	-105.56	-116.29	-111.60	-117.13
+3.0	-116.92	-104.84	-93.46	-92.35	-89.55	-87.61	-99.71	-96.19	-97.89
+3.5	-118.83	-119.35	-115.68	-119.79	-119.82	-120.03	-119.64	-115.74	-122.02
+4.0	-98.06	-98.68	-117.33	-114.13	-103.35	-104.93	-114.94	-113.86	-107.65
+4.5	-121.26	-120.10	-121.62	-117.91	-123.31	-122.82	-123.11	-122.76	-121.67
+5.0	-122.96	-105.40	-105.56	-111.74	-113.06	-112.75	-109.69	-106.89	-106.98

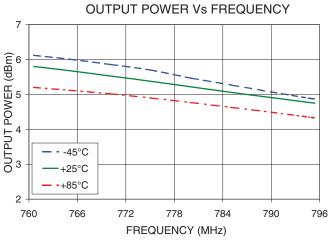
Note 5: Step size 200 kHz

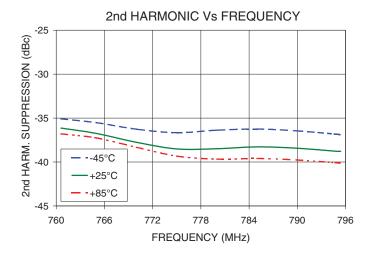
Note 6: All spurs are referenced to carrier signal (n=0).

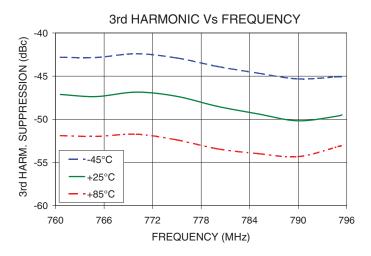
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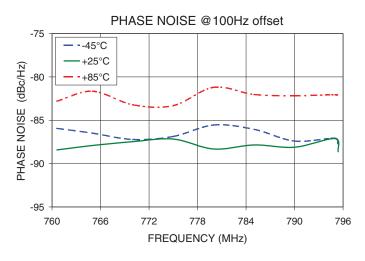
Typical Performance Curves

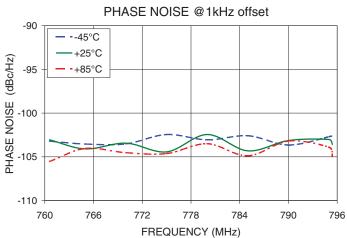


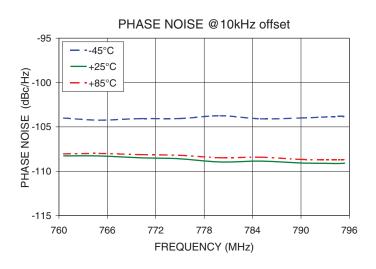


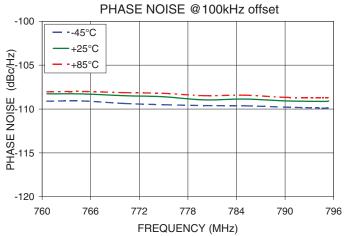


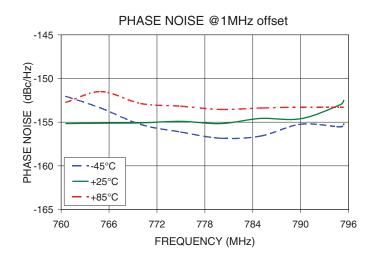
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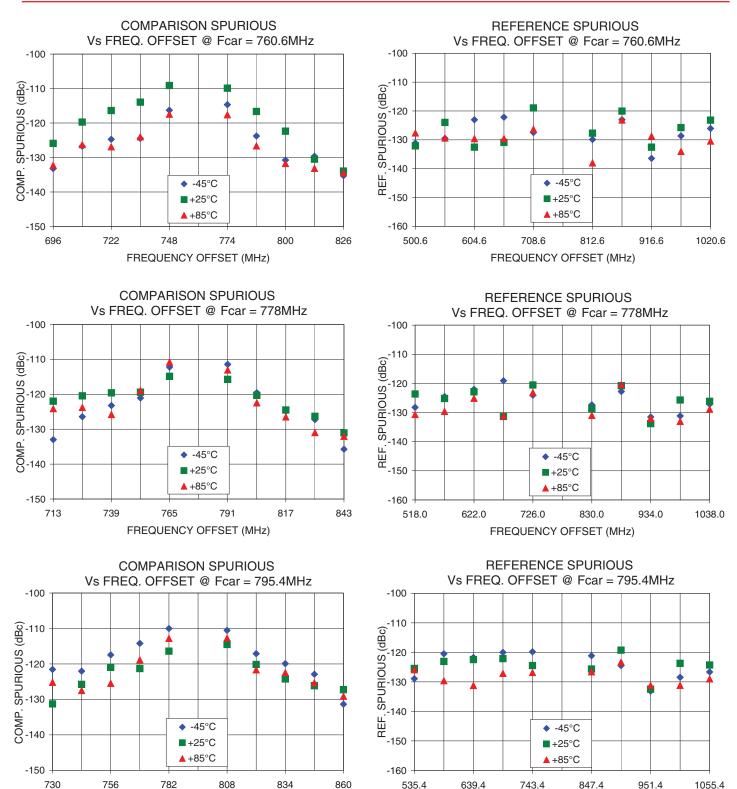








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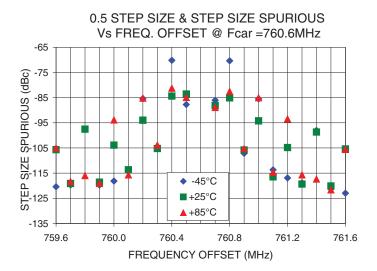


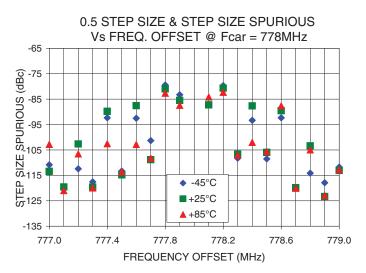
FREQUENCY OFFSET (MHz)

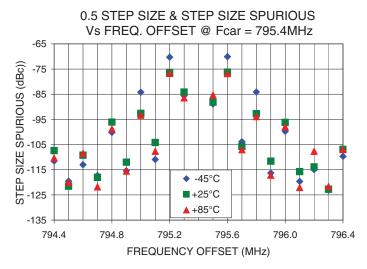
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FREQUENCY OFFSET (MHz)



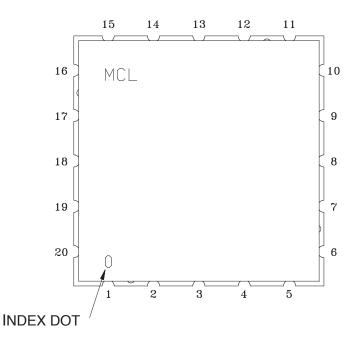




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Pin Configuration

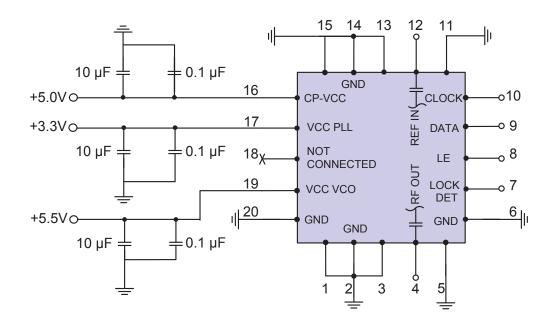


Pin Connection

Pin Number	Function				
1	GND				
2	GND				
3	GND				
4	RF OUT				
5	GND				
6	GND				
7	LOCK DET				
8	LE				
9	DATA				
10	CLOCK				
11	GND				
12	REF IN				
13	GND				
14	GND				
15	GND				
16	VCC CP				
17	VCC PLL				
18	Not Connected				
19	VCC VCO				
20	GND				

Recommended Application Circuit

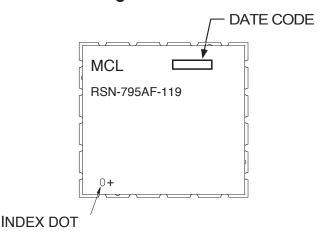
Note: REF IN and RF OUT ports are internally AC coupled.



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Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: JG1228

Tape & Reel: TR-F99

Suggested Layout for PCB Design: PL-319

Evaluation Board: TB-554+

Environment Ratings: ENV65T2

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