Surface Mount Thin-Film Filters

 50Ω DC to 40 GHz

The Big Deal

- Low passband insertion loss
- High rejection
- · Good power handling
- Temperature stability -55°C to 125°C
- High repeatability
- · RoHS complaint
- Small size



Product Overview

Mini-Circuits' Surface Mount Thin-Film filters offer low insertion loss and high rejection realized via Thin-Film on Alumina substrate, using a sputtering process that can guarantee a enhanced Q and repeatable performance.

Low pass, high pass and bandpass surface mount thin-film designs can be realized with this technology. Using thin-film manufacturing, we can guarantee repeatability on large batches of filters. Thin-film filters are small in size with high-quality, precise machining for applications where size is critical.

Key Features

Feature	Advantages
Low insertion loss	High Q material and sputtering process results in lower insertion loss, better SNR is obtained.
Fast roll-off (steeper skirts)	High selectivity results in better adjacent channel rejection and dynamic range
Wider stopband	Wide spur-free stopband results in better adjacent channel rejection and dynamic range
Temperature stability	Very minimal change in electrical performance across temperature makes these filters suitable for a wide range of operating conditions.
Small Size	Various design techniques are employed to realize small size.

Notes
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B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp



Bandpass Filter

 50Ω 6300 to 9200 MHz

ABF-7R75G+



Generic photo used for illustration purposes only

CASE STYLE: UC2731

Features

- · Low passband insertion loss of 1.5 dB typical
- · High rejection of 50 dB typical
- 20 dB rejection up to 30000 MHz
- Small size

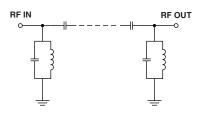
Applications

- 5G
- · Wireless communication systems

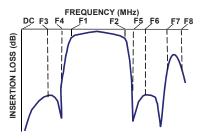
- Test and measurement

- · Satellite communication
- · Military and Defense

Functional Schematic



Typical Frequency Response



+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Electrical Specifications(1) at 25°C

Parameter		F#	Frequency (MHz)	Min.	Тур.	Max.	Unit
Pass Band	Insertion Loss	F1-F2	6300 - 9200	_	1.5	2.5	dB
Pass Ballu	Return Loss	F1-F2	6300 - 9200	_	9.0	_	dB
Stop Band, Lower	ran luccution loca	DC-F3	DC - 3000	40	55	_	dB
Stop Ballu, Lower	Insertion Loss	F3-F4	3000 - 4400	20	40	_	dB
		F5-F6	11000 - 13000	20	35	_	dB
Stop Band, Upper	Insertion Loss	F6-F7	13000 - 16000	40	60	_	dB
		F7-F8	16000 - 30000		20	_	dB

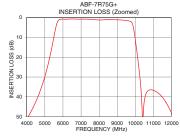
1. Measured on Mini-Circuits Characterization Test Board TB-ABF-7R75G+ with feedline losses removed by normalization of S12 and S21 traces to mesurement of TB thru-line.

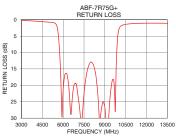
Maximum Ratings							
Operating Temperature	-55°C to 125°C						
Storage Temperature	-55°C to 125°C						
RF Power Input	1W Max. @ 25°C						

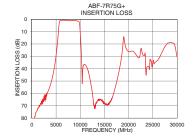
Permanent damage may occur if any of these limits are exceeded.

Typical Performance Data at 25°C

Frequency (MHz)	Insertion Loss (dB)	Return Loss (dB)
10	99.40	0.07
500	80.74	0.25
1000	74.70	0.30
3000	60.45	0.22
4400	44.42	0.56
5000	29.73	0.74
5250	20.82	0.83
5650	3.37	4.53
6300	0.76	17.16
6500	0.73	26.41
7750	1.06	15.24
8000	1.21	12.91
9200	1.21	42.18
9950	7.87	4.17
10300	34.43	1.61
11000	36.66	1.18
12000	47.99	1.05
13000	70.62	1.05
16000	67.83	1.27
30000	30.58	2.23





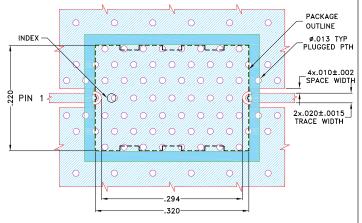


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Pad Connections

RF IN	1
RF OUT	2
GROUND	3

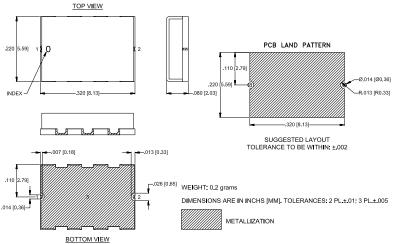
Demo Board MCL P/N: TB-ABF-7R75G+ Suggested PCB Layout (PL-652)



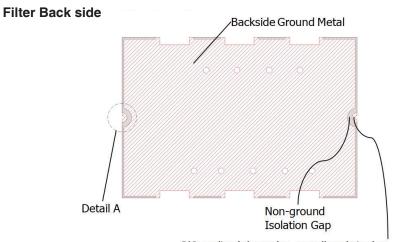
NOTES:

- 1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS (RO4350B) WITH DIELECTRIC THICKNESS .010±.0010. COPPER: 1/2 Oz. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
- 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- DENOTES PCB COPPER PATTERN WITH SMOBC (SOLDER MASK OVER BARE COPPER) DENOTES PCB COPPER PATTERN FREE OF SOLDERMASK

Outline Drawing



Recommendations of PCB pattern at customer board



I/O realized through a castellated via that mates directly to I/O pad on top of test board PCB

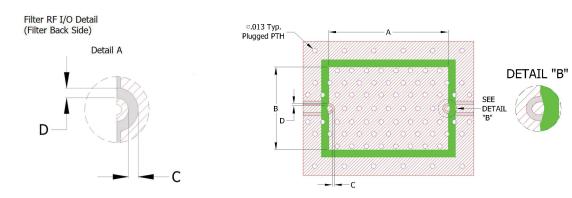
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PCB Pattern Recommendations

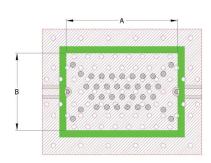


- 1) Customer PCB's ground pattern length (dimension A) can be similar to filter length.
- 2) Customer PCB's ground pattern width (dimension B) can be similar filter width.
- 3) Dimensions C and D on Filter RF I/O detail and Customer PCB pattern can be closely match. The dimensions of C and D on the Customer PCB pattern can be slightly larger to account for component alignment tolerance (ground metal can be pulled back from RF I/O trace).
- Recommend to use Solder mask at Customer PCB at outer area of filter pattern/ footprint with a clearance of about 1.25mil at each side. (Tighter registration tolerance required for solder mask)
- 5) Recommended to use Solder mask at I/O of Customer PCB as per above diagram (refer detail B).

Comments on component handling and solder attach

- 1) Avoid using soldering iron directly to the ceramic filter. This would lead to development of crack in the component due to thermal shock.
- 2) Vacuum pick-up tool or plastic tweezers are recommended for handling the components. Extra care should be taken not to scratch the filter or metal area.
- 3) Use 2-3 mil thickness stencil plate and screen print the solder. Refer below picture for recommended stencil pattern to get the best solder attachment.

Stencil opening drawing



Solder location after screen print

- 4) Plugged ground vias in the PWB will improve attachment consistency.
- 5) Recommended to have a similar or closer test board material and thickness (refer Mini-Circuits evaluation board for details) to minimize the CTE over the temperature range.

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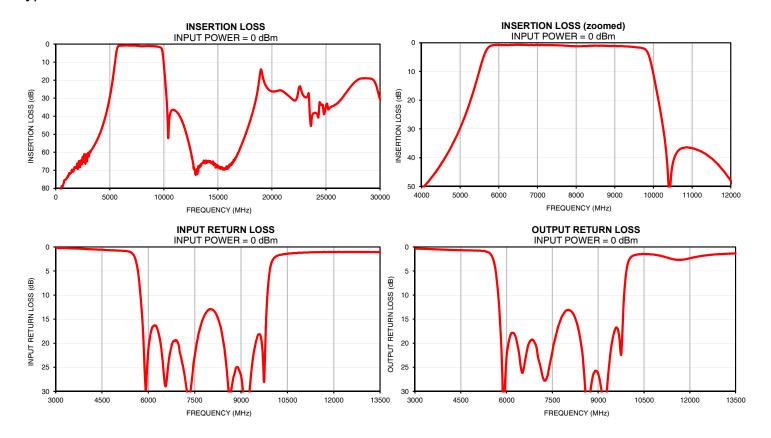
Typical Performance Data

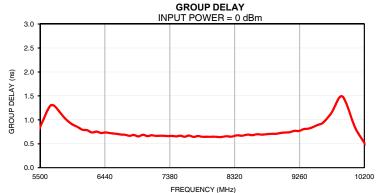
	Loss	Loss
(dB)	(dB)	(dB)
99.40	0.07	0.15
94.41		0.18
		0.13
		0.34
		0.39
		0.30
	0.21	0.29
	0.21	0.30
		0.32
		0.37
		0.42
		0.47
		0.52
		0.58
		0.62
		0.62
		0.68
		0.66
		0.74 0.85
		1.09
		22.76
		18.74
		26.00
		21.61
		27.29
		15.49
		13.10
		23.28
		26.35
		35.04
		18.30
		3.98
		2.87
		1.55
		1.78
-		2.61
		2.35
		1.77
70.62	1.05	1.49
66.26	1.06	1.35
64.89		1.26
		1.22
67.78	1.19	1.20
69.08	1.25	1.21
67.83	1.27	1.23
64.46	1.28	1.26
58.50	1.29	1.30
51.05	1.33	1.35
42.27	1.41	1.39
31.12	1.63	1.47
14.08	3.59	3.17
23.82	1.80	1.46
26.19	1.90	1.39
25.98	2.69	1.39
26.15	4.97	1.44
		1.46
		1.31
		3.04
		3.48
_	99.40 94.41 94.45 80.74 74.70 66.28 62.46 61.28 60.45 60.00 57.94 55.97 53.60 50.99 47.95 44.42 40.29 35.36 29.73 22.77 14.38 0.79 0.76 0.73 0.78 0.81 1.06 1.21 1.13 1.21 1.49 7.87 11.00 34.43 36.66 40.22 47.99 59.44 70.62 66.26 64.89 66.46 67.78 69.08 67.83 64.46 58.50 51.05 42.27 31.12 14.08 23.82 26.19 25.98	99.40 94.41 94.15 0.05 80.74 0.25 74.70 0.30 66.28 0.27 62.46 0.21 61.28 0.21 60.45 0.22 60.00 0.25 57.94 0.27 55.97 0.31 53.60 0.38 50.99 0.42 47.95 0.50 44.42 0.56 40.29 0.64 35.36 0.69 29.73 0.74 22.77 0.81 14.38 1.00 0.79 21.36 0.76 0.76 0.73 0.74 22.77 1.06 15.24 1.21 1.21 1.21 1.31 26.75 1.21 1.49 1.13 26.75 1.21 1.49 1.13 26.75 1.21 1.49 1.11 6.46 6.46 1.18 40.22 1.09 47.99 1.05 59.94 1.04 70.62 1.05 66.26 1.06 64.89 1.11 66.46 1.18 40.22 1.09 47.99 1.05 59.94 1.04 70.62 1.05 66.26 1.06 64.89 1.11 66.46 1.14 67.78 1.19 69.08 1.25 67.83 1.27 64.46 1.28 58.50 1.29 51.05 1.33 42.27 1.41 31.12 1.63 14.08 3.59 23.82 1.80 26.19 1.90 25.98 2.69 26.15 4.97 28.55 3.82 34.05 1.68 19.58 3.68

FREQ.	Group Delay
(MHz)	(ns)
6300	0.75
6320	0.76
6340	0.75
6380	0.72
6400 6450	0.72 0.73
6500	0.72
6550	0.71
6600	0.70
6650	0.69
6700	0.69
6750 6800	0.68 0.67
6850	0.68
6900	0.66
6950	0.66
7000	0.68
7050	0.65
7100	0.67
7150 7200	0.67 0.66
7250 7250	0.66
7300	0.66
7350	0.66
7400	0.66
7450	0.66
7500 7550	0.66 0.66
7600	0.64
7650	0.67
7700	0.65
7750	0.64
7800 7850	0.65 0.64
7900	0.64
7950	0.64
8000	0.64
8050	0.64
8100	0.64
8150 8200	0.64 0.66
8250	0.65
8300	0.66
8350	0.67
8400	0.67
8450	0.67
8500 8550	0.69 0.68
8600	0.68
8650	0.70
8700	0.69
8750	0.69
8800 8850	0.70 0.70
8900	0.70
8950	0.71
9000	0.73
9050	0.73
9100	0.73
9200	0.77



Typical Performance Curves





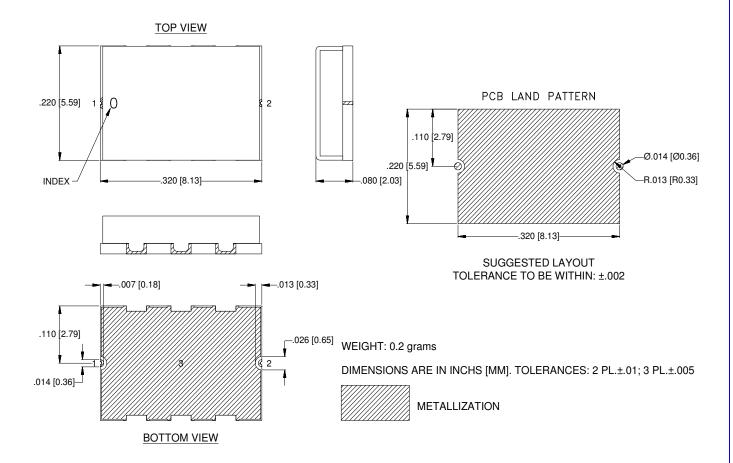


Case Style



Outline Dimensions

UC2731



Notes:

- 1. Case material: Gold over Nickel over Annealed Stainless Steel.
- 2. Base: Ceramic
- 3. Termination finish: as shown below or indicated on Data Sheet.

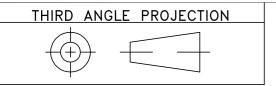
For RoHS Case Styles: Gold over Nickel plate. All models, (+) suffix.





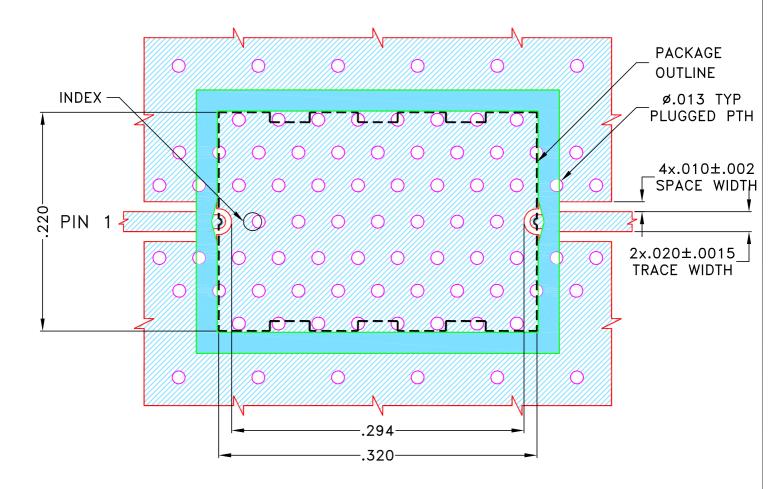
P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site

The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com



	REVISIONS									
REV	REV ECN No. DESCRIPTION DATE DR AUT							AUTH		
OR	ECO-007104		NEW	RELEAS	E		MAR	21	DDR	VC
Α	ECO-010633	UPDATED	AS PER	CURRENT	TEST	BOARD	NOV	21	DDR	VC

SUGGESTED MOUNTING CONFIGURATION FOR UC2731 CASE STYLE



NOTES:

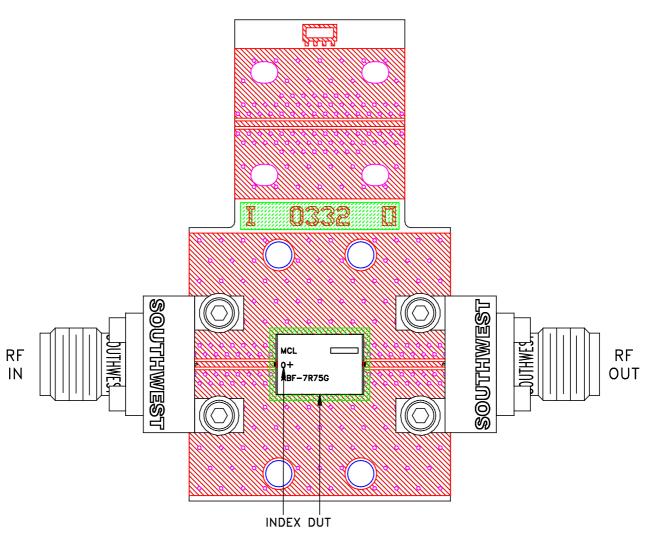
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- 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- DENOTES PCB COPPER PATTERN WITH SMOBC (SOLDER MASK OVER BARE COPPER)

 DENOTES PCB COPPER PATTERN FREE OF SOLDERMASK

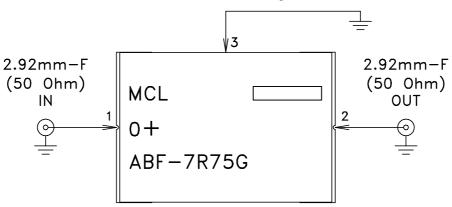
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DIMENSIONS ARE IN INCHES	DRAWN	DDR	29 MAR 21	1		Mini	l — (ircu	its -	13 Neptu	ne Ave	nue
TOLERANCES ON: 2 PL DECIMALS ±	CHECKED	RR	29 MAR 21			Γ				Brooklyn	NY 11	235
3 PL DECIMALS ± .005 ANGLES ±	APPROVED	NN	29 MAR 21									
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ASHFFTA1 DWG REV:A DATE:01/12/95				ษย	3-PL-652		10:1		1	Uľ	T	

Evaluation Board and Circuit





Schematic diagram



Notes:

1. PCB Material: ROGERS (RO4350B) OR Equivalent, Dielectric Constant=3.48±.05

Dielectric Thickness: .010±.001

2. 50 Ohm 2.92mm Female Connectors.

III Mini-Circuits®



ENV120



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Thermal Shock	-55° to 125°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, Except +125°C

ENV120 Rev: OR

04/30/21

DCO-0453 File: ENV120.pdf