



THIN FILM SURFACE MOUNT

Bandpass Filter

ABF-7R75G+

Mini-Circuits

50Ω

6300 to 9200 MHz

KEY FEATURES

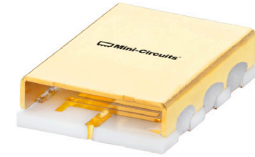
- Low Passband Insertion Loss of 1.5 dB Typ.
- High Rejection of 50 dB Typ.
- 20 dB rejection up to 30000 MHz
- Small Size, 5.59 x 8.13 x 2.03 mm

APPLICATIONS

- 5G
- Wireless Communication Systems
- Satellite Communication
- Military and Defense
- Test and Measurement

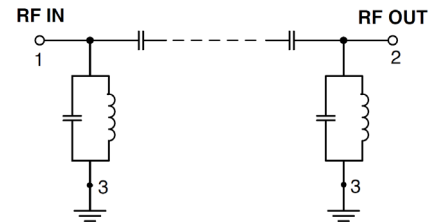
PRODUCT OVERVIEW

Mini-Circuits' Surface Mount Thin-Film filters offer low insertion loss and high rejection realized via Thin-Film on Alumina substrate, using a sputtering process that can guarantee an enhanced Q and repeatable performance. Low pass, high pass, and bandpass surface mount thin-film designs can be realized with this technology up to 40 GHz in a small form factor helping customers achieve their SWaP objectives. Using our high quality thin-film manufacturing process we can guarantee repeatability on large batches of filters.



Generic photo used for illustration purposes only

FUNCTIONAL DIAGRAM



ELECTRICAL SPECIFICATIONS^{1,2} AT +25°C

Parameter	F#	Frequency (MHz)	Min.	Typ.	Max.	Units
Pass Band	Center Frequency ³	—	—	7750	—	MHz
	Insertion Loss	F1-F2	6300 - 9200	1.5	2.5	dB
	Return Loss	F1-F2	6300 - 9200	—	9.0	dB
Stopband, Lower	Rejection	DC-F3	DC - 3000	40	55	dB
		F3-F4	3000 - 4400	20	40	dB
Stopband, Upper	Rejection	F5-F6	11000 - 13000	20	35	dB
		F6-F7	13000 - 16000	40	60	dB
		F7-F8	16000 - 30000	—	20	—

1. Tested on Evaluation Board P/N TB-ABF-7R75G+ with feedline losses removed by normalization of S12 and S21 traces to measurement of TB thru-line.

2. This component is not intended for use as a DC-blocking circuit element. In applications where DC voltage and/or current is present at either the input or output ports, external DC blocking capacitors are required.

3. Typical variation ±3%.

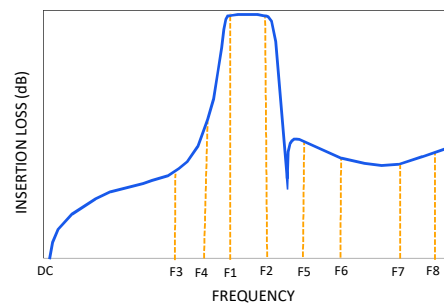
ABSOLUTE MAXIMUM RATINGS⁴

Parameter	Ratings
Operating Temperature	-55 °C to +125 °C
Storage Temperature	-55 °C to +125 °C
Input Power ⁵	1W Max. at +25°C

4. Permanent damage may occur if any of these limits are exceeded.

5. Power rating applies only to signals within the passband.

TYPICAL FREQUENCY RESPONSE AT +25°C





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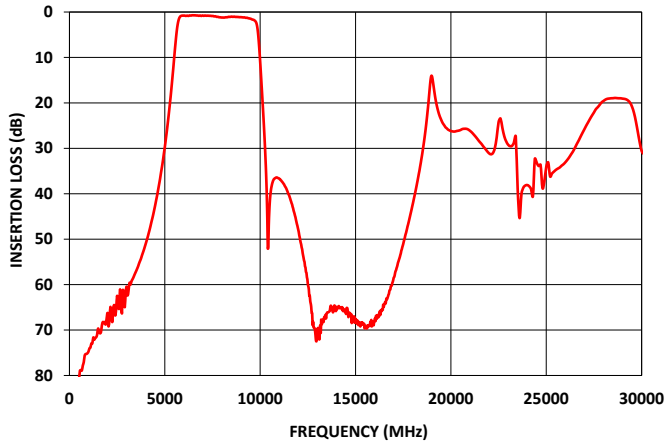
Mini-Circuits

50Ω

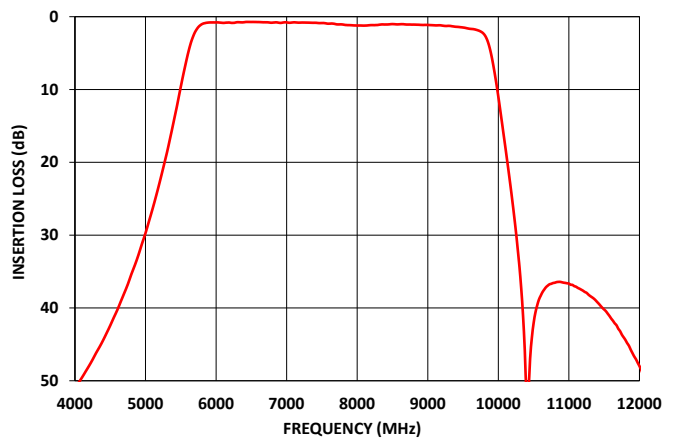
6300 to 9200 MHz

TYPICAL PERFORMANCE GRAPHS AT +25°C

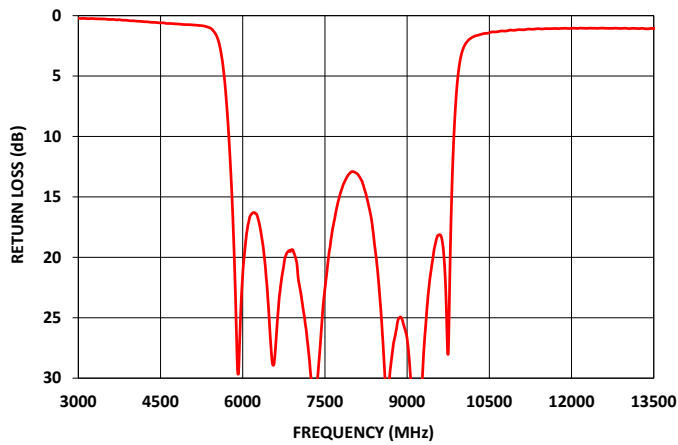
ABF-7R75G+
INSERTION LOSS



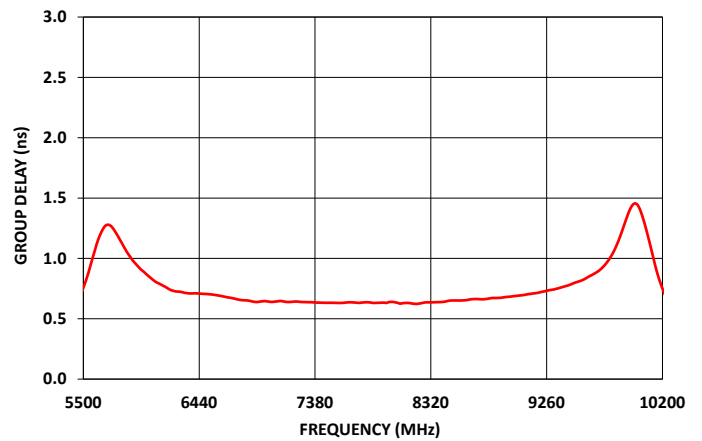
ABF-7R75G+
INSERTION LOSS (Zoomed)



ABF-7R75G+
RETURN LOSS



ABF-7R75G+
GROUP DELAY





FUNCTIONAL DIAGRAM

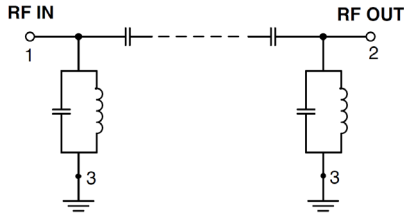
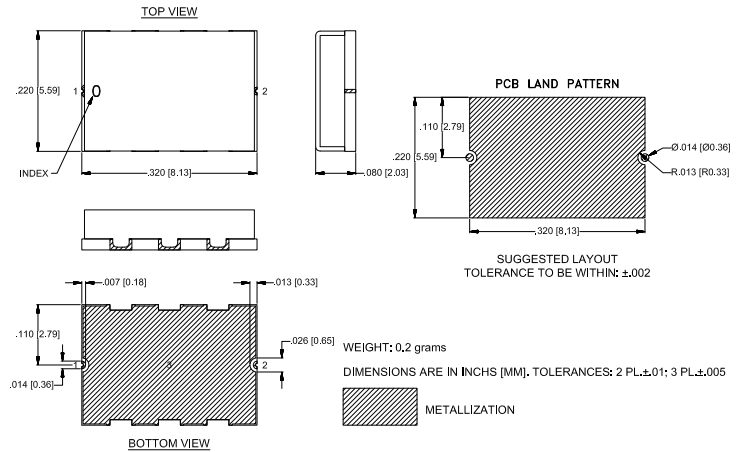


Figure 1. ABF-7R75G+ Functional Diagram

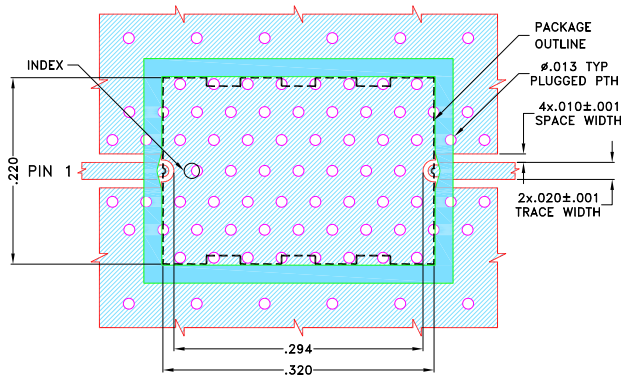
PAD DESCRIPTION

Function	Pad Number	Description
RF1	1	Connects to RF Input Port
RF2	2	Connects to RF Output Port
GROUND	3	Connects to Ground on PCB, (See drawing PL-652)
NC	—	No connection, not used internally. See drawing PL-652 for connection to PCB

CASE STYLE DRAWING



SUGGESTED PCB LAYOUT (PL-652)



NOTES:

1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS (R04350B) WITH DIELECTRIC THICKNESS .010±.0010. COPPER: 1/2 Oz. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- DENOTES PCB COPPER PATTERN WITH SMOBC (SOLDER MASK OVER BARE COPPER)
 - DENOTES PCB COPPER PATTERN FREE OF SOLDERMASK

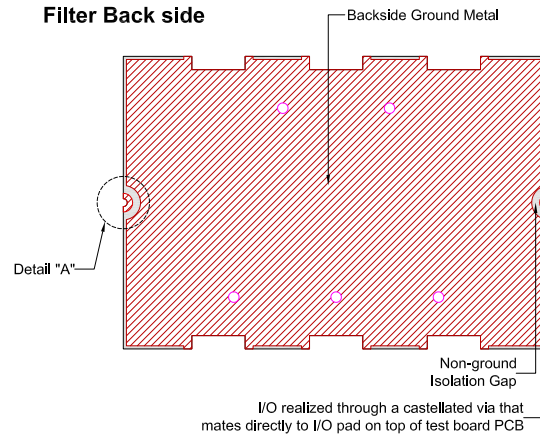
Figure 2. Suggested PCB Layout PL-652

PRODUCT MARKING*: ABF-7R75G

*Marking may contain other features or characters for internal lot control.

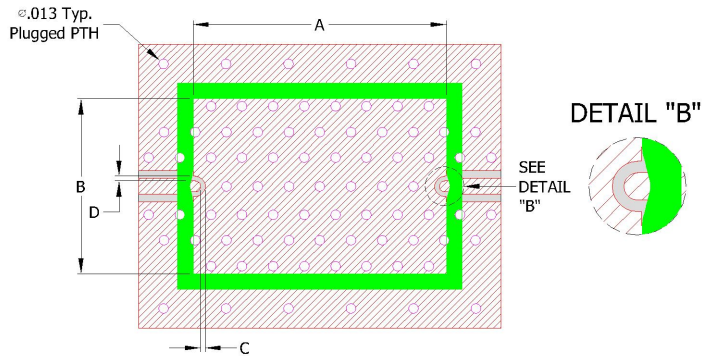
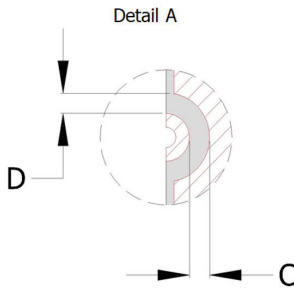


RECOMMENDED PCB LAYOUT PATTERN FOR FILTER



PCB Pattern Recommendations

Filter RF I/O Detail (Filter Back Side)



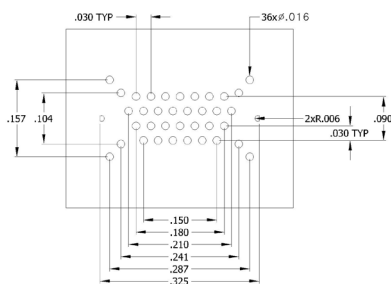
- 1) Customer PCB's ground pattern length (dimension A) can be similar to filter length.
- 2) Customer PCB's ground pattern width (dimension B) can be similar to filter width.
- 3) Dimensions C and D on Filter RF I/O detail and Customer PCB pattern can be closely match. The dimensions of C and D on the Customer PCB pattern can be slightly larger to account for component alignment tolerance (ground metal can be pulled back from RF I/O trace).
- 4) Recommend to use Solder mask at Customer PCB at outer area of filter pattern/ footprint with a clearance of about 1.25mil at each side. (Tighter registration tolerance required for solder mask)
- 5) Recommended to use Solder mask at I/O of Customer PCB as per above diagram (refer detail B).



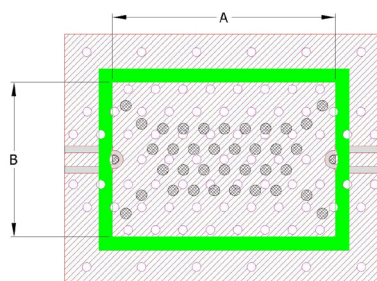
COMMENTS ON COMPONENT HANDLING AND SOLDER ATTACH

- 1) Avoid using soldering iron directly to the ceramic filter. This would lead to development of crack in the component due to thermal shock.
- 2) Vacuum pick-up tool or plastic tweezers are recommended for handling the components. Extra care should be taken not to scratch the filter or metal area.
- 3) Use 2-3 mil thickness stencil plate and screen print the solder. Refer below picture for recommended stencil pattern to get the best solder attachment.

Stencil opening drawing



Solder location after screen print



- 4) Plugged ground vias in the PWB will improve attachment consistency.
- 5) Recommended to have a similar or closer test board material and thickness (refer Mini-Circuits evaluation board for details) to minimize the CTE over the temperature range.



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ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASH BOARD.

[CLICK HERE](#)

Performance Data and Graphs	Data
	Graphs
	S-Parameter (S2P Files) Data Set (.zip file) De-embedded to device pads
Case Style	UC2731 Lead Finish: Gold over Nickel Plate.
RoHS Status	Compliant
Tape and Reel	TR-F003
Suggested Layout for PCB Design	PL-652
Evaluation Board	TB-ABF-7R75G+
	Gerber File
Environmental Rating	ENV120

NOTES

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits' standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html



Thin-Film Bandpass Filter

ABF-7R75G+

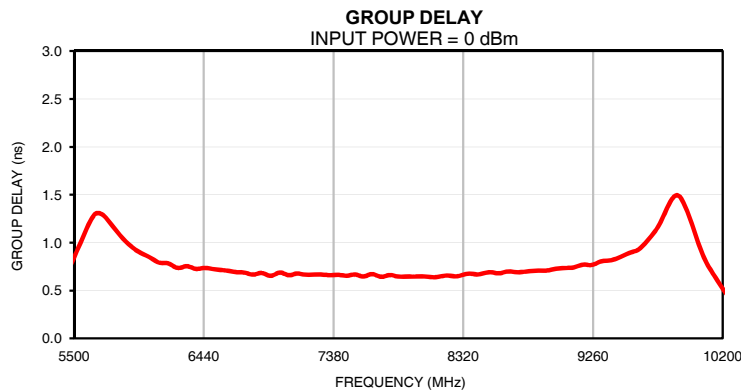
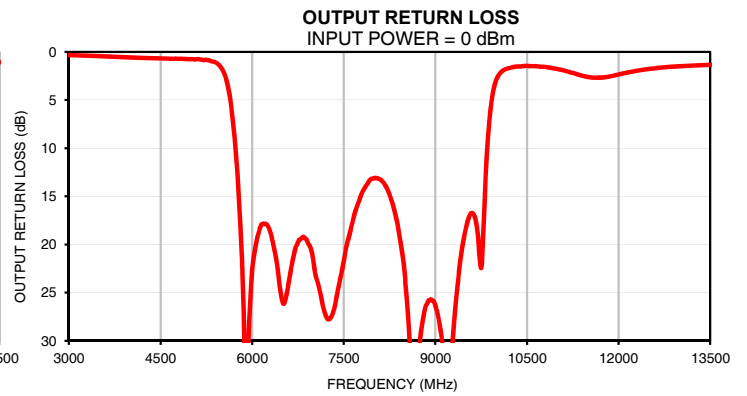
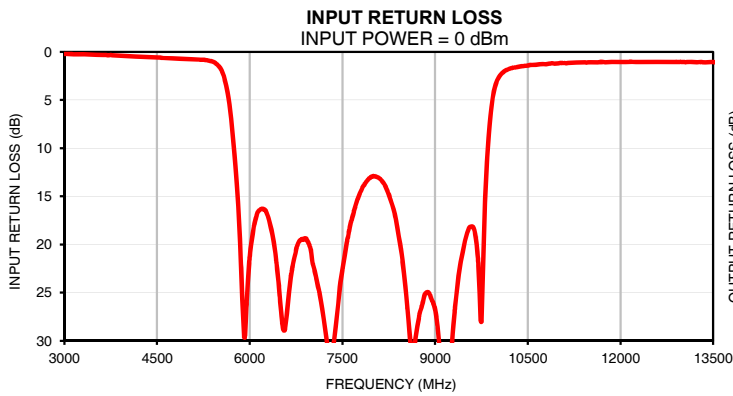
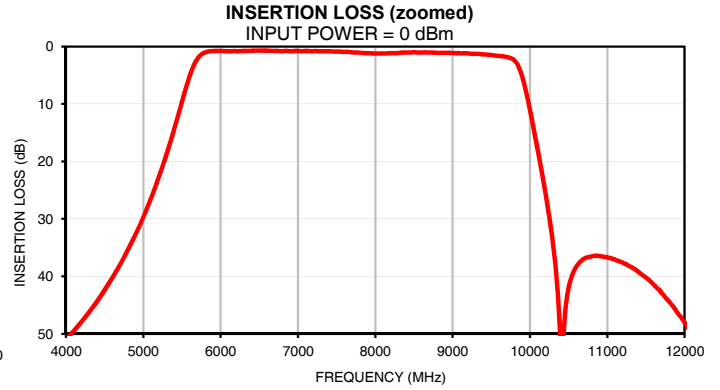
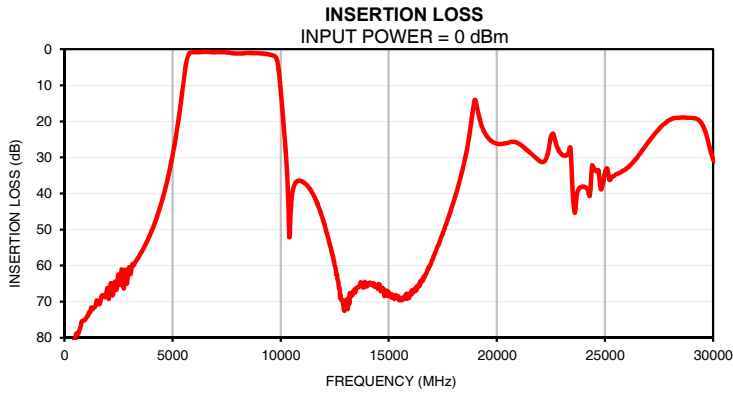
Typical Performance Data

FREQ.	Insertion Loss	Input Return Loss	Output Return Loss
(MHz)	(dB)	(dB)	(dB)
10	99.40	0.07	0.15
50	94.41	0.10	0.18
100	94.15	0.05	0.13
500	80.74	0.25	0.34
1000	74.70	0.30	0.39
2000	66.28	0.27	0.30
2500	62.46	0.21	0.29
2800	61.28	0.21	0.30
3000	60.45	0.22	0.32
3200	60.00	0.25	0.37
3400	57.94	0.27	0.42
3600	55.97	0.31	0.47
3800	53.60	0.38	0.52
4000	50.99	0.42	0.58
4200	47.95	0.50	0.62
4400	44.42	0.56	0.67
4600	40.29	0.64	0.68
4800	35.36	0.69	0.73
5000	29.73	0.74	0.74
5200	22.77	0.81	0.85
5400	14.38	1.00	1.09
6000	0.79	21.36	22.76
6300	0.76	17.16	18.74
6500	0.73	26.41	26.00
7000	0.78	21.12	21.61
7200	0.81	27.77	27.29
7750	1.06	15.24	15.49
8000	1.21	12.91	13.10
8500	1.01	23.19	23.28
9000	1.13	26.75	26.35
9200	1.21	42.18	35.04
9500	1.49	19.18	18.30
9950	7.87	4.17	3.98
10000	11.00	3.04	2.87
10300	34.43	1.61	1.55
11000	36.66	1.18	1.78
11500	40.22	1.09	2.61
12000	47.99	1.05	2.35
12500	59.94	1.04	1.77
13000	70.62	1.05	1.49
13500	66.26	1.06	1.35
14000	64.89	1.11	1.26
14500	66.46	1.14	1.22
15000	67.78	1.19	1.20
15500	69.08	1.25	1.21
16000	67.83	1.27	1.23
16500	64.46	1.28	1.26
17000	58.50	1.29	1.30
17500	51.05	1.33	1.35
18000	42.27	1.41	1.39
18500	31.12	1.63	1.47
19000	14.08	3.59	3.17
19500	23.82	1.80	1.46
20000	26.19	1.90	1.39
20500	25.98	2.69	1.39
21000	26.15	4.97	1.44
21500	28.55	3.82	1.46
25000	34.05	1.68	1.31
28000	19.58	3.68	3.04
30000	30.58	2.23	3.48

FREQ.	Group Delay
(MHz)	(ns)
6300	0.75
6320	0.76
6340	0.75
6380	0.72
6400	0.72
6450	0.73
6500	0.72
6550	0.71
6600	0.70
6650	0.69
6700	0.69
6750	0.68
6800	0.67
6850	0.68
6900	0.66
6950	0.66
7000	0.68
7050	0.65
7100	0.67
7150	0.67
7200	0.66
7250	0.66
7300	0.66
7350	0.66
7400	0.66
7450	0.66
7500	0.66
7550	0.66
7600	0.64
7650	0.67
7700	0.65
7750	0.64
7800	0.65
7850	0.64
7900	0.64
7950	0.64
8000	0.64
8050	0.64
8100	0.64
8150	0.64
8200	0.66
8250	0.65
8300	0.66
8350	0.67
8400	0.67
8450	0.67
8500	0.69
8550	0.68
8600	0.68
8650	0.70
8700	0.69
8750	0.69
8800	0.70
8850	0.70
8900	0.70
8950	0.71
9000	0.73
9050	0.73
9100	0.73
9200	0.77

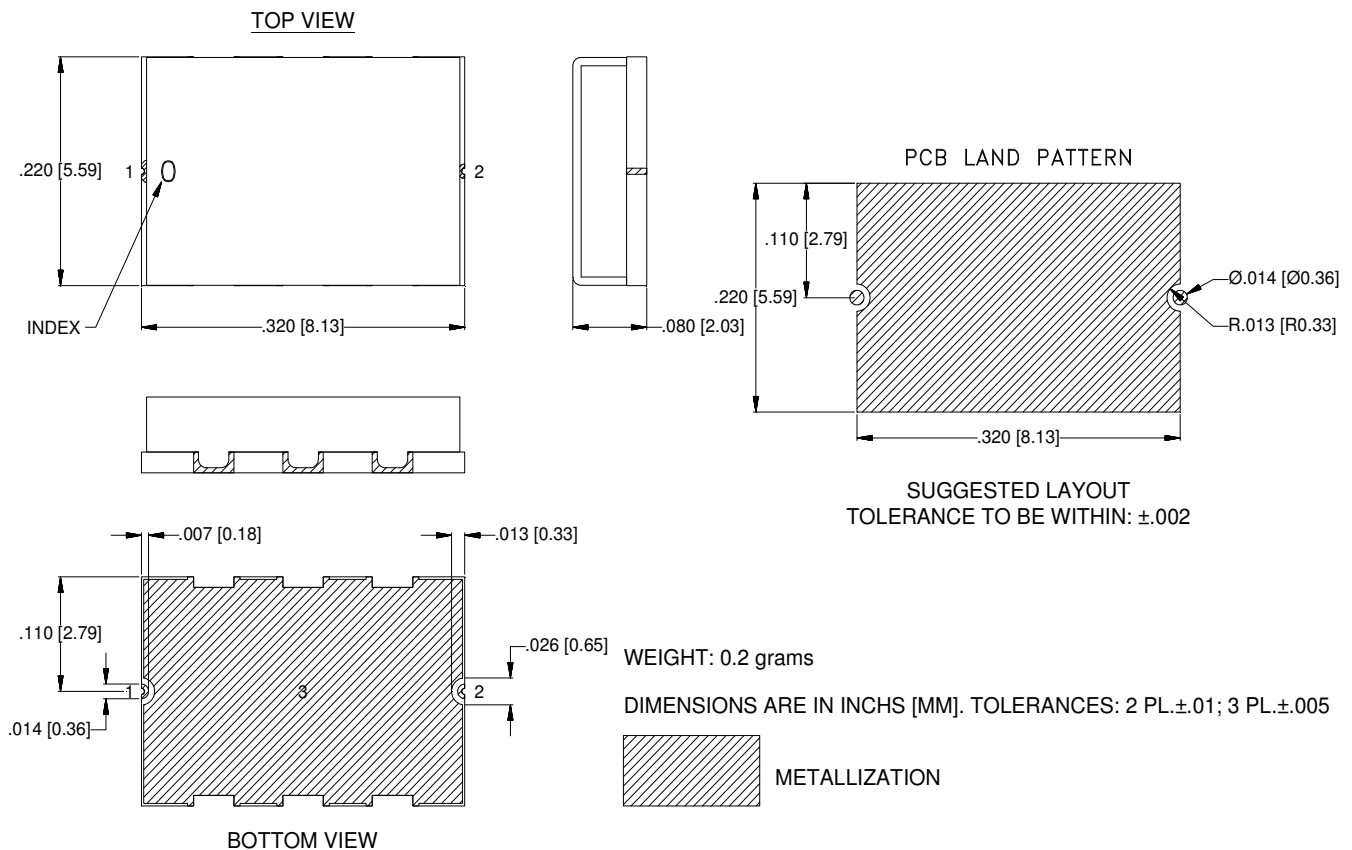


Typical Performance Curves



Outline Dimensions

UC2731



Notes:

1. Case material: Gold over Nickel over Annealed Stainless Steel.
2. Base: Ceramic
3. Termination finish: **as shown below or indicated on Data Sheet.**
For RoHS Case Styles: Gold over Nickel plate. All models, (+) suffix.



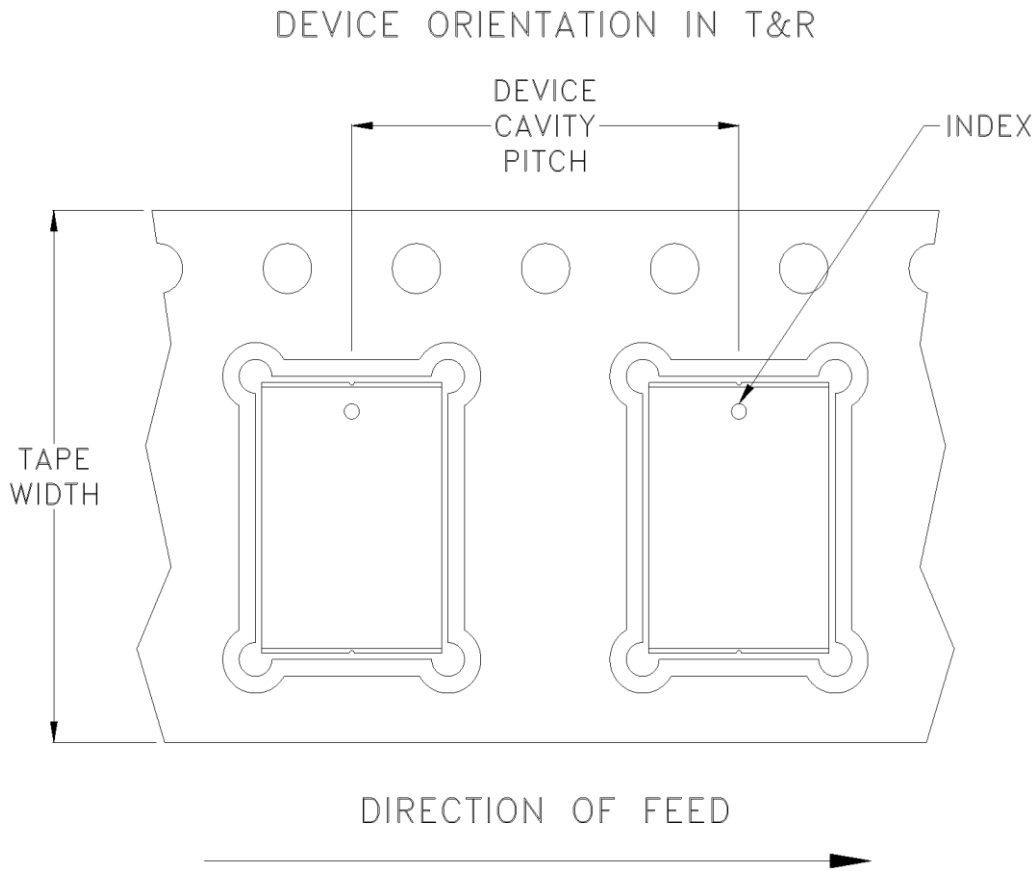
P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F003



Tape Width, mm	Device Cavity Pitch, mm	Real Size, inches	Devices per Reel See note
16	12	7	10
			20
			50
			100
			200
		13	500

Note: Please consult individual model data sheet/dashboard to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



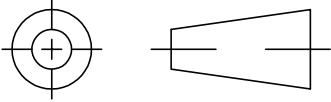
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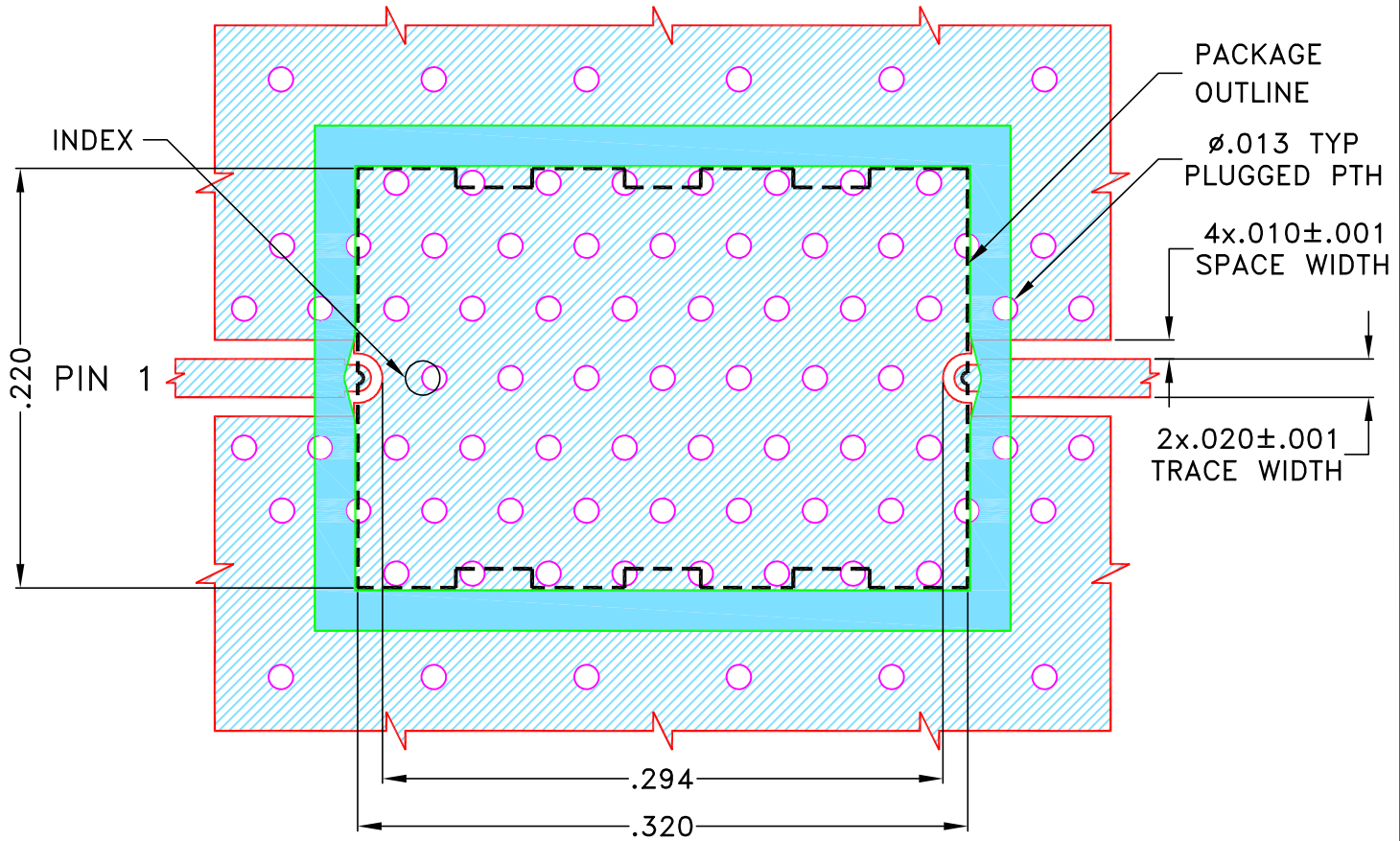
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	ECO-007104	NEW RELEASE	MAR 21	DDR	VC
A	ECO-010633	UPDATED AS PER CURRENT TEST BOARD	NOV 21	DDR	VC
B	ECO-019739	UPDATED TRACE AND SPACE WIDTH	OCT 23	LK	VC
		TOLERANCE ONLY NO OTHER CHANGES			

**SUGGESTED MOUNTING CONFIGURATION
FOR UC2731 CASE STYLE**



NOTES:

1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS (R04350B) WITH DIELECTRIC THICKNESS $.010 \pm .0010$. COPPER: 1/2 Oz. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER PATTERN WITH SMOBC (SOLDER MASK OVER BARE COPPER)
 DENOTES PCB COPPER PATTERN FREE OF SOLDERMASK

UNLESS OTHERWISE SPECIFIED	INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN: DDR	29 MAR 21
TOLERANCES ON:	CHECKED: RR	29 MAR 21
2 PL DECIMALS ±	APPROVED: NN	29 MAR 21
3 PL DECIMALS ± .005		
ANGLES ±		
FRACTIONS ±		

Mini-Circuits® 13 Neptune Avenue
Brooklyn NY 11235

PL DWG, UC2731 C.S, 50 OHM, ABF

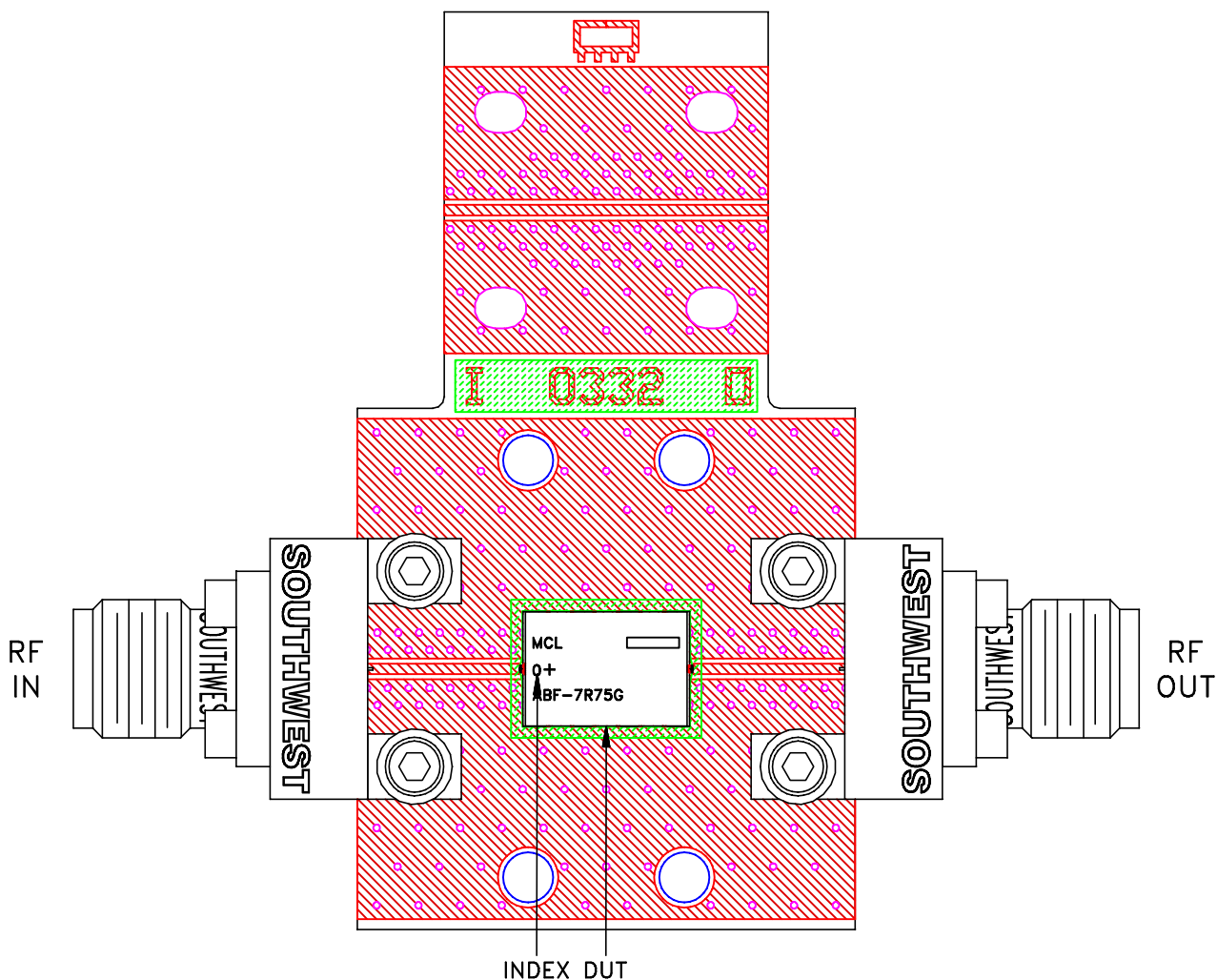
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ASHEETA1.DWG REV:A DATE:01/12/95

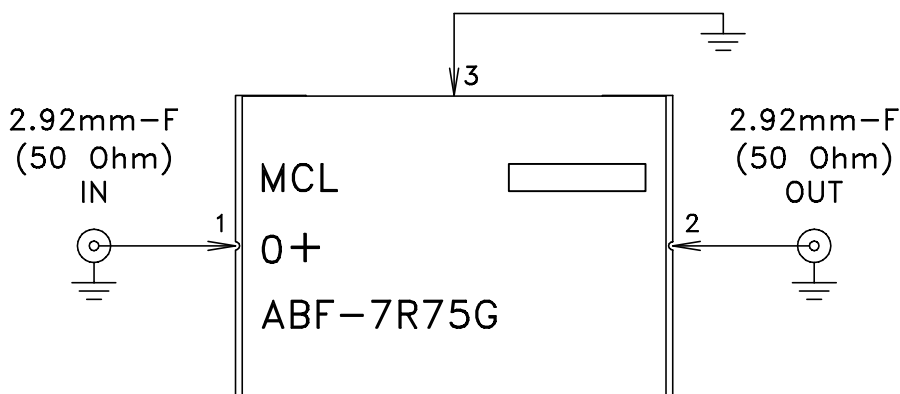
SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-652	B
FILE:	98-PL-652	SCALE:	10:1
		SHEET:	1 OF 1

Evaluation Board and Circuit

TB-ABF-7R75G+




Schematic diagram



Notes:

1. PCB Material: ROGERS (R04350B) OR Equivalent, Dielectric Constant=3.48±.05
Dielectric Thickness: .010±.001
2. 50 Ohm 2.92mm Female Connectors.

 **Mini-Circuits®**

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Thermal Shock	-55° to 125°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, Except +125°C