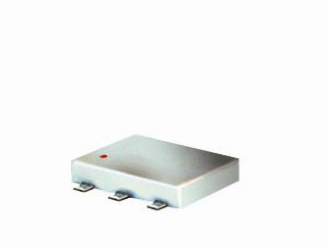
Engineering Development Model

Directional Coupler ADC-ED12199

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.



CASE STYLE : CD542

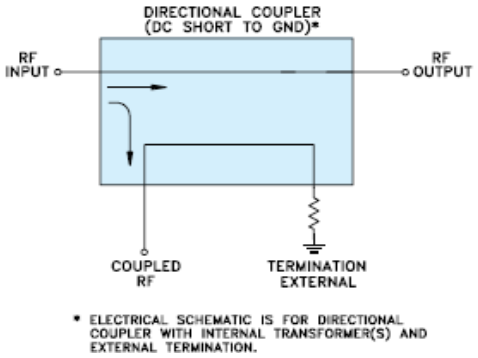
ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		1		1000	MHz
Coupling	**Nominal**		13 ± 0.5		dB
	Flatness		± 1.065		dB
Mainline Loss **	**1-10 MHz**		0.90		dB
	10-500 MHz		0.70		dB
	500-1000 MHz		0.90		dB
Directivity	**1-10 MHz**		27		dB
	10-500 MHz		27		dB
	500-1000 MHz		18		dB
VSWR	**1-1000 MHz**		1.2		(:1)
RF Power Input	**1-1000 MHz**			1.0	W

Note: ** Mainline loss includes theoretical coupled power loss of 0.223 dB at 13 dB coupling.

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
INPUT	1
OUTPUT	6
COUPLED FORWARD	3
50Ω TERM EXTERNAL	4
GROUND	2
NOT USED	5

Electrical Schematic



Directional Coupler ADC-ED12199

Typical Performance Data

FREQUENCY (MHz)	INSERTION LOSS (dB)	COUPLING (dB)	DIRECTIVITY (dB)	RETURN LOSS (dB)		
				IN	OUT	CPL
0.3	1.99	13.15	17.90	9.33	9.51	9.03
0.5	1.59	12.98	20.71	11.94	12.51	11.86
0.8	1.37	12.87	22.66	13.93	15.00	14.01
1.0	1.29	12.83	23.30	14.77	16.10	14.85
2.0	1.06	12.72	25.46	17.04	19.30	17.05
3.0	0.93	12.66	27.22	18.35	21.24	18.35
4.0	0.84	12.61	28.52	19.32	22.78	19.33
5.0	0.78	12.57	29.33	20.08	24.07	20.10
10.0	0.69	12.50	30.42	22.19	28.64	22.13
15.0	0.65	12.47	30.58	22.96	31.31	22.85
20.0	0.64	12.46	30.55	23.30	32.90	23.17
25.0	0.63	12.45	30.38	23.48	33.92	23.26
30.0	0.63	12.45	30.20	23.58	34.53	23.32
35.0	0.63	12.45	30.04	23.66	34.89	23.34
40.0	0.63	12.45	29.91	23.72	35.05	23.30
45.0	0.64	12.45	29.82	23.76	35.10	23.25
50.0	0.64	12.46	29.76	23.78	35.06	23.20
100.0	0.67	12.51	28.86	23.49	33.23	22.01
150.0	0.69	12.57	28.12	23.06	30.63	20.58
200.0	0.71	12.63	27.00	22.55	28.75	19.07
250.0	0.72	12.70	26.34	22.00	27.28	17.85
300.0	0.73	12.77	25.26	21.51	26.03	16.78
350.0	0.74	12.86	24.18	20.98	24.89	15.84
400.0	0.75	12.94	23.37	20.59	23.94	14.99
450.0	0.76	13.03	22.42	20.16	23.16	14.24
500.0	0.78	13.12	21.54	19.75	22.37	13.65
550.0	0.79	13.21	20.82	19.50	21.79	13.11
600.0	0.80	13.30	20.19	19.26	21.25	12.61
650.0	0.82	13.38	19.70	19.07	20.66	12.15
700.0	0.84	13.47	19.14	18.96	20.29	11.78
750.0	0.86	13.55	18.63	18.78	19.86	11.41
800.0	0.87	13.64	18.11	18.80	19.59	11.12
850.0	0.89	13.72	17.61	18.69	19.23	10.84
900.0	0.92	13.79	17.27	18.76	18.97	10.56
950.0	0.94	13.87	16.80	18.75	18.73	10.31
1000.0	0.96	13.94	16.55	18.78	18.42	10.10
1500.0	1.33	14.58	14.89	19.60	17.05	8.70
2000.0	2.13	15.75	15.59	15.83	15.72	7.47
2500.0	5.81	22.75	-3.26	7.50	21.31	6.60
3000.0	9.02	26.44	-10.05	4.32	10.84	4.98

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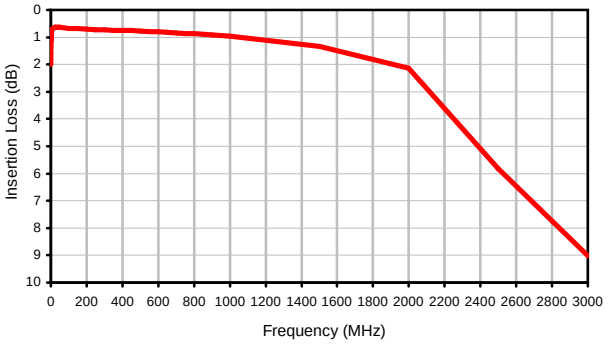
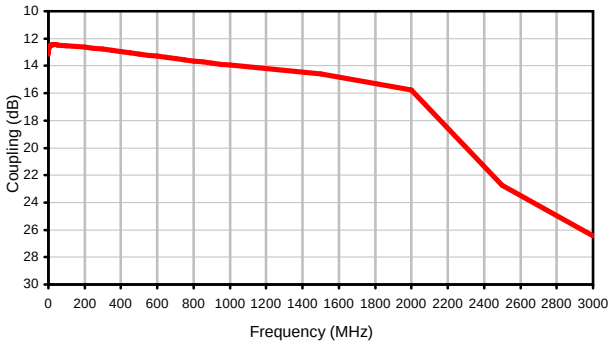
Typical Performance Curves

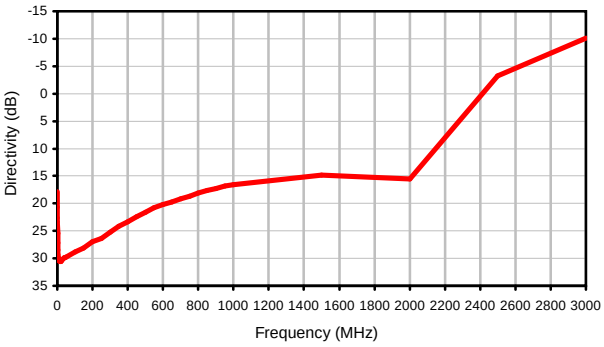
Insertion Loss



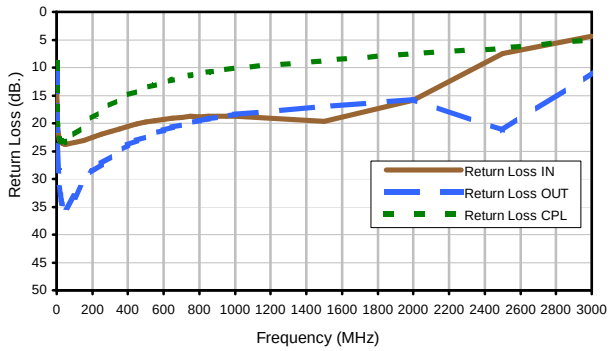
Coupling



Directivity



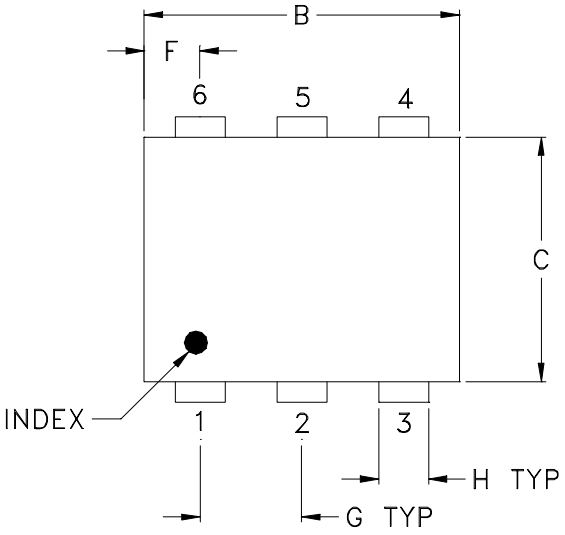
Return Loss

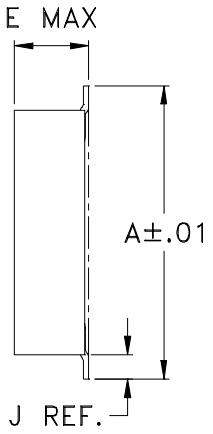


Case Style CD

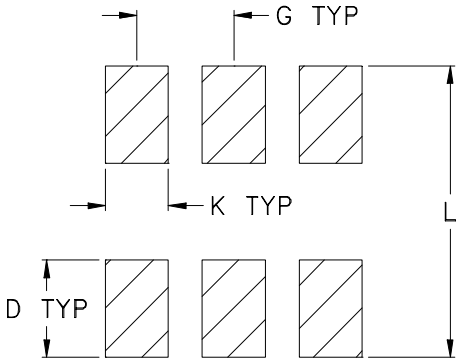
CD541
CD542
CD636
CD637

Outline Dimensions





PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE#	A	B	C	D	E	F	G	H	J	K	L	WT, GRAM
CD541	.272 (6.91)	.310 (7.87)	.220 (5.58)	.100 (2.54)	.082 (2.08)	.055 (1.40)	.100 (2.54)	.030 (0.76)	.026 (0.66)	.065 (1.65)	.300 (7.62)	.15
CD542					.112 (2.84)							.20
CD636					.162 (4.11)							.25
CD637					.206 (5.23)							.40

Dimensions are in inches (mm). Tolerances: 2 Pl. ± .01; 3 Pl. ± .005

Notes:

1. Case material: Plastic.
2. Termination finish:
   For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
   For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.

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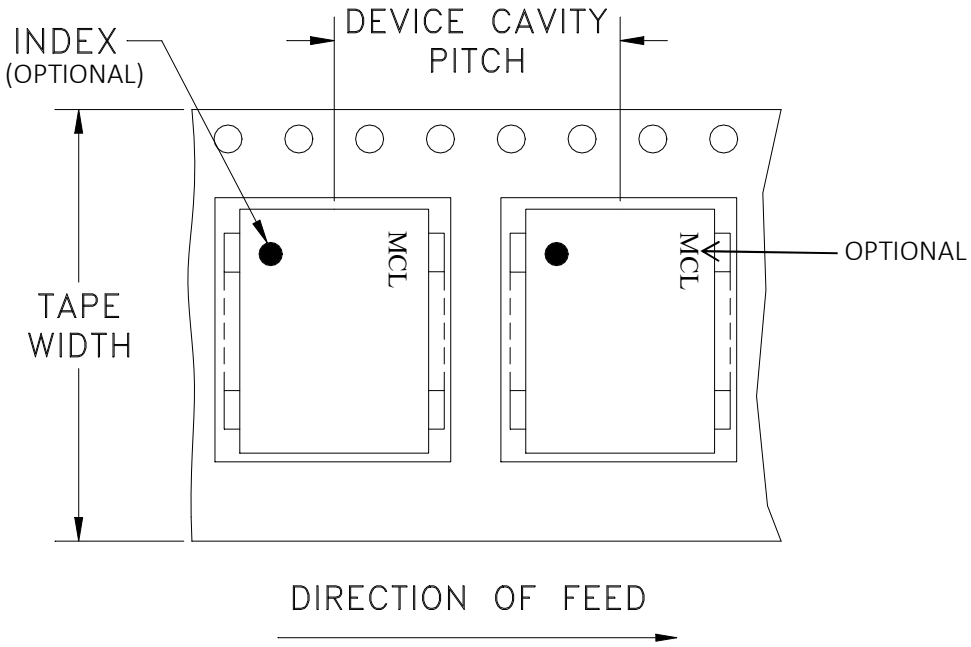
DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel see note	
16	12	7	Small quantity standard (see note)	20
				50
				100
				200
		13	Standard	500
				1000

Note: Availability of small reel quantity varies by model.
Refer to pricing and availability on individual model dashboard.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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Environmental Specifications ENV02T1

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215