

# Engineering Development Model

## Power Splitter/Combiner

## ADP-ED11188/1

### 2 Way-0°

#### Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

**CASE STYLE : CD636**

ELECTRICAL SPECIFICATIONS 75Ω @ +25°C					
Parameter		Min.	Typ.	Max.	Units
<b>Frequency</b>		0.1		500	MHz
<b>Isolation</b>	0.1 - 1 MHz		34		dB
	1 - 250 MHz		26		dB
	250 - 500 MHz		17		dB
<b>Insertion Loss Above 3.0 dB</b>	0.1 - 1 MHz		0.20		dB
	1 - 250 MHz		0.20		dB
	250 - 500 MHz		0.23		dB
<b>Phase Unbalance</b>	0.1 - 1 MHz		0.015		deg.
	1 - 250 MHz		0.027		deg.
	250 - 500 MHz		0.056		deg.
<b>Amplitude Unbalance</b>	0.1 - 1 MHz		0.008		dB
	1 - 250 MHz		0.009		dB
	250 - 500 MHz		0.008		dB
<b>VSWR</b>	SUM Port		1.15		(:1)
	OUT Ports		1.15		(:1)

MAXIMUM RATINGS	
<b>Operating Temperature</b>	-40°C to 85°C
<b>Storage Temperature</b>	-55°C to 100°C

PIN CONNECTIONS	
<b>SUM PORT</b>	1
<b>PORT 1</b>	4
<b>PORT 2</b>	3
<b>GND EXT</b>	2, 5, 6

#### Functional Diagram



# 2 Way-0° Power Splitter/Combiner

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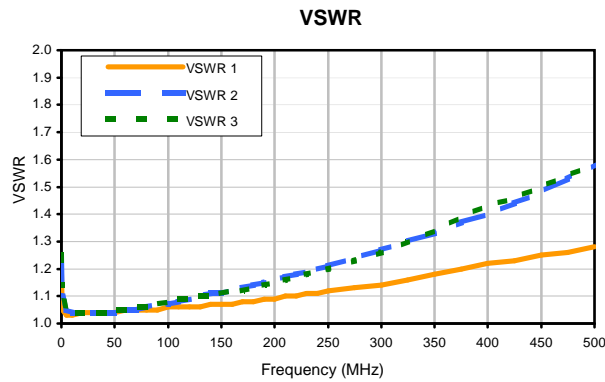
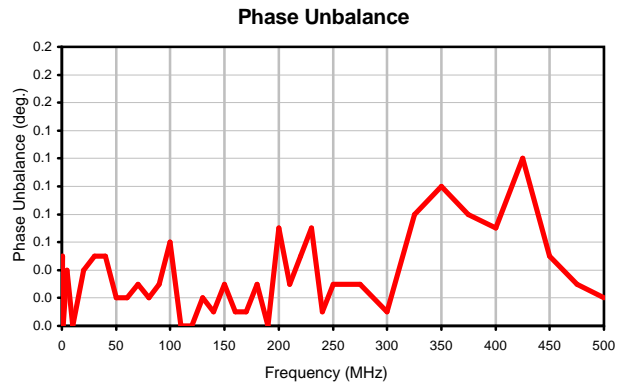
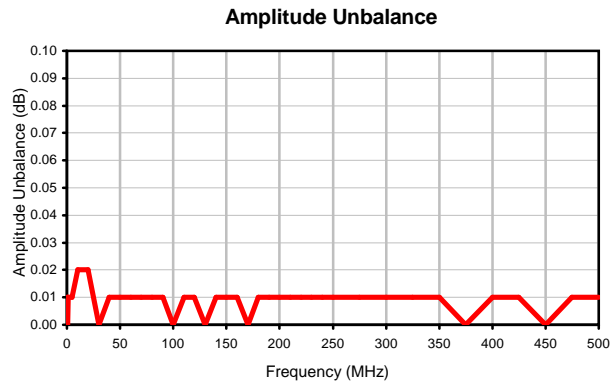
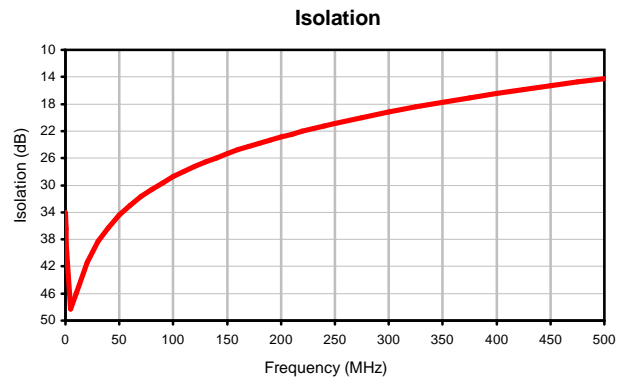
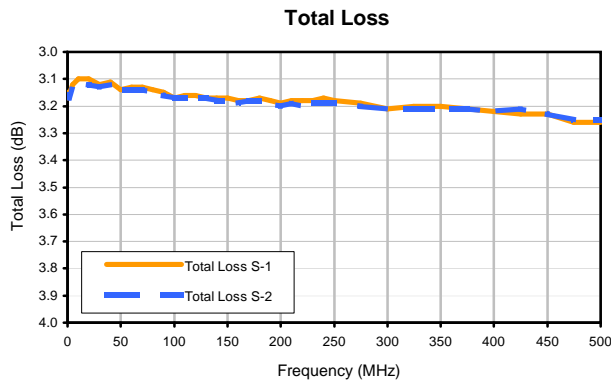
## Typical Performance Data

FREQ. (MHz)	TOTAL LOSS <sup>1</sup> (dB)		AMP. UNBAL. (dB)	ISOLATION (dB) 1-2	PHASE UNBAL. (deg.)	FREQ. (MHz)	VSWR (:1)		
	S-1	S-2					1	2	3
0.3	3.17	3.17	0.01	34.05	0.01	0.3	1.19	1.25	1.25
0.5	3.15	3.16	0.01	36.52	0.05	0.5	1.09	1.14	1.14
0.8	3.16	3.16	0.00	38.48	0.00	0.8	1.06	1.11	1.11
1.0	3.16	3.17	0.01	39.96	0.00	1.0	1.05	1.10	1.10
5.0	3.12	3.14	0.01	48.27	0.04	5.0	1.03	1.05	1.05
10.0	3.10	3.12	0.02	45.97	0.00	10.0	1.03	1.04	1.04
20.0	3.10	3.12	0.02	41.42	0.04	20.0	1.04	1.04	1.04
30.0	3.12	3.13	0.00	38.29	0.05	30.0	1.04	1.04	1.04
40.0	3.11	3.12	0.01	36.26	0.05	40.0	1.04	1.04	1.04
50.0	3.14	3.14	0.01	34.40	0.02	50.0	1.04	1.04	1.05
60.0	3.13	3.14	0.01	33.00	0.02	60.0	1.05	1.05	1.05
70.0	3.13	3.14	0.01	31.70	0.03	70.0	1.05	1.05	1.06
80.0	3.14	3.15	0.01	30.60	0.02	80.0	1.05	1.06	1.06
90.0	3.15	3.16	0.01	29.69	0.03	90.0	1.05	1.07	1.07
100.0	3.17	3.17	0.00	28.77	0.06	100.0	1.06	1.07	1.08
110.0	3.16	3.17	0.01	28.00	0.00	110.0	1.06	1.08	1.09
120.0	3.16	3.17	0.01	27.24	0.00	120.0	1.06	1.09	1.09
130.0	3.17	3.17	0.00	26.59	0.02	130.0	1.06	1.10	1.10
140.0	3.17	3.18	0.01	25.95	0.01	140.0	1.07	1.11	1.10
150.0	3.17	3.18	0.01	25.36	0.03	150.0	1.07	1.11	1.11
160.0	3.18	3.19	0.01	24.79	0.01	160.0	1.07	1.12	1.12
170.0	3.18	3.18	0.00	24.27	0.01	170.0	1.08	1.13	1.12
180.0	3.17	3.18	0.01	23.79	0.03	180.0	1.08	1.14	1.13
190.0	3.18	3.19	0.01	23.31	0.00	190.0	1.09	1.15	1.14
200.0	3.19	3.20	0.01	22.87	0.07	200.0	1.09	1.16	1.15
210.0	3.18	3.19	0.01	22.44	0.03	210.0	1.10	1.17	1.16
220.0	3.18	3.20	0.01	22.03	0.05	220.0	1.10	1.18	1.17
230.0	3.18	3.19	0.01	21.61	0.07	230.0	1.11	1.19	1.18
240.0	3.17	3.19	0.01	21.24	0.01	240.0	1.11	1.20	1.19
250.0	3.18	3.19	0.01	20.87	0.03	250.0	1.12	1.21	1.20
275.0	3.19	3.20	0.01	20.01	0.03	275.0	1.13	1.24	1.23
300.0	3.21	3.21	0.01	19.20	0.01	300.0	1.14	1.27	1.26
325.0	3.20	3.21	0.01	18.46	0.08	325.0	1.16	1.30	1.30
350.0	3.20	3.21	0.01	17.75	0.10	350.0	1.18	1.33	1.34
375.0	3.21	3.21	0.00	17.09	0.08	375.0	1.20	1.37	1.39
400.0	3.22	3.22	0.01	16.45	0.07	400.0	1.22	1.40	1.43
425.0	3.23	3.21	0.01	15.85	0.12	425.0	1.23	1.44	1.46
450.0	3.23	3.23	0.00	15.29	0.05	450.0	1.25	1.48	1.50
475.0	3.26	3.25	0.01	14.75	0.03	475.0	1.26	1.53	1.54
500.0	3.26	3.25	0.01	14.23	0.02	500.0	1.28	1.58	1.58

<sup>1</sup> Total Loss = Insertion Loss + 3dB Splitter Loss



## Typical Performance Curves



# Case Style

# CD

CD541  
CD542  
CD636  
CD637

## Outline Dimensions



## PCB Land Pattern



Suggested Layout,  
Tolerance to be within  $\pm .002$

CASE#	A	B	C	D	E	F	G	H	J	K	L	WT, GRAM
CD541					.082 (2.08)							.15
CD542	.272 (6.91)	.310 (7.87)	.220 (5.58)	.100 (2.54)	.112 (2.84)	.055 (1.40)	.100 (2.54)	.030 (0.76)	.026 (0.66)	.065 (1.65)	.300 (7.62)	.20
CD636					.162 (4.11)							.25
CD637					.206 (5.23)							.40

Dimensions are in inches (mm). Tolerances: 2 Pl.  $\pm .01$ ; 3 Pl.  $\pm .005$

### Notes:

- Case material: Plastic.
- Termination finish:
  - For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
  - For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.

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# Tape & Reel Packaging TR-F34



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel see note	
16	12	7	Small quantity standard (see note)	20
				50
			100	
			200	
		13	Standard	500
				1000

Note: Availability of small reel quantity varies by model.  
Refer to pricing and availability on individual model dashboard.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215