Engineering Development Model

Power Splitter/Combiner ADP-ED6714

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : CD542

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		250		600	MHz
Isolation	**250 - 600 MHz**		24		dB
Insertion Loss Above 3.0 dB	**250 - 600 MHz**		0.39		dB
Phase Unbalance	**250 - 600 MHz**		0.447		deg.
Amplitude Unbalance	**250 - 600 MHz**		0.034		dB
VSWR	**SUM Port**		1.32		(:1)
	OUT Ports		1.19		(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	6
PORT 1	3
PORT 2	4
GND EXT	1
NOT USED	2, 5

Functional Diagram

2 Way-0° Power Splitter/Combiner ADP-ED6714

Typical Performance Data

FREQ. (MHz)	TOTAL LOSS[1] (dB) S-1	TOTAL LOSS[1] (dB) S-2	AMP. UNBAL. (dB)	ISOLATION (dB) 1-2	PHASE UNBAL. (deg.)
250.0	3.19	3.26	0.07	27.05	0.84
300.0	3.37	3.43	0.06	25.63	0.68
325.0	3.27	3.32	0.05	24.96	0.69
350.0	3.24	3.27	0.04	24.46	0.61
360.0	3.23	3.28	0.05	24.31	0.58
370.0	3.31	3.36	0.05	24.22	0.54
380.0	3.40	3.44	0.05	24.12	0.48
390.0	3.40	3.45	0.05	24.00	0.40
400.0	3.36	3.40	0.04	23.87	0.50
410.0	3.33	3.37	0.04	23.77	0.51
420.0	3.31	3.35	0.04	23.69	0.48
430.0	3.30	3.33	0.03	23.64	0.44
440.0	3.31	3.34	0.03	23.64	0.46
450.0	3.33	3.35	0.02	23.66	0.43
460.0	3.39	3.43	0.03	23.68	0.40
470.0	3.46	3.49	0.03	23.70	0.41
480.0	3.43	3.45	0.03	23.69	0.39
490.0	3.37	3.39	0.03	23.73	0.36
500.0	3.36	3.38	0.02	23.80	0.34
510.0	3.39	3.41	0.03	23.89	0.34
520.0	3.43	3.44	0.02	23.97	0.36
530.0	3.44	3.46	0.02	24.06	0.28
540.0	3.46	3.48	0.02	24.16	0.29
550.0	3.51	3.53	0.02	24.24	0.29
575.0	3.50	3.52	0.01	24.39	0.28
600.0	3.55	3.56	0.01	24.42	0.24

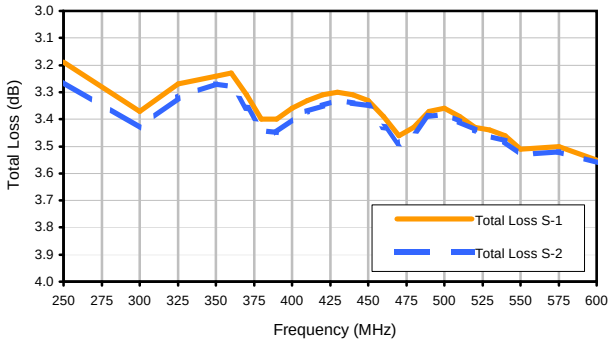
FREQ. (MHz)	VSWR (:1) S	VSWR (:1) 1	VSWR (:1) 2
250.0	1.30	1.19	1.23
300.0	1.29	1.16	1.19
325.0	1.29	1.15	1.18
350.0	1.29	1.15	1.17
360.0	1.29	1.15	1.17
370.0	1.29	1.15	1.17
380.0	1.29	1.15	1.17
390.0	1.29	1.15	1.16
400.0	1.29	1.15	1.17
410.0	1.29	1.16	1.17
420.0	1.29	1.16	1.17
430.0	1.30	1.16	1.17
440.0	1.30	1.17	1.17
450.0	1.30	1.17	1.17
460.0	1.30	1.18	1.18
470.0	1.31	1.18	1.18
480.0	1.31	1.19	1.19
490.0	1.32	1.20	1.19
500.0	1.32	1.20	1.20
510.0	1.33	1.21	1.21
520.0	1.34	1.22	1.21
530.0	1.34	1.23	1.22
540.0	1.35	1.24	1.23
550.0	1.36	1.24	1.24
575.0	1.40	1.27	1.26
600.0	1.44	1.30	1.28

[1] Total Loss = Insertion Loss + 3dB Splitter Loss

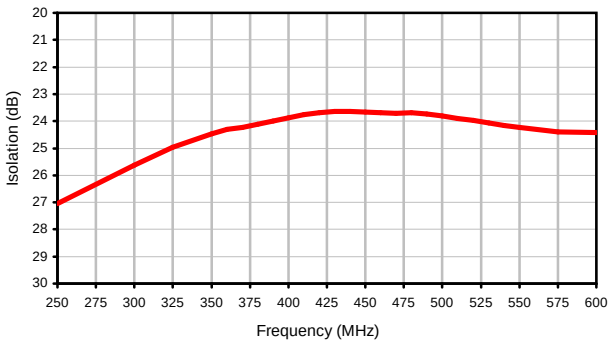
2 Way-0° Power Splitter/Combiner ADP-ED6714

Typical Performance Curves

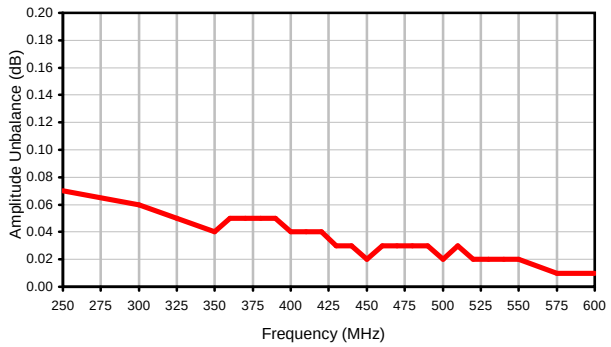
Total Loss



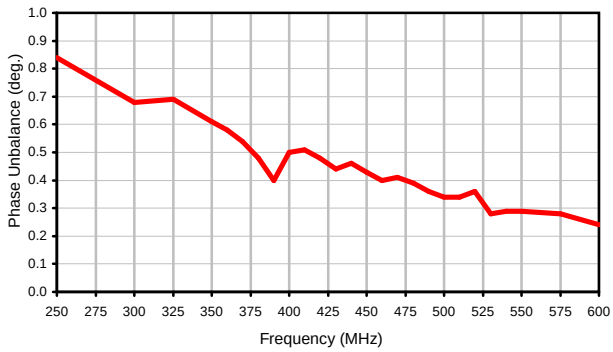
Isolation



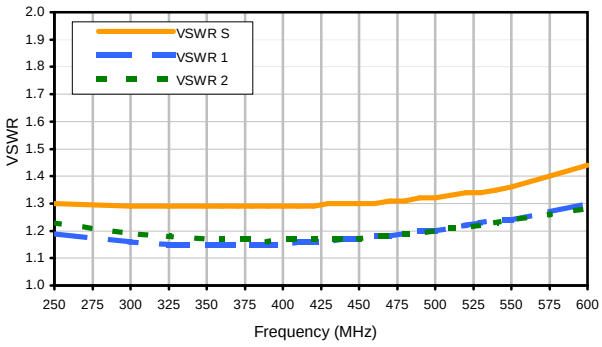
Amplitude Unbalance



Phase Unbalance



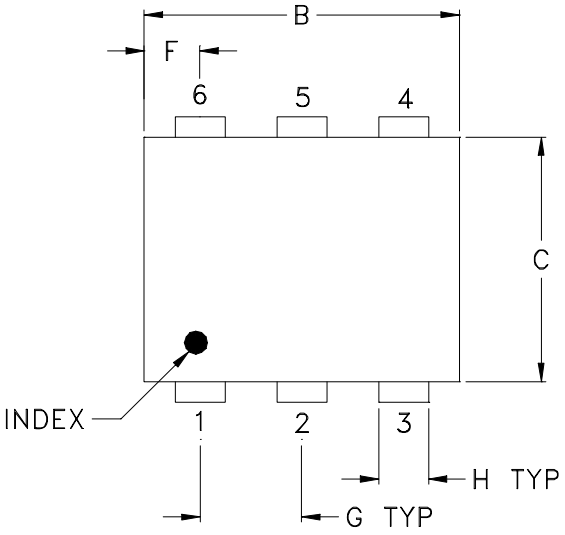
VSWR

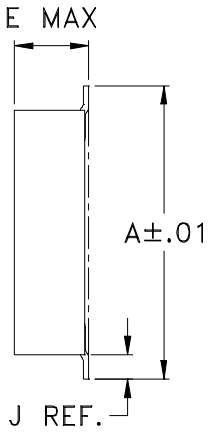


Case Style CD

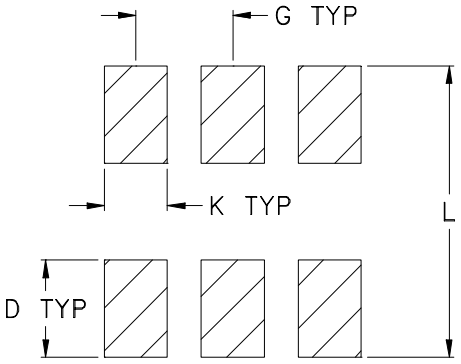
CD541
CD542
CD636
CD637

Outline Dimensions





PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE#	A	B	C	D	E	F	G	H	J	K	L	WT, GRAM
CD541	.272 (6.91)	.310 (7.87)	.220 (5.58)	.100 (2.54)	.082 (2.08)	.055 (1.40)	.100 (2.54)	.030 (0.76)	.026 (0.66)	.065 (1.65)	.300 (7.62)	.15
CD542					.112 (2.84)							.20
CD636					.162 (4.11)							.25
CD637					.206 (5.23)							.40

Dimensions are in inches (mm). Tolerances: 2 Pl. ± .01; 3 Pl. ± .005

Notes:

1. Case material: Plastic.
2. Termination finish:
   For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
   For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.

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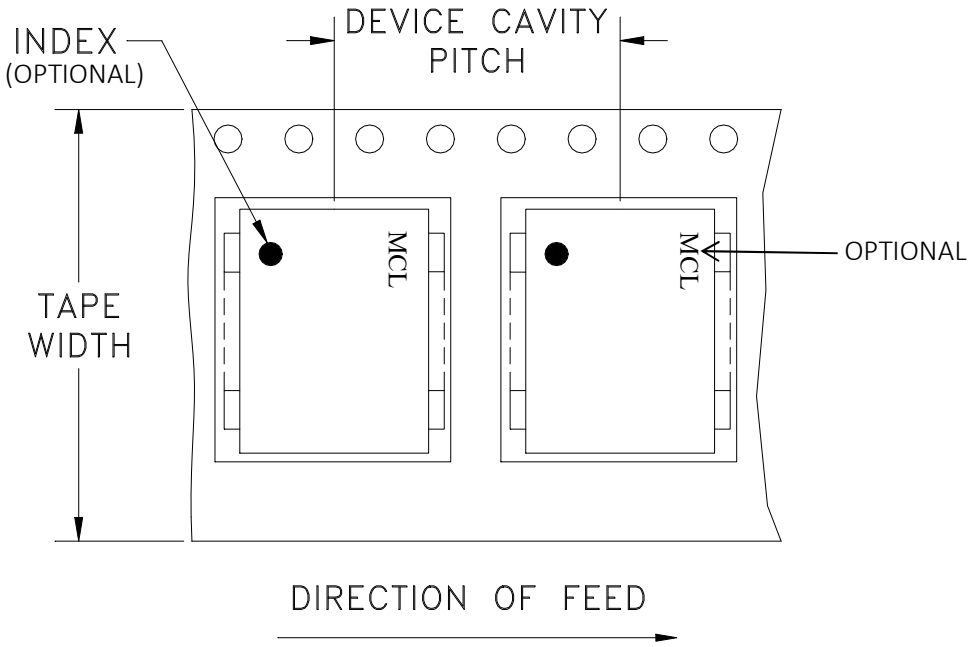
Tape & Reel Packaging TR-F34

DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel see note	
16	12	7	Small quantity standard (see note)	20
				50
				100
				200
		13	Standard	500
				1000

Note: Availability of small reel quantity varies by model.
Refer to pricing and availability on individual model dashboard.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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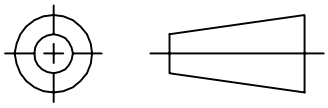
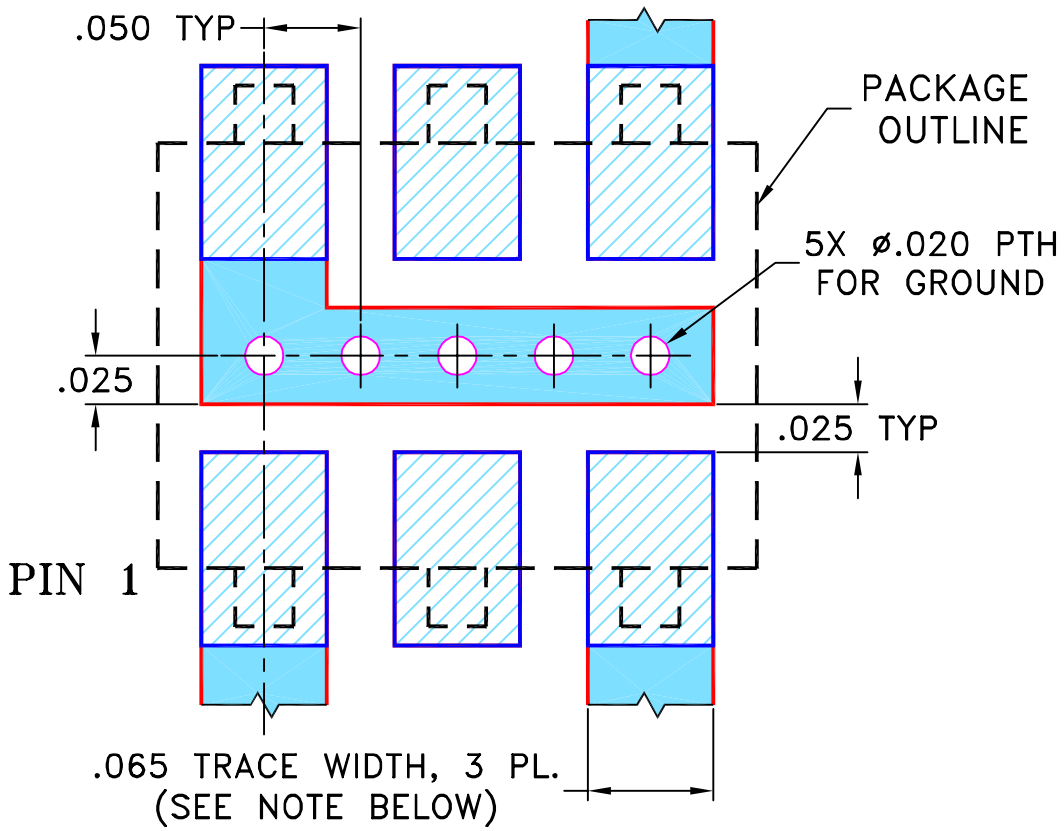
THIRD ANGLE PROJECTION



REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M77049	NEW RELEASE	05/01	AV	CT
A	M82377	UPDATED DRAWING	07/31/02	AV	HY
B	M82846	UPDATED DRAWING	05/13/03	MMG	HY
C	M102713	ADDED BK292 CASE STYLE & "...WITH SMOBC"	01/17/06	MMG	IL

SUGGESTED MOUNTING CONFIGURATION FOR BK292/CD542/CD636 CASE STYLES, "hv" PIN CONNECTION



NOTES:
1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .030" ± .002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	AV	05/30/01
TOLERANCES ON:	CHECKED	DY	06/11/01
2 PL DECIMALS ±	APPROVED	CT	06/11/01
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

ASHEETA1.DWG REV:A DATE:01/12/95

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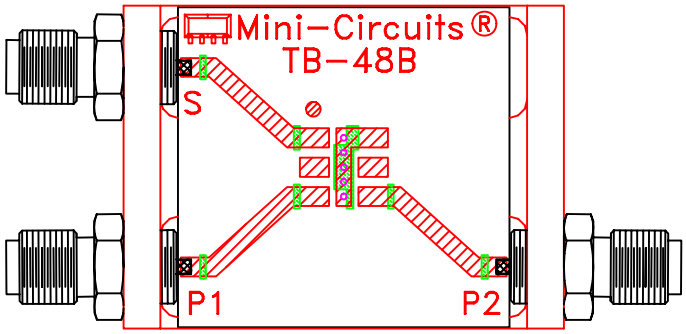
PL, hv, BK292/CD542/CD636, ADP/JPS, TB-48

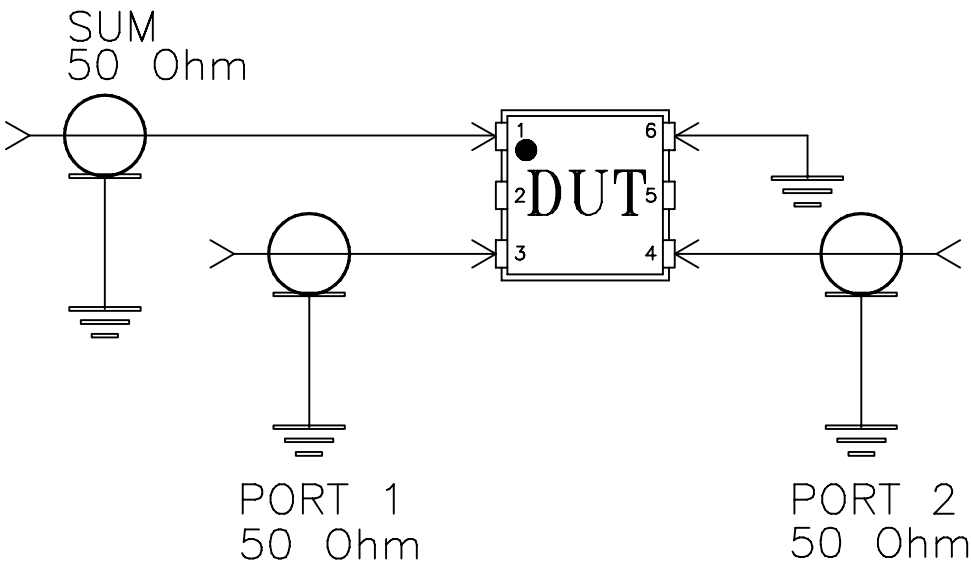
SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-035	C

FILE:	SCALE:	SHEET:
98PL035	10:1	1 OF 1

Evaluation Board and Circuit



TB-48+



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.

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Environmental Specifications ENV02T1

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215