

Engineering Development Model

Power Splitter/Combiner

ADP-ED7369/1

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : CD542

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		3		2400 MHz
Isolation	3 - 30 MHz		18	dB
	30 - 1200 MHz		18	dB
	1200 - 2400 MHz		19	dB
Insertion Loss Above 3.0 dB	3 - 30 MHz		0.52	dB
	30 - 1200 MHz		0.62	dB
	1200 - 2400 MHz		0.88	dB
Phase Unbalance	3 - 30 MHz		0.043	deg.
	30 - 1200 MHz		0.579	deg.
	1200 - 2400 MHz		0.568	deg.
Amplitude Unbalance	3 - 30 MHz		0.018	dB
	30 - 1200 MHz		0.027	dB
	1200 - 2400 MHz		0.060	dB
VSWR	SUM Port		1.82	(:1)
	OUT Ports		1.51	(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1	3
PORT 2	4
GND EXT	6
NOT USED	2, 5

Functional Diagram



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Typical Performance Data

FREQ. (MHz)	TOTAL LOSS ¹ (dB)		AMP. UNBAL. (dB)	ISOLATION (dB)	PHASE UNBAL. (deg.)	FREQ. (MHz)	VSWR (:1)		
	S-1	S-2					S	1	2
3.0	3.53	3.51	0.02	17.03	0.03	3.0	2.03	1.53	1.53
4.0	3.53	3.51	0.02	17.62	0.04	4.0	2.03	1.54	1.54
5.0	3.53	3.51	0.02	17.94	0.01	5.0	2.03	1.54	1.54
6.0	3.53	3.51	0.02	18.13	0.04	6.0	2.03	1.55	1.55
7.0	3.53	3.51	0.02	18.26	0.03	7.0	2.02	1.55	1.55
8.0	3.54	3.52	0.02	18.34	0.05	8.0	2.02	1.55	1.55
9.0	3.54	3.51	0.02	18.42	0.04	9.0	2.02	1.55	1.55
10.0	3.53	3.52	0.02	18.44	0.04	10.0	2.02	1.55	1.55
12.0	3.53	3.51	0.02	18.48	0.04	12.0	2.02	1.55	1.55
14.0	3.54	3.52	0.02	18.50	0.03	14.0	2.02	1.55	1.55
16.0	3.53	3.52	0.01	18.49	0.01	16.0	2.01	1.55	1.55
18.0	3.53	3.52	0.01	18.50	0.08	18.0	2.01	1.55	1.55
20.0	3.53	3.52	0.01	18.46	0.12	20.0	2.01	1.56	1.56
40.0	3.54	3.52	0.01	18.47	0.05	40.0	1.99	1.57	1.57
60.0	3.55	3.53	0.02	18.50	0.18	60.0	1.97	1.58	1.58
80.0	3.55	3.54	0.01	18.54	0.18	80.0	1.98	1.58	1.58
100.0	3.55	3.53	0.02	18.56	0.11	100.0	2.00	1.56	1.56
200.0	3.58	3.57	0.01	18.63	0.38	200.0	1.97	1.59	1.59
400.0	3.61	3.63	0.01	18.67	0.66	400.0	1.97	1.60	1.60
600.0	3.64	3.66	0.02	18.54	0.92	600.0	1.99	1.60	1.61
800.0	3.67	3.72	0.05	18.32	1.11	800.0	2.00	1.61	1.62
1000.0	3.70	3.75	0.05	18.07	1.13	1000.0	1.99	1.62	1.63
1200.0	3.74	3.81	0.07	17.94	1.07	1200.0	1.94	1.61	1.62
1400.0	3.82	3.87	0.05	17.99	0.75	1400.0	1.88	1.58	1.59
1600.0	3.86	3.87	0.01	18.28	0.72	1600.0	1.78	1.55	1.57
1800.0	3.88	3.85	0.03	18.90	0.61	1800.0	1.65	1.49	1.52
2000.0	3.85	3.79	0.06	19.74	0.43	2000.0	1.48	1.42	1.46
2050.0	3.86	3.81	0.05	19.96	0.41	2050.0	1.44	1.39	1.44
2100.0	3.87	3.83	0.05	20.01	0.25	2100.0	1.40	1.37	1.42
2150.0	3.87	3.82	0.05	19.95	0.03	2150.0	1.36	1.34	1.40
2200.0	3.89	3.83	0.06	19.65	0.16	2200.0	1.33	1.31	1.37
2250.0	3.92	3.85	0.07	19.23	0.36	2250.0	1.32	1.29	1.35
2300.0	3.96	3.88	0.09	18.63	0.64	2300.0	1.32	1.26	1.32
2350.0	4.02	3.93	0.09	17.82	0.81	2350.0	1.36	1.24	1.30
2400.0	4.09	3.99	0.10	16.86	1.14	2400.0	1.41	1.22	1.28

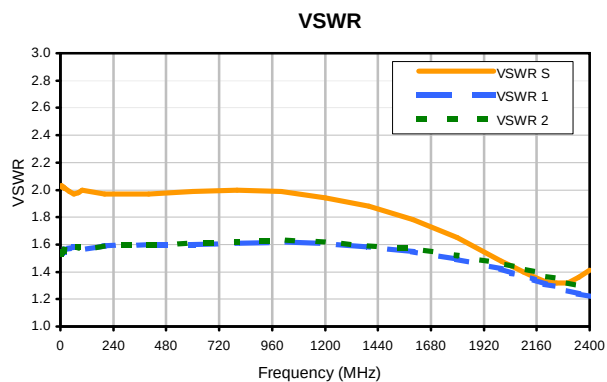
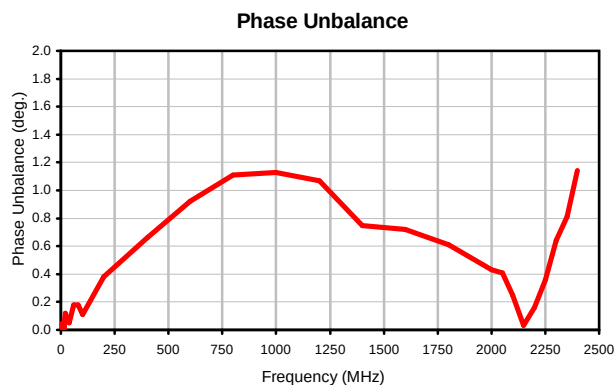
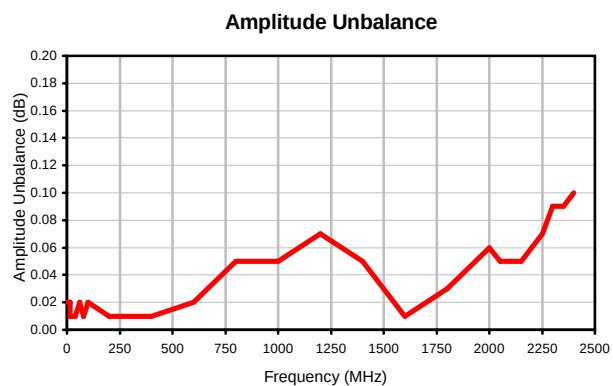
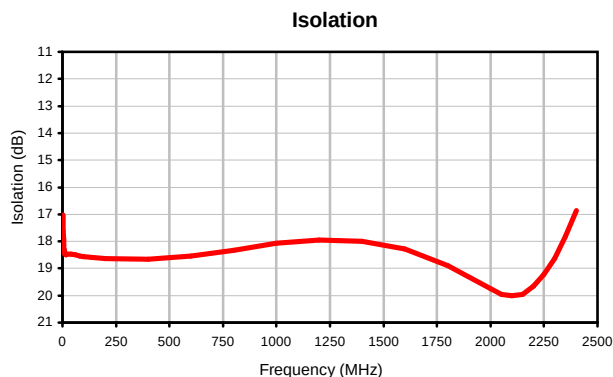
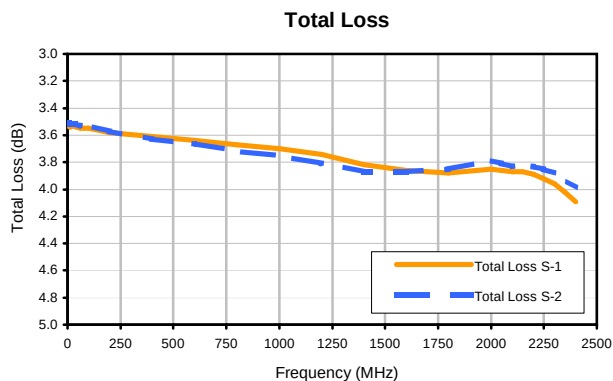
¹ Total Loss = Insertion Loss + 3dB Splitter Loss

REV. X2
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100707
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2 Way-0° Power Splitter/Combiner

ADP-ED7369/1

Typical Performance Curves



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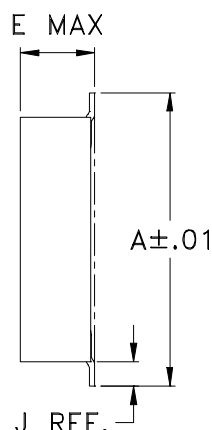
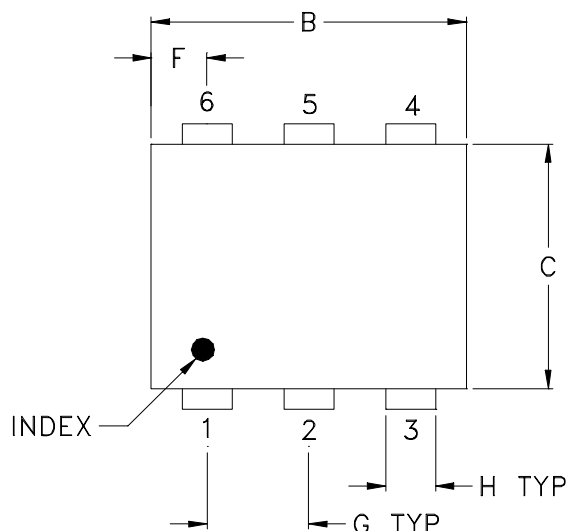
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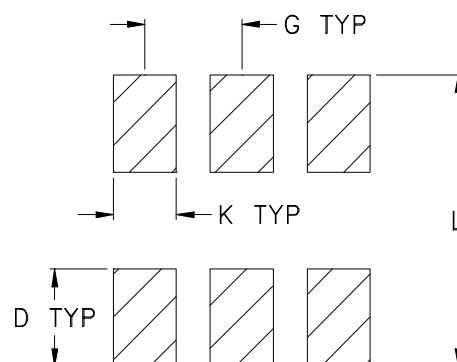


CD541
CD542
CD636
CD637

Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE#	A	B	C	D	E	F	G	H	J	K	L	WT, GRAM
CD541					.082 (2.08)							.15
CD542	.272 (6.91)	.310 (7.87)	.220 (5.58)	.100 (2.54)	.112 (2.84)	.055 (1.40)	.100 (2.54)	.030 (0.76)	.026 (0.66)	.065 (1.65)	.300 (7.62)	.20
CD636					.162 (4.11)							.25
CD637					.206 (5.23)							.40

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01$; 3 Pl. $\pm .005$

Notes:

- Case material: Plastic.
- Termination finish:
For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



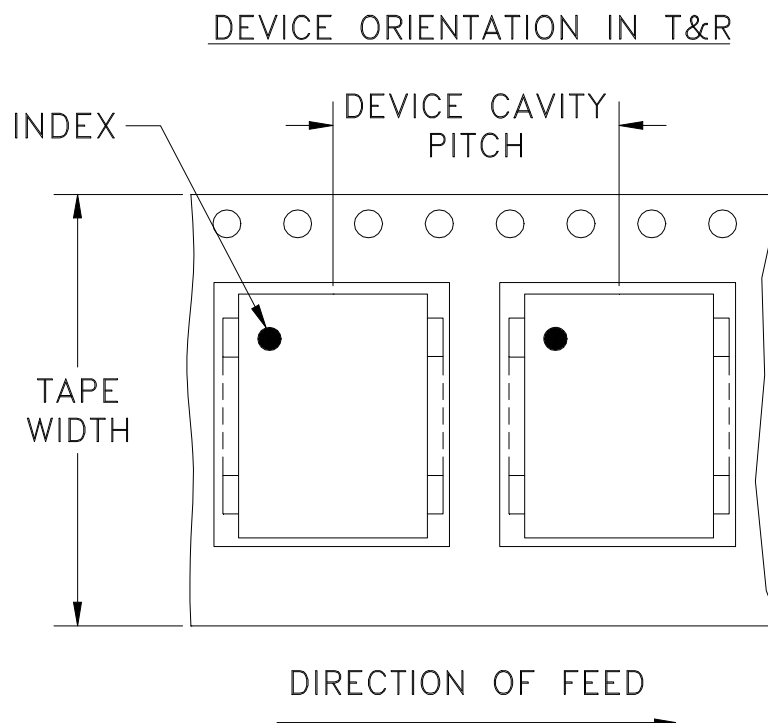
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Tape & Reel Packaging TR-F34



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel see note	
16	12	7	Small quantity standard (see note)	20
				50
				100
				200
		13	Standard	500
				1000

Note: Availability of small reel quantity varies by model.
Refer to pricing and availability on individual model dashboard.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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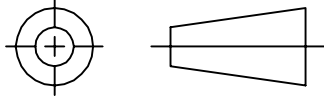
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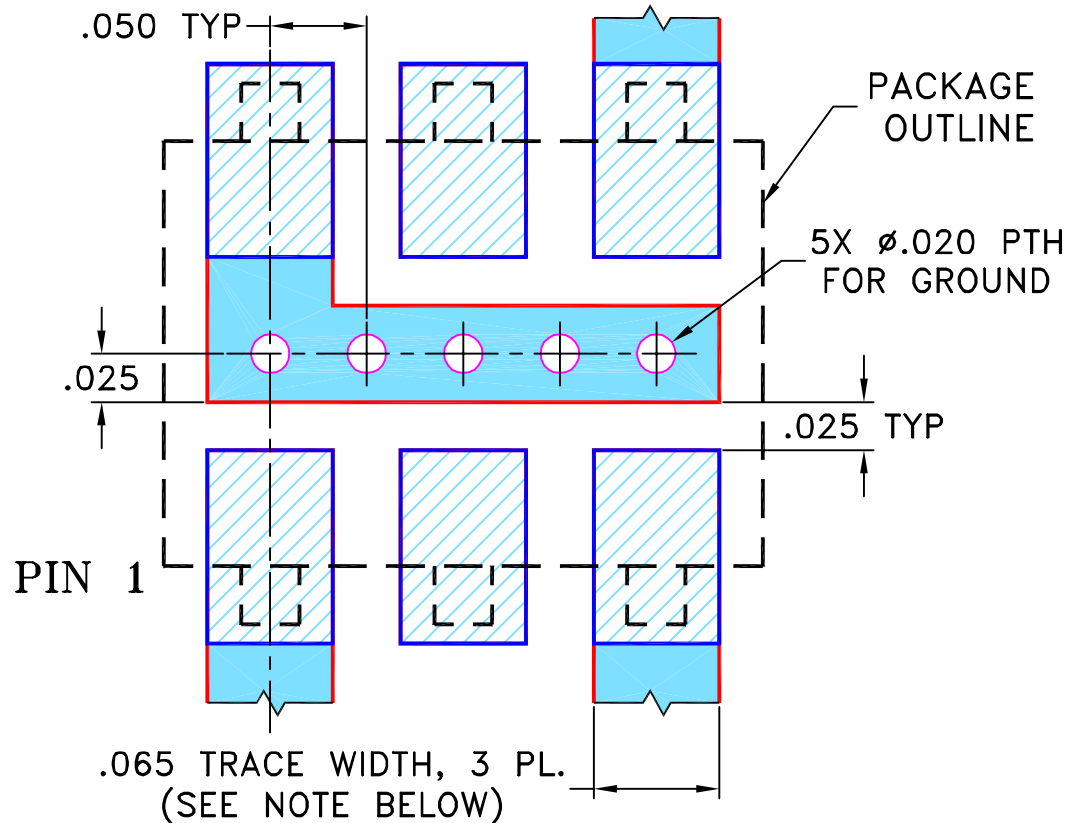
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M77049	NEW RELEASE	05/01	AV	CT
A	M82377	UPDATED DRAWING	07/31/02	AV	HY
B	M82846	UPDATED DRAWING	05/13/03	MMG	HY
C	M102713	ADDED BK292 CASE STYLE & "...WITH SMOBC"	01/17/06	MMG	IL

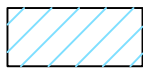
**SUGGESTED MOUNTING CONFIGURATION
FOR BK292/CD542/CD636 CASE STYLES,
"hv" PIN CONNECTION**



- NOTES:** 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .030" $\pm$.002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC
(SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005ANGLES $\pm$ FRACTIONS $\pm$

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DY

06/11/01

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PL, hv, BK292/CD542/CD636, ADP/JPS, TB-48

SIZE

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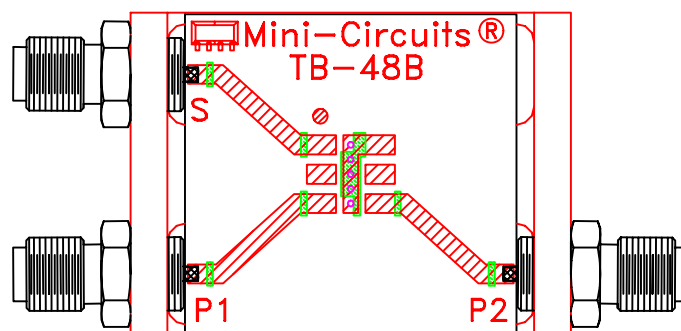
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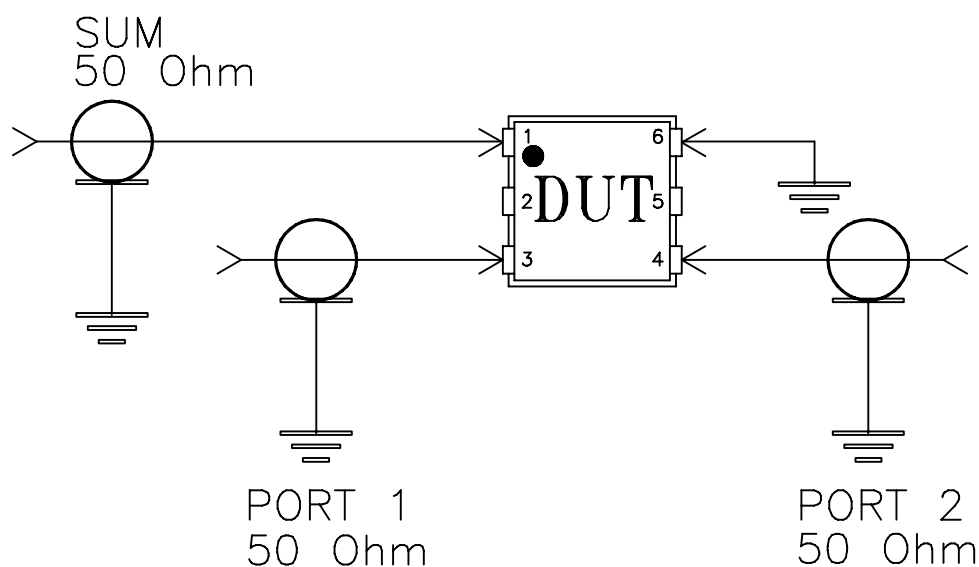
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ASHEETA1.DWG REV:A DATE:01/12/95

Evaluation Board and Circuit



TB-48+



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent,
Dielectric Constant=3.5, Thickness=.030 inch.

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All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215