Engineering Development Model

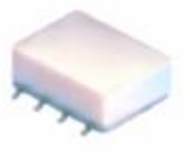
Power Splitter/Combiner ADQ-ED12533/1

2 Way-90°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.



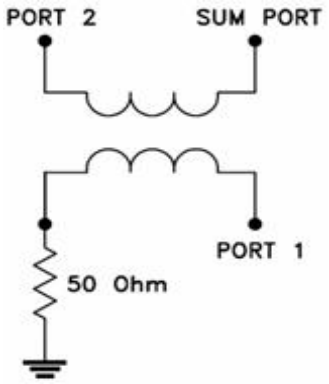
CASE STYLE : CJ725

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		100		300	MHz
Isolation	**100 - 300 MHz**		25		dB
Insertion Loss Average of Coupled Outputs above 3.0 dB	**100 - 300 MHz**		0.50		dB
Phase Unbalance	**100 - 300 MHz**		89.878		deg.
Amplitude Unbalance	**100 - 300 MHz**		0.976		dB
VSWR	**SUM Port**		1.15		(:1)
	OUT Ports		1.10		(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1	8
PORT 2	4
GND EXT	2,3,6,7
50Ω TERMINATION	5

Functional Diagram



2 Way-90° Power Splitter/Combiner ADQ-ED12533/1

Typical Performance Data

FREQ. (MHz)	TOTAL LOSS[1] (dB)			AMP. UNBAL. (dB)	ISOLATION (dB)	PHASE UNBAL. (deg.)	FREQ. (MHz)	VSWR (:1)		
	S-1	S-2	AVG.		1-2			S	1	2
60.0	0.85	8.16	4.51	7.31	36.09	89.57	60.0	1.03	1.04	1.03
70.0	1.08	7.14	4.11	6.07	34.55	89.58	70.0	1.03	1.04	1.03
80.0	1.31	6.33	3.82	5.01	33.43	89.60	80.0	1.04	1.04	1.03
100.0	1.79	5.12	3.46	3.34	31.65	89.61	100.0	1.04	1.05	1.04
110.0	2.02	4.68	3.35	2.66	30.89	89.62	110.0	1.05	1.05	1.04
120.0	2.25	4.32	3.29	2.07	30.14	89.63	120.0	1.05	1.05	1.05
125.0	2.35	4.16	3.26	1.81	29.79	89.65	125.0	1.05	1.05	1.05
130.0	2.45	4.02	3.24	1.57	29.50	89.64	130.0	1.05	1.05	1.05
135.0	2.55	3.89	3.22	1.34	29.23	89.65	135.0	1.06	1.06	1.06
139.0	2.63	3.80	3.22	1.17	29.05	89.65	139.0	1.06	1.06	1.06
140.0	2.65	3.78	3.22	1.13	29.01	89.66	140.0	1.06	1.06	1.06
142.0	2.68	3.73	3.21	1.05	28.90	89.66	142.0	1.06	1.06	1.06
144.0	2.72	3.69	3.21	0.97	28.82	89.66	144.0	1.06	1.06	1.06
146.0	2.75	3.65	3.20	0.90	28.73	89.67	146.0	1.07	1.06	1.06
148.0	2.79	3.61	3.20	0.83	28.63	89.67	148.0	1.07	1.06	1.06
150.0	2.82	3.58	3.20	0.76	28.53	89.67	150.0	1.07	1.06	1.07
154.0	2.88	3.51	3.20	0.62	28.33	89.68	154.0	1.07	1.06	1.07
158.0	2.94	3.45	3.20	0.50	28.15	89.69	158.0	1.07	1.06	1.07
160.0	2.97	3.42	3.20	0.44	28.04	89.70	160.0	1.07	1.06	1.07
170.0	3.10	3.29	3.20	0.19	27.59	89.73	170.0	1.08	1.07	1.08
180.0	3.21	3.20	3.21	0.01	27.17	89.75	180.0	1.09	1.07	1.09
190.0	3.29	3.14	3.22	0.15	26.81	89.78	190.0	1.09	1.07	1.09
200.0	3.35	3.10	3.23	0.24	26.46	89.80	200.0	1.10	1.08	1.10
210.0	3.37	3.10	3.24	0.27	26.13	89.83	210.0	1.11	1.08	1.10
220.0	3.38	3.13	3.26	0.25	25.77	89.87	220.0	1.11	1.08	1.11
230.0	3.34	3.18	3.26	0.16	25.41	89.92	230.0	1.12	1.08	1.12
240.0	3.28	3.27	3.28	0.01	25.06	89.98	240.0	1.12	1.09	1.12
250.0	3.18	3.40	3.29	0.22	24.73	90.06	250.0	1.12	1.09	1.12
260.0	3.06	3.59	3.33	0.52	24.40	90.13	260.0	1.13	1.09	1.12
262.0	3.04	3.63	3.34	0.60	24.35	90.14	262.0	1.13	1.09	1.12
264.0	3.01	3.68	3.35	0.67	24.29	90.16	264.0	1.13	1.09	1.12
266.0	2.97	3.72	3.35	0.75	24.23	90.18	266.0	1.13	1.09	1.12
268.0	2.94	3.77	3.36	0.83	24.17	90.20	268.0	1.13	1.08	1.12
269.0	2.92	3.80	3.36	0.88	24.14	90.21	269.0	1.13	1.08	1.12
270.0	2.91	3.83	3.37	0.92	24.12	90.22	270.0	1.13	1.08	1.12
280.0	2.71	4.13	3.42	1.42	23.84	90.31	280.0	1.12	1.08	1.12
290.0	2.48	4.54	3.51	2.06	23.66	90.41	290.0	1.11	1.07	1.11
300.0	2.22	5.08	3.65	2.86	23.56	90.53	300.0	1.10	1.06	1.10
310.0	1.94	5.82	3.88	3.88	23.64	90.68	310.0	1.08	1.04	1.08
320.0	1.64	6.83	4.24	5.19	23.97	90.92	320.0	1.06	1.02	1.06
330.0	1.34	8.28	4.81	6.94	24.68	91.32	330.0	1.02	1.01	1.03

[1] Total Loss = Insertion Loss + 3dB Splitter Loss

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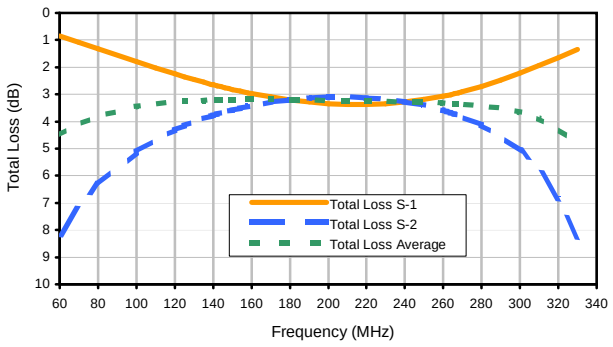
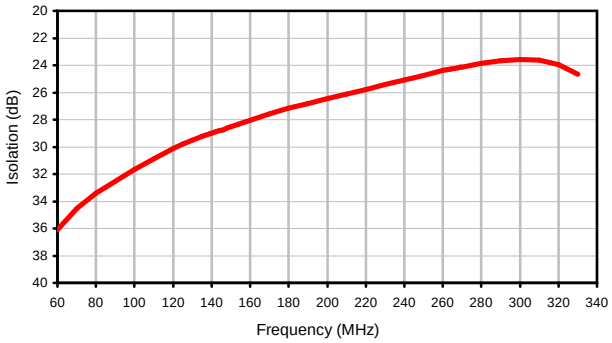
Typical Performance Curves

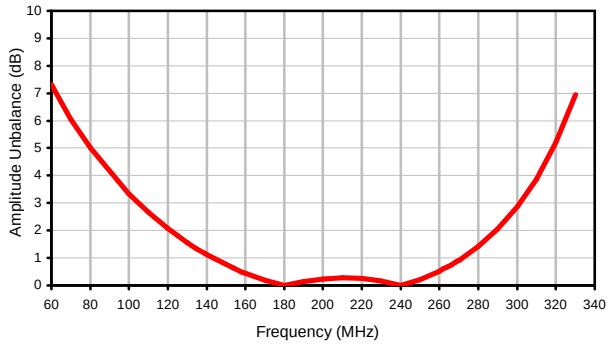
Total Loss



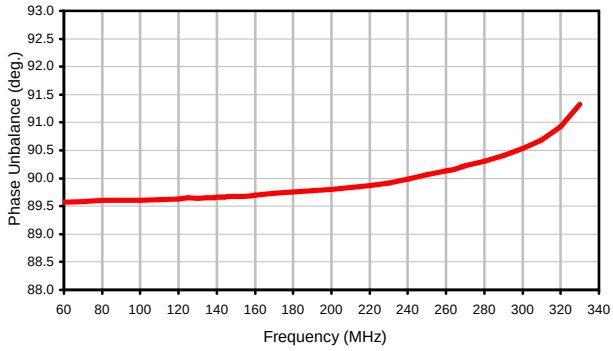
Isolation



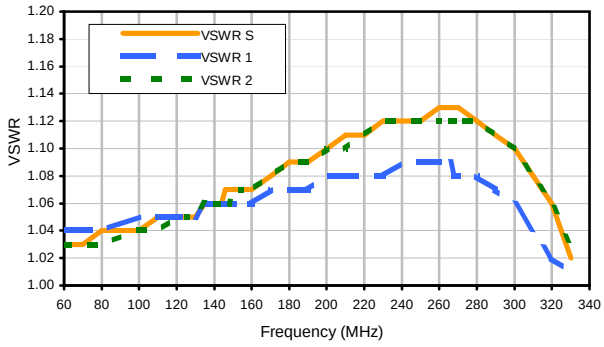
Amplitude Unbalance



Phase Unbalance



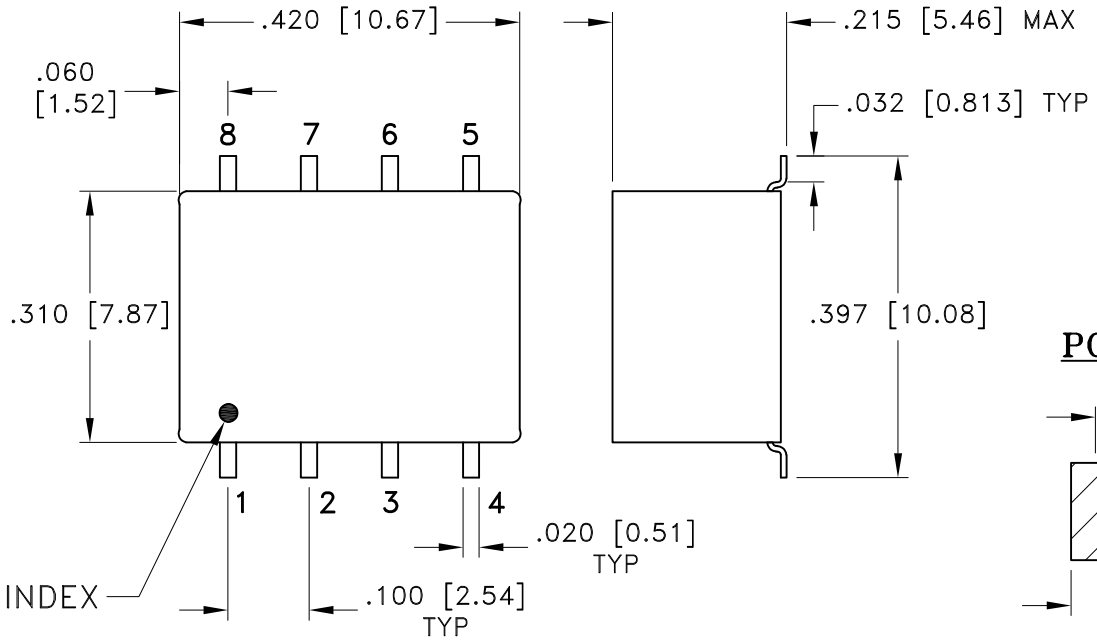
VSWR



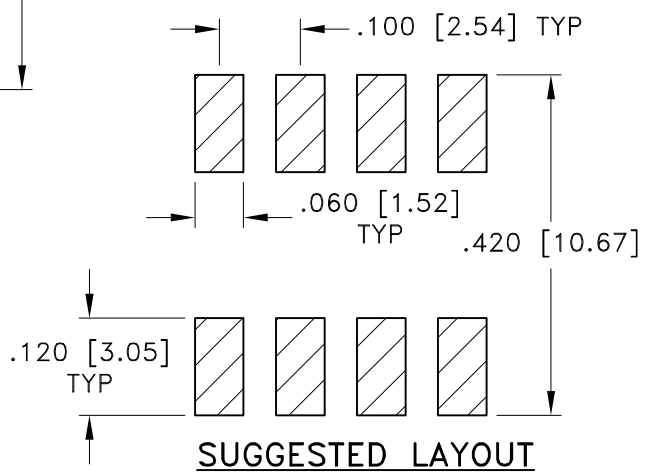
Case Style CJ

Outline Dimensions

CJ725



PCB Land Pattern



SUGGESTED LAYOUT
FOR PCB LAND PATTERN
PATTERN TO BE WITHIN ±.002

METALLIZATION

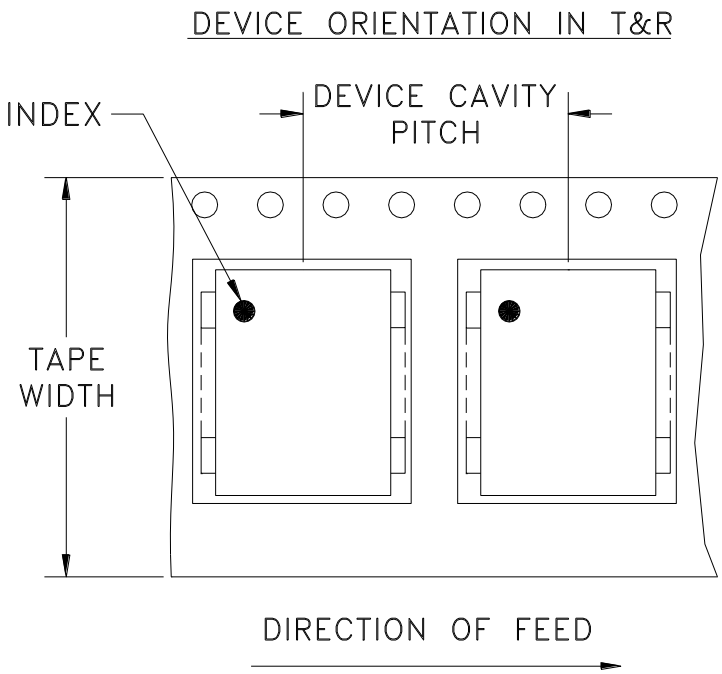
Weight: .40 gram

Dimensions are in inches [mm]. Tolerances: 2 Pl.±.01; 3 Pl. ±.005 Inch

Notes:

1. Case material: Plastic.
2. Termination finish:
   Tin plate over Nickel plate.

Tape & Reel Packaging TR-F10



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel
24	16	7	10,20,50,100
		13	200,500

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

Note: Please consult individual model data sheet to determine device per reel availability.

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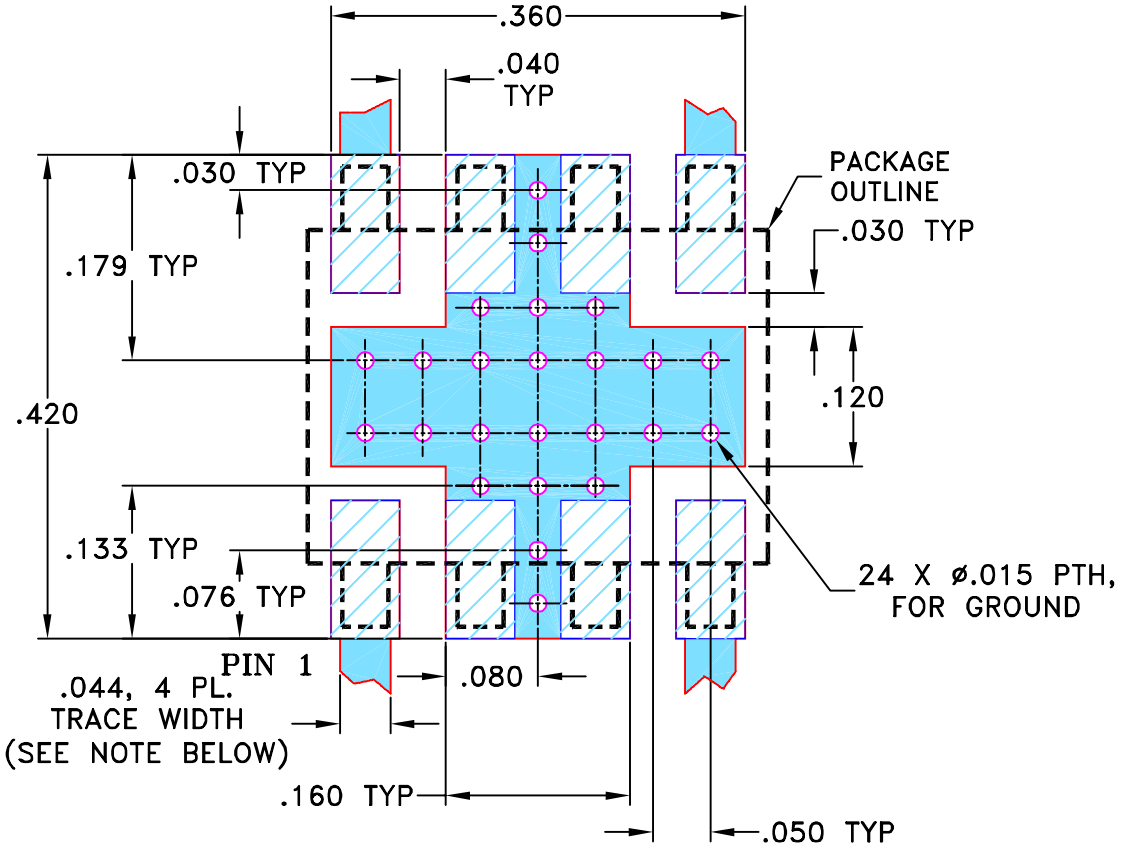
THIRD ANGLE PROJECTION

REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M82272	NEW RELEASE	08/06/02	GF	DJ
A	M102713	UPDATED NOTES, ADDED "...WITH SMOBC"	01/16/06	GT	IL

SUGGESTED MOUNTING CONFIGURATION FOR CJ725 CASE STYLE, "ma", "nf" PIN CONNECTIONS



NOTES:
1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS 0.020" ± 0.0015". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	GF	07/18/02
TOLERANCES ON:	CHECKED	HY	08/01/02
2 PL DECIMALS ±	APPROVED	DJ	08/06/02
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

ASHEETA1.DWG REV:A DATE:01/12/95

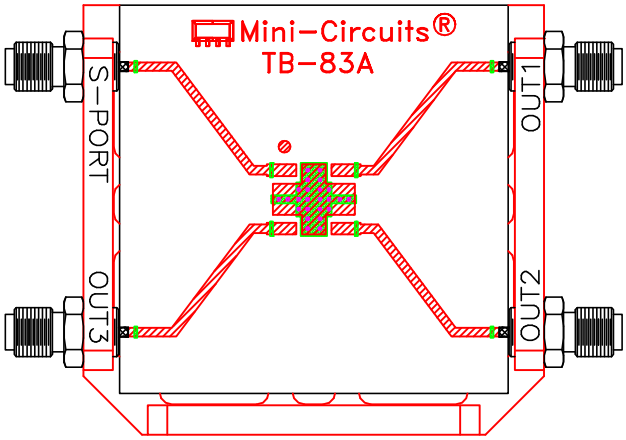
Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

PL, ma/nf, CJ725, AD3PS/ADQ, TB-83

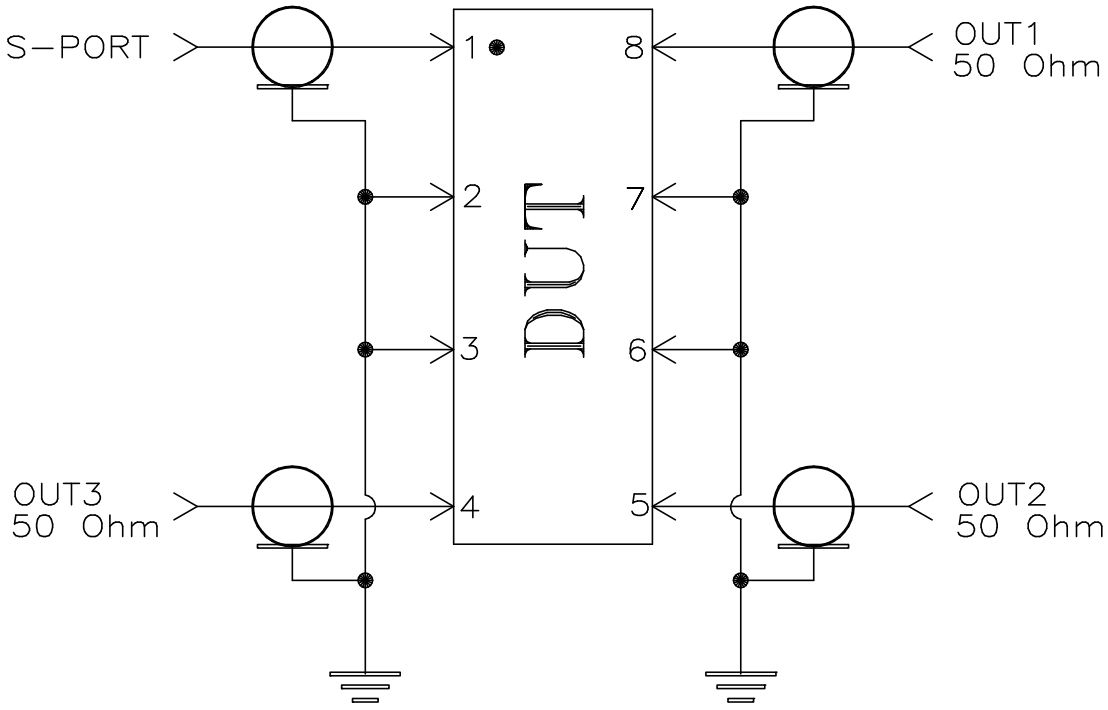
SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-063	A

FILE:	SCALE:	SHEET:
98PL063	6:1	1 OF 1

Evaluation Board and Circuit



TB-83



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.020 inch.

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Environmental Specifications ENV02T1

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215