### Surface Mount

# **RF Transformer**

**ADT2-71T+** 

 $50\Omega$  0.005 to 70 MHz

## **The Big Deal**

- Low insertion loss, 0.9 dB
- Low unbalance, 0.5 dB, 3°
- Small size, 0.27 x 0.31 x 0.22"



CASE STYLE: CD637

### **Product Overview**

Mini-Circuits' ADT2-71T+ is a  $50\Omega$  DC isolated surface-mount transformer with a secondary/primary impedance ratio of 2:1 and a center tap on the secondary winding. This model covers the 0.005 to 70 MHz band with low insertion loss (0.9 dB) as well as low phase unbalance (3°) and amplitude unbalance (0.5 dB). The unit comes enclosed in a miniature, 6-lead plastic package measuring just 0.27 x 0.31 x 0.22", ideal for dense circuit board layouts.

## **Key Features**

Feature	Advantages
DC isolation	Provides DC isolation between circuits and efficient AC transmission, eliminating the need for external DC biasing components.
Secondary center tap	Allows DC feed up to 30 mA and DC bias without adding bias tees into the signal chain.
Low insertion loss, 0.9 dB	Excellent transmission of signal power from input to output.
Low phase and amplitude unbalance, $3^{\circ}$ , 0.5 dB	Low phase and amplitude unbalance can improve a system's electromagnetic compatibility by rejecting unwanted common-mode noise.
Small footprint, 0.27 x 0.31 x 0.22"	Accommodates tight space requirements for dense PCB layouts.

#### Notes

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.

B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.

C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.ninicircuits.com/MCLStore/terms.jsp

# **RF Transformer**

## ADT2-71T+

#### 0.005 to 70 MHz 50Q

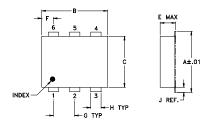
#### **Maximum Ratings**

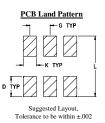
Operating Temperature	-20°C to 85°C
Storage Temperature	-55°C to 100°C
RF Power	0.25W
DC Current	30mA
Permanent damage may occur if any of t	hese limits are exceeded.

#### **Pin Connections**

PRIMARY DOT	3
PRIMARY	1
SECONDARY DOT	4
SECONDARY	6
SECONARY CT	5
NOT USED	2

#### **Outline Drawing**





#### Outline Dimensions (inch )

G	F	<b>E</b>	D	C	<b>B</b>	A
.100	.055	. <b>206</b>	.100	. <b>220</b>	. <b>310</b>	.272
2.54	1.40	5.23	2.54	5.59	7.87	6.91
wt grams 0.40			L .300 7.62	.065 1.65	J . <b>026</b> 0.66	H .030 0.76

Demo Board MCL P/N: TB-430

# Config. A O SEC PRI

#### **Features**

- wideband, 0.005 to 70 MHz
- excellent amplitude unbalance, 0.2 dB typ.
- and phase unbalance, 2 deg. typ. in 1 dB bandwidth
- aqueous washable
- plastic base with leads

# **Applications**

- impedance matching
- balanced to unbalanced transformation
- · push-pull amplifiers

Generic photo used for illustration purposes only

CASE STYLE: CD637

#### +RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

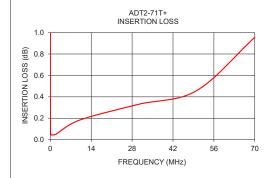


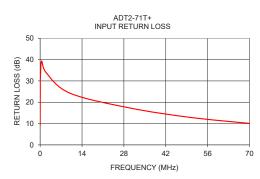
#### Electrical Specifications @25°C

Parameter	Frequency (MHz)	Min.	Тур.	Max.	Unit	
Impedance Ratio (secondary/primary)			2			
Frequency Range		0.005	_	70	MHz	
Insertion Loss	0.01 - 50	_	0.4	1.3	dB	
Insertion Loss	0.005 - 70	_	0.9	1.8		
Amplitude Unbalance	0.01 - 50	_	0.2	0.5	dB	
Amplitude officialities	0.005 - 70	_	0.5	1.0	uБ	
Phase Unbalance	0.01 - 50	_	1.0	5.0	Degree	
Filase Olibalatice	0.005 - 70	_	3.0	8.0	Degree	

#### **Typical Performance Data**

FREQUENCY (MHz)	INSERTION LOSS (dB)	INPUT R. LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE (Deg.)
0.005	1.00	9.12	0.00	0.03
0.010	0.39	13.90	0.00	0.02
0.050	0.07	26.83	0.00	0.02
0.100	0.05	32.43	0.00	0.03
0.500	0.04	39.29	0.00	0.03
2.000	0.05	33.89	0.00	0.06
10.000	0.18	24.23	0.02	0.24
30.000	0.33	17.30	0.09	0.73
50.000	0.46	12.93	0.25	1.55
70.000	0.96	10.08	0.54	2.82





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# Typical Performance Data

FREQUENCY	AVERAGE INSERTION LOSS	INPUT RETURN LOSS	AMPLITUDE UNBALANCE	PHASE UNBALANCE
MHz	(dB)	(dB)	(dB)	(deg.)
0.003	1.99	6.03	0.01	0.08
0.004	1.35	7.71	0.00	0.04
0.005	1.00	9.12	0.00	0.03
0.010	0.39	13.90	0.00	0.02
0.011	0.34	14.61	0.00	0.04
0.02	0.16	19.28	0.00	0.02
0.05	0.07	26.83	0.00	0.02
0.08	0.06	30.72	0.00	0.03
0.10	0.05	32.43	0.00	0.03
0.20	0.05	37.13	0.00	0.02
0.50	0.04	39.29	0.00	0.03
0.80	0.04	38.12	0.00	0.05
2	0.05	33.89	0.00	0.06
5	0.11	28.53	0.01	0.13
8	0.15	25.61	0.01	0.20
10	0.18	24.23	0.02	0.24
20	0.29	19.94	0.05	0.46
30	0.33	17.30	0.09	0.73
40	0.35	15.02	0.16	1.09
50	0.46	12.93	0.25	1.55
60	0.68	11.24	0.38	2.13
70	0.96	10.08	0.54	2.82
80	1.19	9.32	0.74	3.70
90	1.34	8.72	0.98	4.88
100	1.45	8.08	1.30	6.57

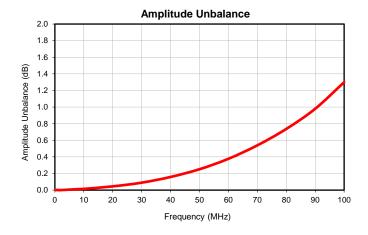


Page 1 of 1

# Typical Performance Data







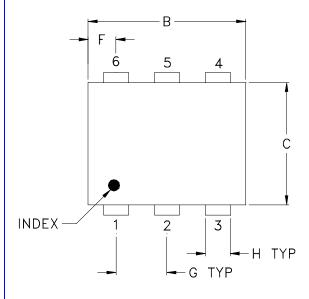


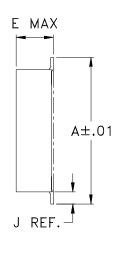
# Case Style

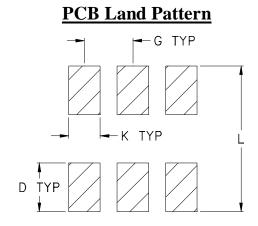
# CD

### **Outline Dimensions**

CD541 CD542 CD636 CD637







Suggested Layout, Tolerance to be within ±.002

CASE#	A	В	С	D	E	F	G	Н	J	K	L	WT, GRAM
CD541					.082 (2.08)							.15
CD542	.272	.310	.220	.100	.112 (2.84)	.055	.100	.030	.026	.065	.300	.20
CD636	(6.91)	(7.87)	(5.58)	(2.54)	.162 (4.11)	(1.40)	(2.54)	(0.76)	(0.66)	(1.65)	(7.62)	.25
CD637					.206 (5.23)							.40

Dimensions are in inches (mm). Tolerances: 2 Pl.  $\pm$  .01; 3 Pl.  $\pm$  .005

#### **Notes:**

1. Case material: Plastic.

2. Termination finish:

For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.

For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



INTERNET http://www.minicircuits.com

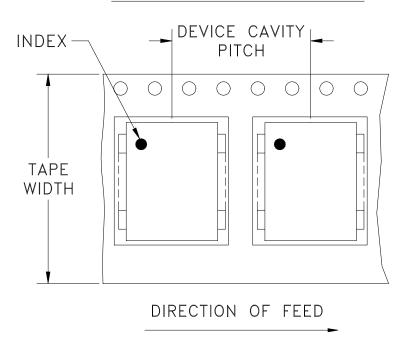
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Mini-Circuits ISO 9001 & ISO 14001 Certified

# Tape & Reel Packaging TR-F46

### DEVICE ORIENTATION IN T&R



Tape Width,	<b>Device Cavity</b>	Reel Size,	Devices per Reel
mm	Pitch, mm	inches	
16	12	13	900

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



INTERNET http://www.minicircuits.com

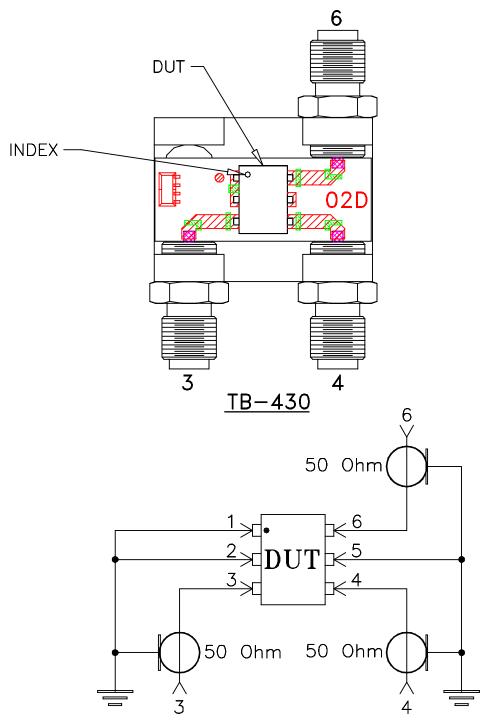
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# Evaluation Board and Circuit

For Pin Connections refer to Data Sheet of the DUT



Schematic Diagram

# Notes:

- 1. SMA Female connectors.
- 2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.
- 3. Must use ENA/PNA type agilent's network analyzers with impedance conversion option to convert ports to appropriate impedances.

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### **Environmental Specifications**

ENV02

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-20° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215

ENV02 Rev: A

02/25/11

M130240 File: ENV02.pdf

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