



Bandpass Filter & Balun **BBFCG2-362+**

50Ω 3250 to 3950 MHz

THE BIG DEAL

- Tiny size, (0805)
- Compact design includes Balun & Filter in one package
- Low cost
- Temperature stable
- Hermetically sealed



Generic photo used for illustration purposes only

CASE STYLE: GE0805C-15

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our website for methodologies and qualifications

APPLICATIONS

- Telecommunications
- 5G sub 6GHz

PRODUCT OVERVIEW

Mini-Circuits' BBFCG2-362+ is a tiny ceramic RF balun filter with an impedance ratio of 1:2, covering a variety of wireless communications applications from 3250 to 3950 MHz. This model provides low insertion loss, low phase unbalance (relative to 180°), low amplitude unbalance. Fabricated using LTCC technology, the unit comes housed in a tiny, rugged ceramic package (0.079" x 0.049" x 0.037") suitable for harsh operating environments.

KEY FEATURES

Feature	Advantages
Compact Design	Integrates filter and balun in one tiny package
Tiny size, 0805	Accommodates tight space requirements for dense PCB layouts.
LTCC construction	LTCC process enables tiny size and low cost, suitable for high-volume production. Rugged ceramic package provides excellent reliability in harsh operating environments.



CERAMIC

Bandpass Filter & Balun **BBFCG2-362+**

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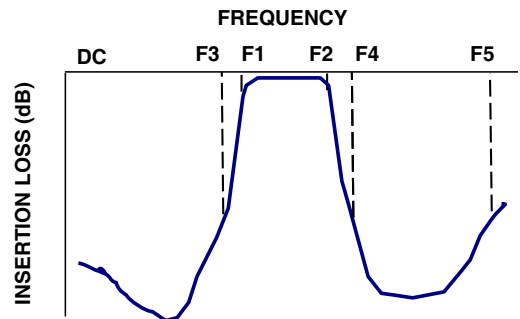
ELECTRICAL SPECIFICATIONS AT 25°C

Parameter	F#	Frequency (MHz)	Min.	Typ.	Max.	Units
Impedance Ratio	-	-	2			
Insertion Loss	F1-F2	3250 - 3950	-	-	3	dB
Return Loss	Unbalanced Port	F1-F2	8.5	-	-	dB
	Balanced Port	F1-F2	8.5	-	-	dB
Stopband Rejection	DC-F3	0 - 2498	27	-	-	dB
		2498 - 2598	21	-	-	
		2598 - 2648	15	-	-	
	F4-F5	7846 - 9848	27	-	-	
Amplitude Unbalance ±	F1-F2	3250 - 3950	-1.5	-	1.5	dB
Phase Unbalance	F1-F2	3250 - 3950	-13	-	13	Degree

MAXIMUM RATINGS

Parameter	Ratings
Operating Temperature	-55°C to 125°C
Storage Temperature	-55°C to 125°C
RF Power Input	0.5W at 25°C

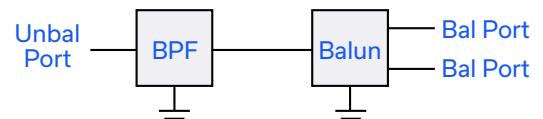
TYPICAL FREQUENCY RESPONSE



DC INTERFACE TABLE

Unbalance Port - GND	DC short
Unbalance Port - Balance Ports	DC open
Balance port - GND	DC open
Balance port-Balance Port	DC short

FUNCTIONAL SCHEMATIC





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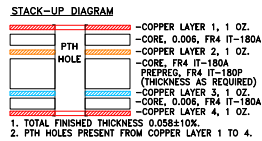
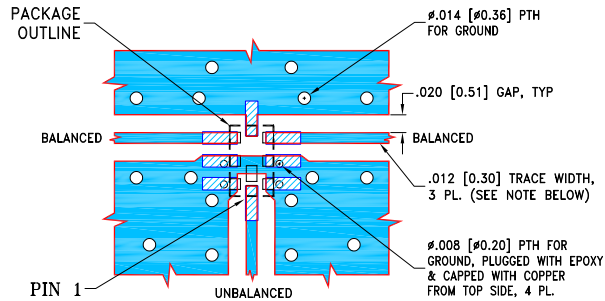
Bandpass Filter & Balun **BBFCG2-362+**

PAD CONNECTIONS

UNBALANCED PORT	1
BALANCED PORT	4,6
GROUND	2,3,7,8
NOT CONNECT OR GND	5

PRODUCT MARKING: N/A

EVALUATION BOARD MCL P/N: TB-BBFCG2-362+
SUGGESTED PCB LAYOUT (PL-711)

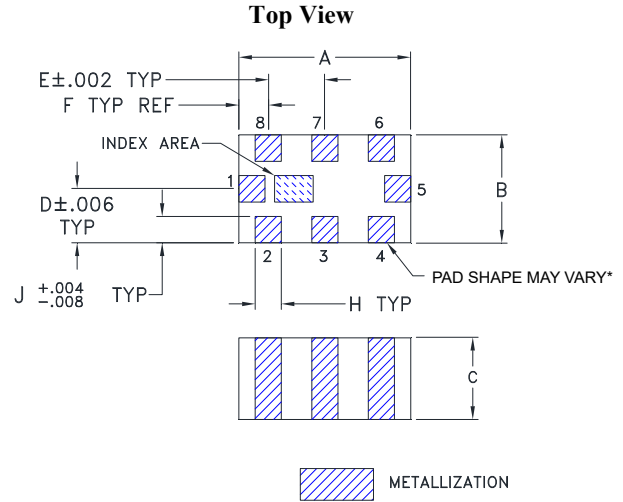


NOTES:

- TRACE WIDTH AND GAP ARE SHOWN FOR LAYER 1 (SEE MULTILAYER STACK-UP DIAGRAM). FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
- COPPER LAYERS 2,3,4 OF THE PCB ARE CONTINUOUS GROUND PLANE.

- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).
- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

OUTLINE DRAWING



*During the manufacturing process, the pad shape may not be rectangular and may take on a more semi-circle shape. However, the pad dimensions reflect this, with the pad shape being within the specified lengths. The metallization compensates accordingly and so performance will not be affected. In addition, solderability will not be influenced by the pad shape.

OUTLINE DIMENSIONS (Inches / mm)

A	B	C	D	E	F	G	H	J	wt
.079	.049	.037	.025	.026	.014	.110	.012	.010	grams
2.00	1.25	0.95	0.63	0.65	0.35	2.80	0.30	0.25	.008



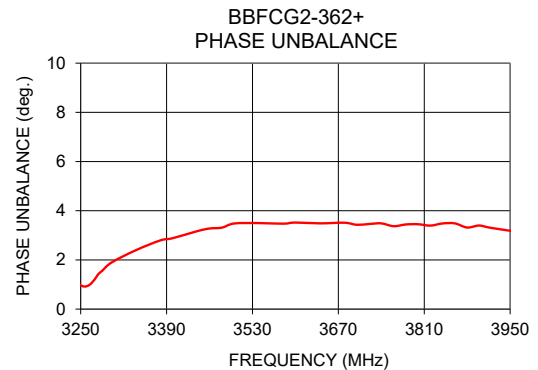
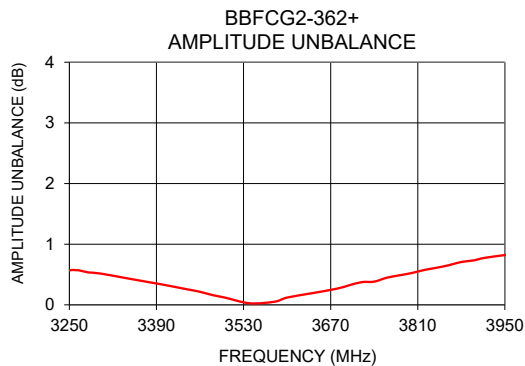
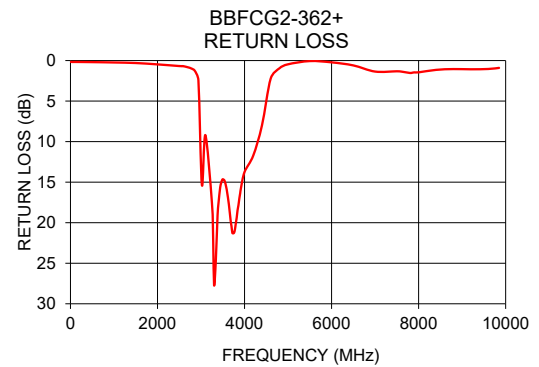
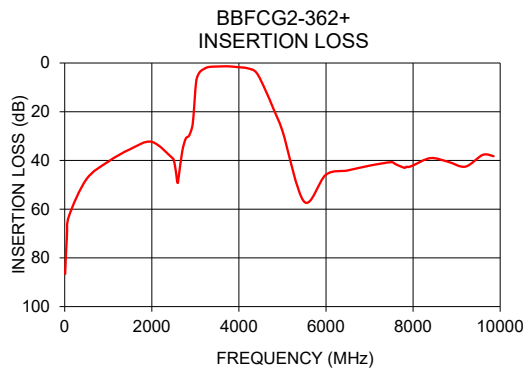
CERAMIC

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TYPICAL PERFORMANCE DATA

Frequency (MHz)	Insertion Loss (dB)	Return Loss (dB)	Amplitude Unbalance (dB)	Phase Unbalance (Deg.)
10	86.57	0.16	14.06	171.60
1000	40.48	0.24	36.79	140.94
2498	39.44	0.68	14.37	168.83
2598	49.26	0.71	1.42	150.04
3250	1.93	17.50	0.57	0.97
3920	1.59	15.66	0.78	3.30
5500	56.94	0.06	0.08	167.53
6000	45.83	0.22	3.73	179.96
7000	42.13	1.36	2.20	178.31
8000	42.04	1.45	1.68	177.96
9200	42.54	1.08	1.06	172.86
9848	38.28	0.90	2.75	178.80



- NOTES**
- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
 - B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
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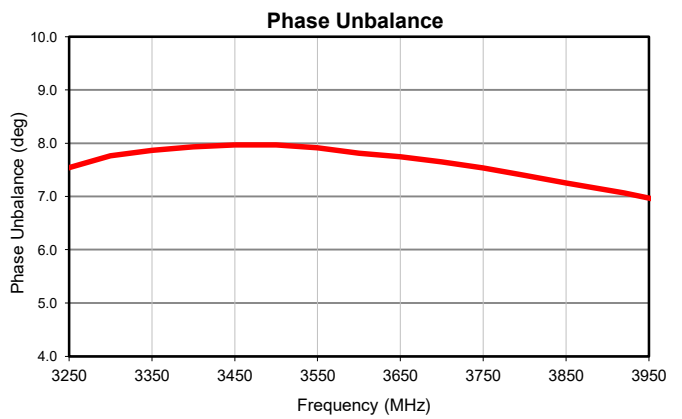
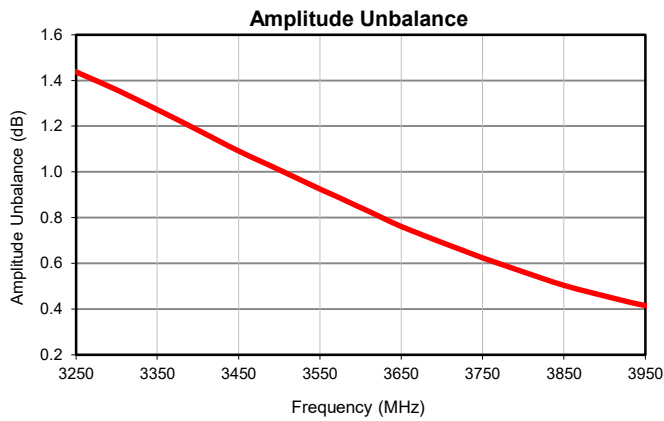
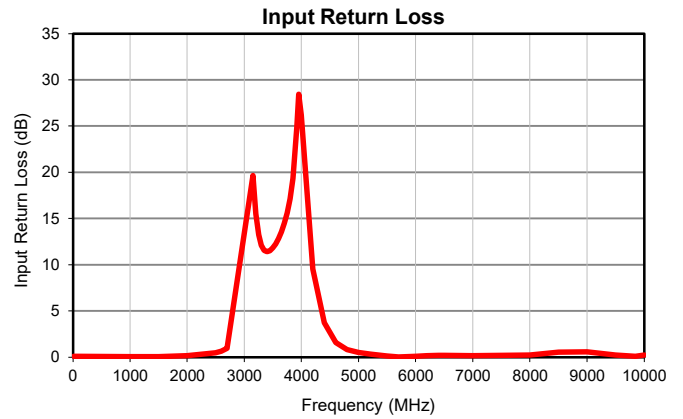
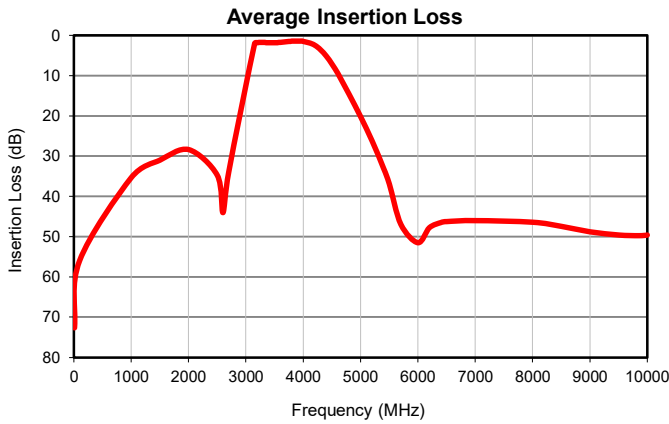


Ceramic Bandpass Filter & Balun BBFCG2-362+

Typical Performance Data

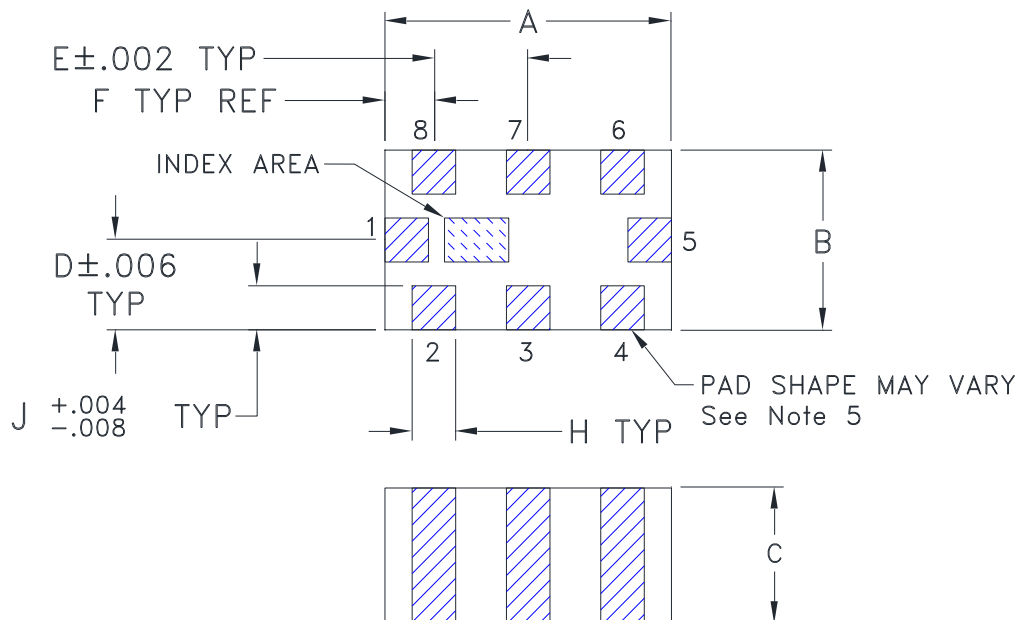
FREQUENCY (MHz)	INSERTION LOSS (dB)	RETURN LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE (deg.)
10	72.66	0.10	1.56	72.45
100	55.91	0.08	0.96	0.58
1000	35.31	0.04	1.68	2.54
1500	30.96	0.05	7.92	26.71
2000	28.39	0.14	2.13	104.28
2498	34.68	0.46	9.79	104.34
2598	44.04	0.62	7.13	160.75
2700	33.89	0.94	0.46	39.01
3150	1.97	19.65	1.58	6.70
3200	1.76	15.48	1.51	7.20
3250	1.71	13.24	1.44	7.54
3300	1.73	12.09	1.36	7.76
3350	1.77	11.55	1.27	7.87
3400	1.80	11.40	1.18	7.93
3450	1.81	11.51	1.09	7.97
3500	1.79	11.82	1.01	7.97
3550	1.76	12.25	0.93	7.91
3600	1.70	12.82	0.84	7.81
3650	1.63	13.57	0.76	7.74
3700	1.56	14.47	0.69	7.65
3750	1.49	15.61	0.62	7.54
3800	1.43	17.12	0.56	7.40
3850	1.39	19.32	0.50	7.25
3920	1.38	24.86	0.44	7.06
3950	1.39	28.46	0.42	6.97
4000	1.45	26.21	0.38	6.80
4200	2.41	9.53	0.33	6.06
4400	5.12	3.72	0.42	5.44
4600	9.45	1.57	0.71	5.29
4800	14.60	0.82	1.30	6.49
5000	20.18	0.51	2.44	10.47
5200	26.21	0.33	4.71	20.61
5400	33.02	0.19	9.55	50.61
5500	36.93	0.13	12.28	92.37
5700	46.94	0.03	6.06	172.14
6000	51.48	0.08	2.06	152.98
6200	47.79	0.14	5.98	132.82
6400	46.47	0.18	8.90	97.03
6500	46.20	0.19	8.69	69.46
7000	45.97	0.16	11.58	1.59
8000	46.43	0.22	4.47	177.37
8500	47.46	0.53	2.70	175.25
9000	48.75	0.57	1.88	173.27
9500	49.55	0.22	1.63	172.05
9848	49.76	0.08	1.46	171.30
10000	49.61	0.21	1.44	171.10

Typical Performance Data



Outline Dimensions

Top View



CASE#	A	B	C	D	E	F	G	H	J	WT, GRAM
GE0805C-15	.079 (2.00)	.049 (1.25)	.037 (0.95)	.025 (0.63)	.026 (0.65)	.014 (0.35)	.110 (2.80)	.012 (0.30)	.010 (0.25)	.008

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01(.25)$; 3 Pl. $\pm .005(.100)$

Notes:

- Open style, ceramic base
- Termination finish: For RoHS Case Styles: Tin plate over Nickel plate. All model, (+) suffix.
For RoHS-5 Case Styles: Tin-lead plate over Nickel plate. All model, no (+) suffix.
- Pad tolerance to be non-cumulative. Minimum spacing between each pad is .004(0.1).
- Pin numbers do not appear on unit. For reference only.
- During the manufacturing process, the pad shape may not be rectangular and may take on a more semi-circle shape. However, the pad dimensions reflect this, with the pad shape being within the specified lengths. The metallization compensates accordingly and so performance will not be affected. In addition, solderability will not be influenced by the pad shape

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Tape & Reel Packaging TR-F114

DEVICE ORIENTATION IN T&R



ILLUSTRATION 1

Applicable Case Styles	
GE0805C	JC0603C
GE0805C-1	JC0603C-4
GE0805C-1AP	JC0603C-6
GE0805C-7	
GE0805C-9	
GE0805C-10	
GE0805C-11	
GE0805C-12	



ILLUSTRATION 2

Applicable Case Styles	
GE0805C-2	JC0603C-1
GE0805C-3	JC0603C-2
GE0805C-4	JC0603C-3
GE0805C-5	JC0603C-5
GE0805C-6	JC0603C-7
GE0805C-8	
GE0805C-15	

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
			Standard	1000
			Standard	4000

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



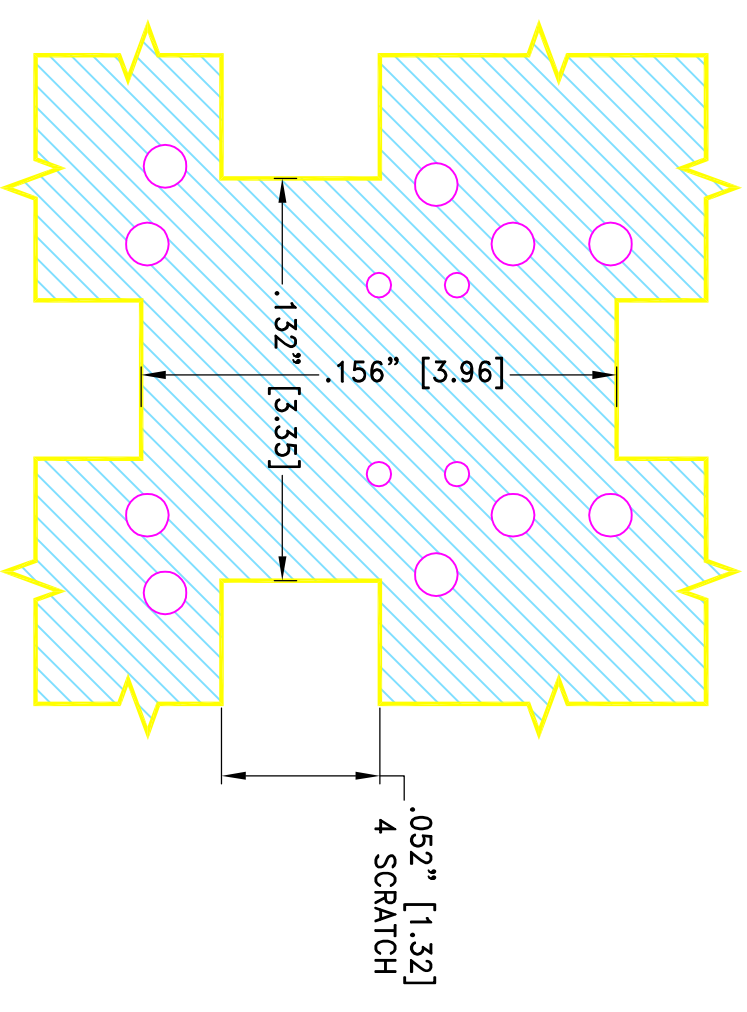
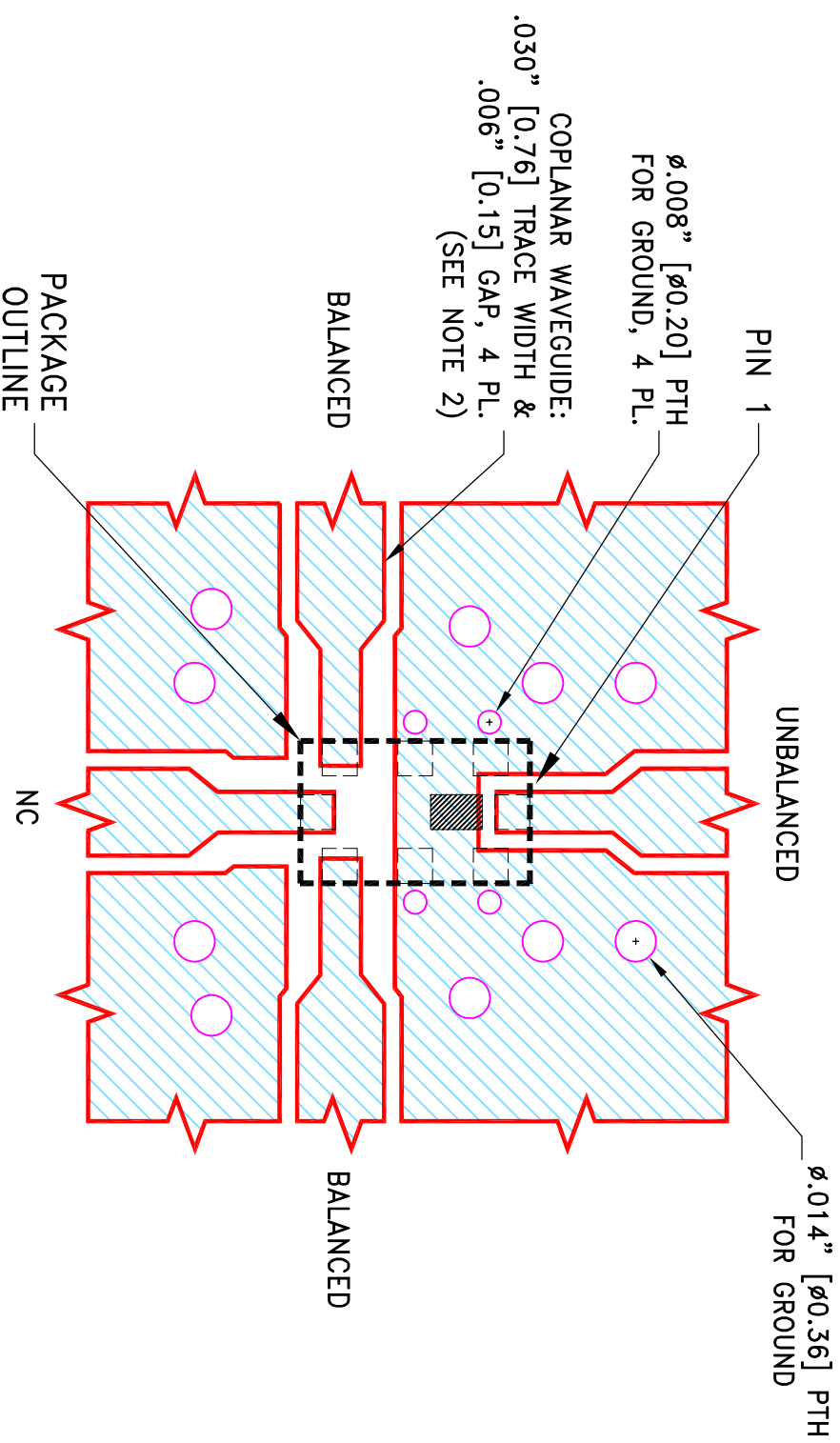
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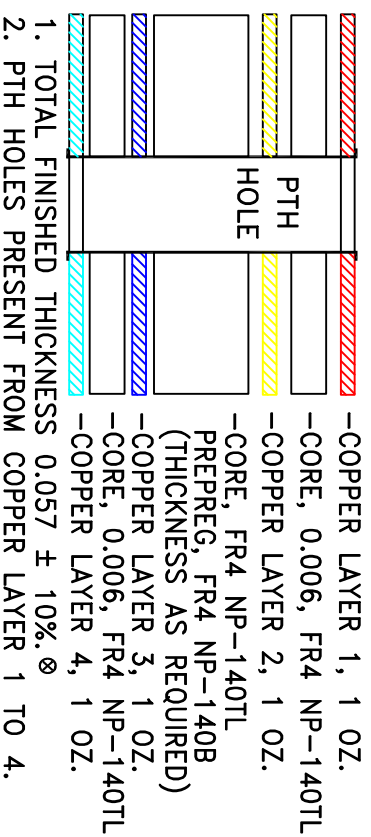
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**SUGGESTED MOUNTING CONFIGURATION
FOR GE0805C-15 CASE STYLE**

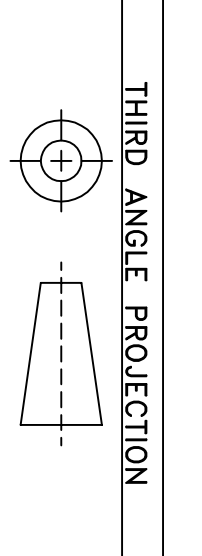


TOP VIEW TO LAYER 2

STACK-UP DIAGRAM



- NOTES:**
1. PCB IS MULTILAYER PCB, SEE STACK-UP DIAGRAM.
 2. TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR FR4 NP-140TL WITH DIELECTRIC THICKNESS .006"±.0005"; COPPER: 1 OZ. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
 3. COPPER LAYERS 3,4 OF THE PCB IS CONTINUOUS GROUND PLANES.
-  DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.



OR	ECN	NO.	DESCRIPTION	DATE	DR	AUTH
ECO-010908			NEW RELEASE	12/02/21	ITG	IL

UNLESS OTHERWISE SPECIFIED		INITIALS		DATE	
DIMENSIONS ARE IN INCHES					
TOLERANCES ON:					
2 PL DECIMALS ±		ITG		12/02/21	
3 PL DECIMALS ±		GF		12/02/21	
ANGLES ±		IL		12/02/21	
FRACTIONS ±					

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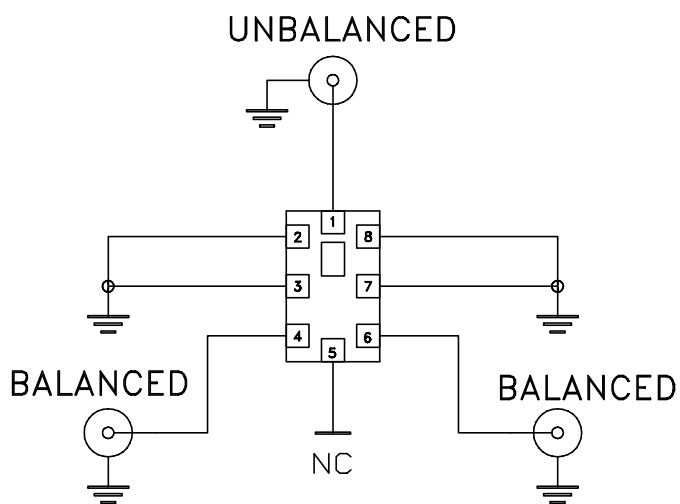
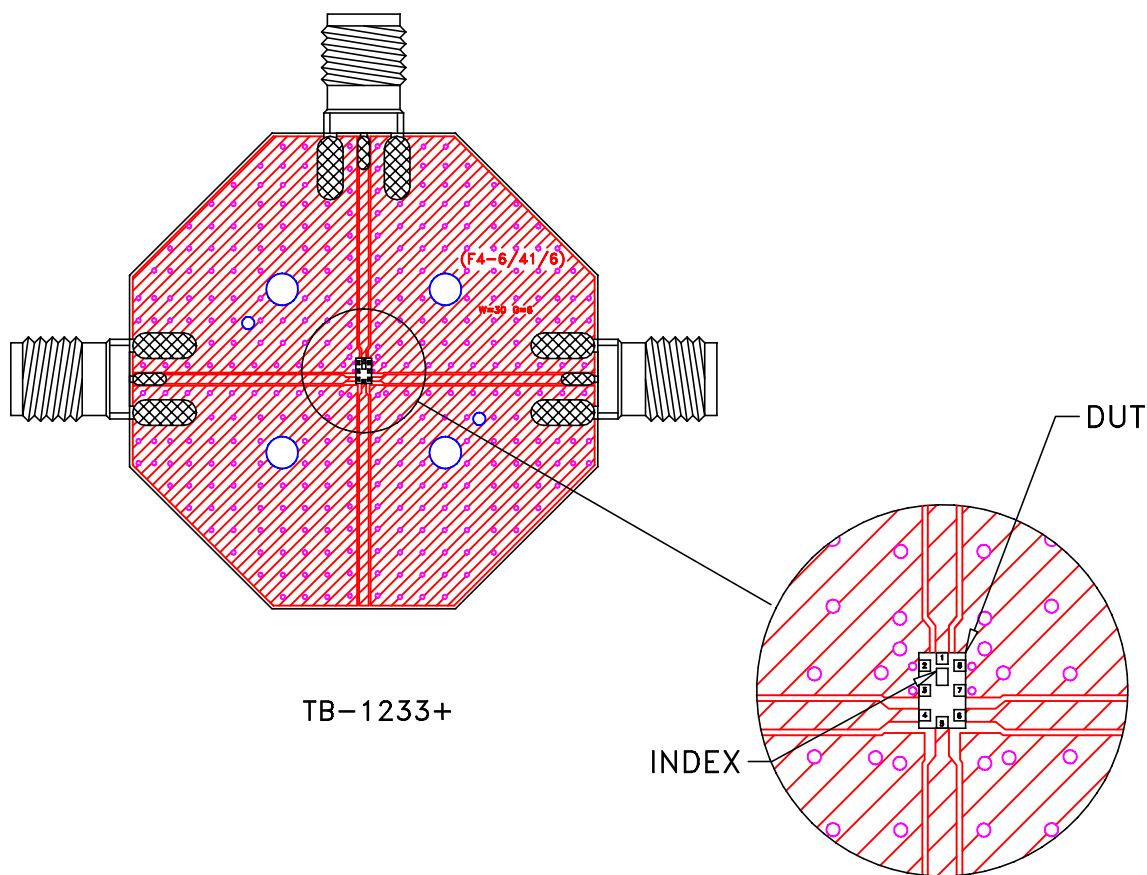
PL, GE0805C-15, TB-1233+

SIZE	CODE IDENT	DRAWING NO:	REV:
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FILE:	98PL724	SCALE:	15:1
		SHEET:	1 OF 1

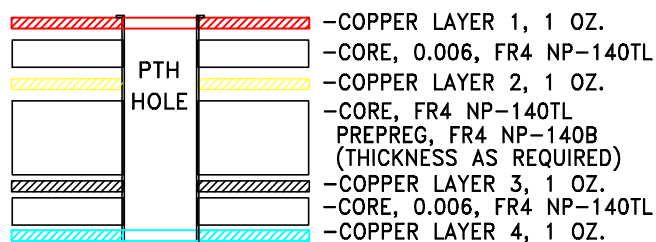
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Evaluation Board and Circuit



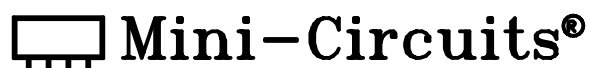
Schematic Diagram



Stack-up Diagram

Notes:

1. 50 Ohm SMA Female connectors.
2. PCB Material: FR4 or equivalent, Dielectric Constant=4.5, Total finished Thickness = .057 inch.





All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process: 250°C peak	J-STD-020C, Table 4-1, 4-2 and 5-2; Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A