

Ceramic Balun RF Transformer

50Ω 2400 to 2500 MHz 1:1 Ratio

BLGE1-252R+



Generic photo used for illustration purposes only

CASE STYLE: GE0805C-9

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications



Available Tape and Reel at no extra cost

Reel Size	Devices/Reel
7"	20, 50, 100, 200, 500, 1000, 4000

Maximum Ratings

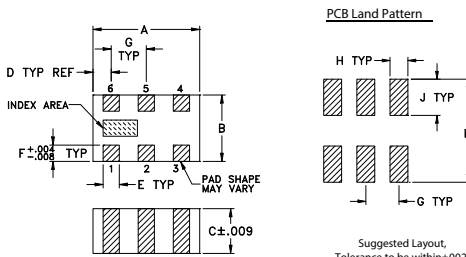
Operating Temperature	-40°C to 85°C
Storage Temperature*	-40°C to 85°C
Input RF Power**	3W at 25°C

*Refer to product storage temperature after installation. Suggestion for T&R unused product storage condition: +5--35°C, Humidity 45-75%RH, 12 Month max. Permanent damage may occur if any of these limits are exceeded. **Derate linearly to 1.5W at 85°C.

Pad Connections

PRIMARY DOT (Unbalanced Port)	1
PRIMARY (GND) or DC Feed	2
SECONDARY DOT (Balanced)	3
SECONDARY (Balanced)	4
NO CONNECTION	6
NOT USED (GND Externally)	5

Outline Drawing



Outline Dimensions (inch/mm)

A	B	C	D	E	F
.079	.049	.033	.014	.012	.012
2.0	1.24	0.84	0.36	0.30	0.30
G	H	J	K	wt	
.026	.014	.039	.110	grams	
0.66	0.36	1.00	2.80	.008	

Features

- miniature size 0805 (0.079" [2.0mm] x 0.049" [1.25mm] x 0.037" [0.95mm])
- low phase unbalance, 5 deg. typ. and amplitude unbalance, 0.2 dB typ.
- LTCC construction
- low cost
- aqueous washable

Applications

- ISM Band
- WLAN/Wi-Fi
- Bluetooth
- Zigbee

Electrical Specifications at 25°C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Unit
Impedance Ratio			1		
Frequency Range		2400	—	2500	MHz
Insertion Loss*	2400 - 2500	—	0.6	1	dB
Amplitude Unbalance	2400 - 2500	—	0.2	2	dB
Phase Unbalance†	2400 - 2500	—	5	10	Degree
Unbalance Return Loss	2400 - 2500	9.5	16	—	dB

* Tested on Evaluation Board TB-1009+

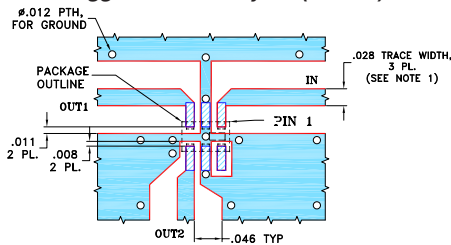
† Relative to 180°

Typical Performance Data at 25°C**

FREQUENCY (GHz)	INSERTION LOSS (dB)	INPUT R. LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE (Deg.)
2.40	0.54	15.88	0.13	6.06
2.41	0.54	15.93	0.13	5.83
2.42	0.54	15.96	0.12	5.60
2.43	0.55	15.98	0.11	5.41
2.44	0.55	15.98	0.11	5.14
2.45	0.55	15.97	0.12	4.93
2.46	0.55	15.98	0.11	4.70
2.47	0.54	15.98	0.12	4.46
2.48	0.54	15.98	0.11	4.22
2.49	0.54	15.98	0.11	4.01
2.50	0.55	15.98	0.12	3.74

** Measured with Agilent E5071B network analyzer using impedance conversion and port extension.

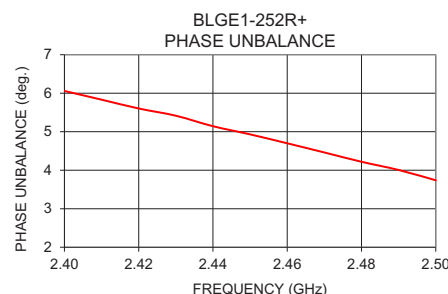
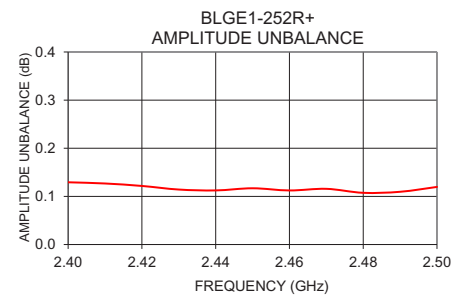
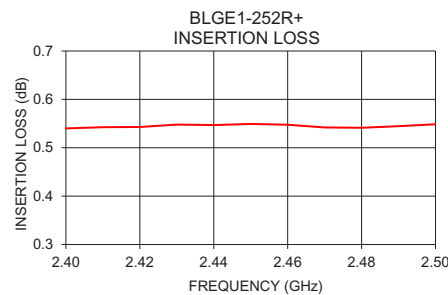
Evaluation Board MCL P/N: TB-1009+ Suggested PCB Layout (PL-556)



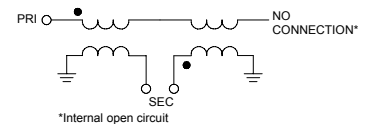
NOTES:

1. TRACE WIDTH IS SHOWN FOR FR4, GRADE IT-180TC (ITEQ CORP.) WITH DIELECTRIC THICKNESS .016±.0015. COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK



Configuration J



Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp



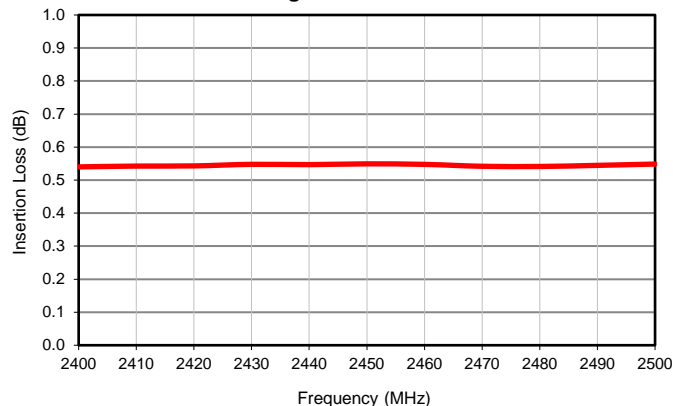
Typical Performance Data

FREQUENCY (MHz)	AVERAGE INSERTION LOSS (dB)	INPUT RETURN LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE ⁽¹⁾ (deg.)
2400	0.54	15.88	0.13	6.06
2410	0.54	15.93	0.13	5.83
2420	0.54	15.96	0.12	5.60
2430	0.55	15.98	0.11	5.41
2440	0.55	15.98	0.11	5.14
2450	0.55	15.97	0.12	4.93
2460	0.55	15.98	0.11	4.70
2470	0.54	15.98	0.12	4.46
2480	0.54	15.98	0.11	4.22
2490	0.54	15.98	0.11	4.01
2500	0.55	15.98	0.12	3.74

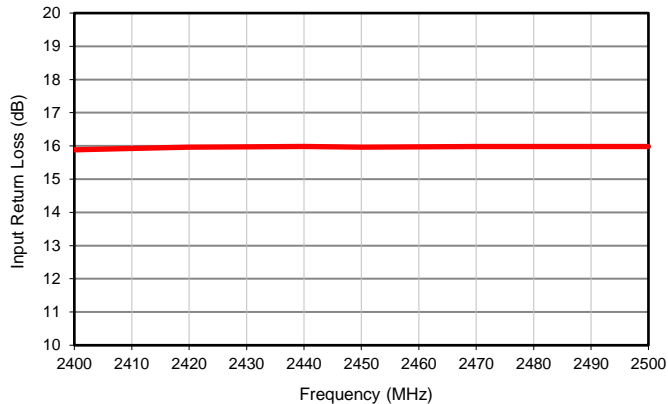
⁽¹⁾Relative to 180°

Typical Performance Data

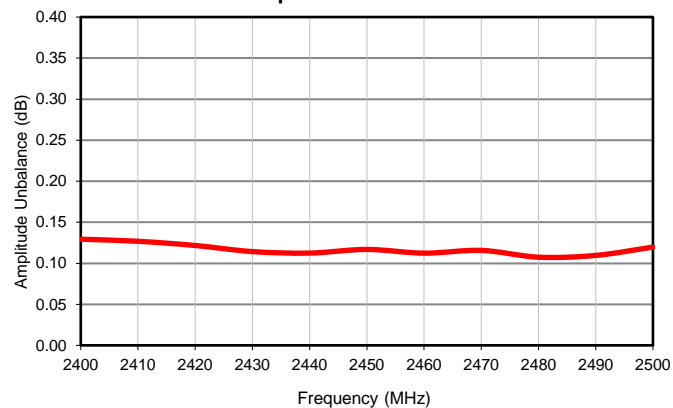
Average Insertion Loss



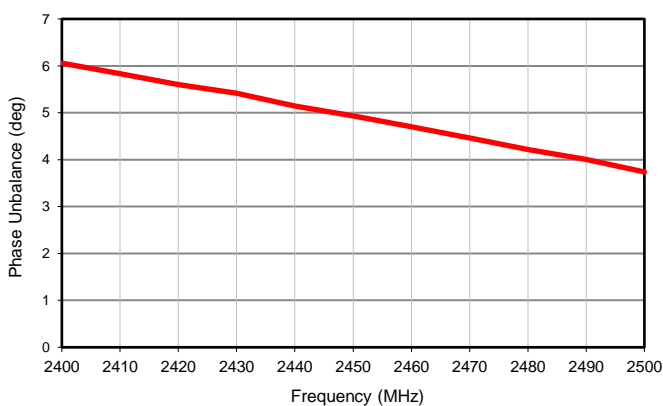
Input Return Loss



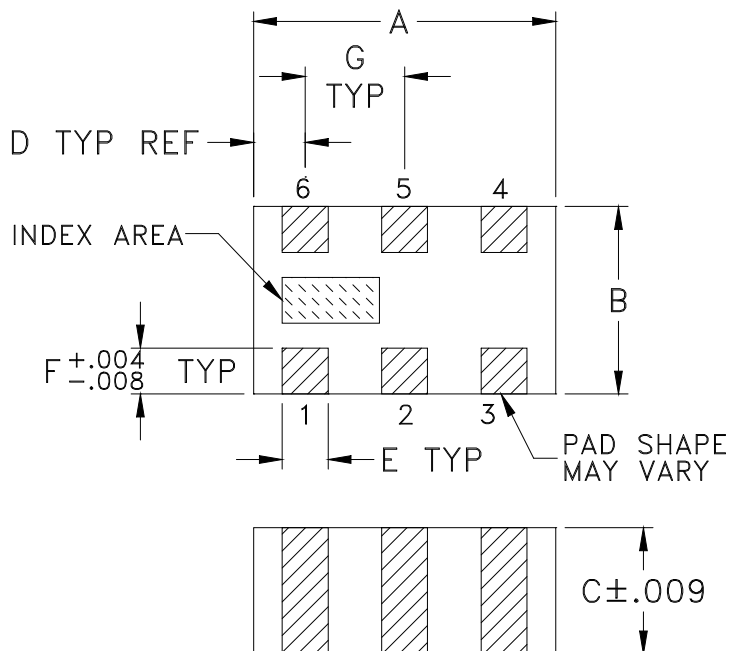
Amplitude Unbalance



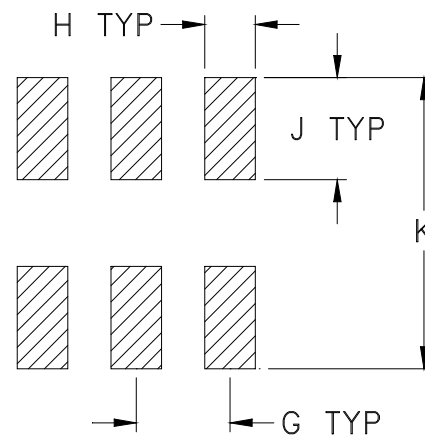
Phase Unbalance



Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	WT.GRAM
GE0805C-9	.079 (2.00)	.049 (1.25)	.037 (0.95)	.014 (0.35)	.012 (0.30)	.012 (0.30)	.026 (0.65)	.014 (0.35)	.039 (1.00)	.110 (2.80)	.008

Dimensions are in inches (mm). Tolerances: 2Pl. $\pm .01$; 3 Pl. $\pm .005$

Notes:

- Open style, ceramic base.
- Termination finish: For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
For RoHS-5 Case Styles: Tin-Lead plate over Nickel plate. All models, no (+) suffix.



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F114

DEVICE ORIENTATION IN T&R

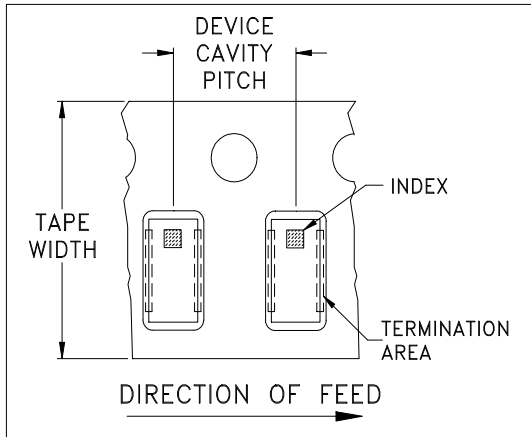


ILLUSTRATION 1

Applicable Case Styles	
GE0805C	JC0603C
GE0805C-1	JC0603C-4
GE0805C-1AP	JC0603C-6
GE0805C-7	
GE0805C-9	
GE0805C-10	
GE0805C-11	
GE0805C-12	

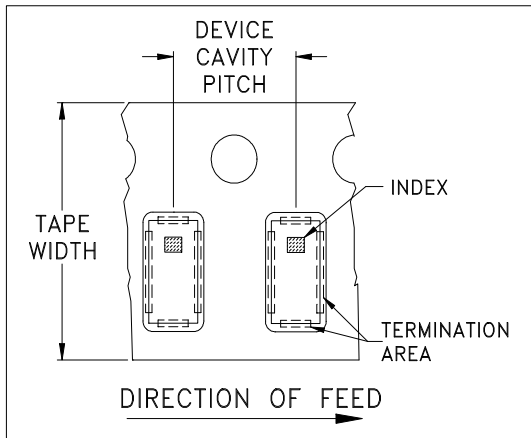


ILLUSTRATION 2

Applicable Case Styles	
GE0805C-2	JC0603C-1
GE0805C-3	JC0603C-2
GE0805C-4	JC0603C-3
GE0805C-5	JC0603C-5
GE0805C-6	JC0603C-7
GE0805C-8	JV1210C-1
GE0805C-15	

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	4000

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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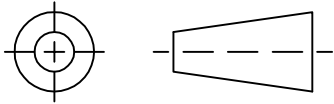
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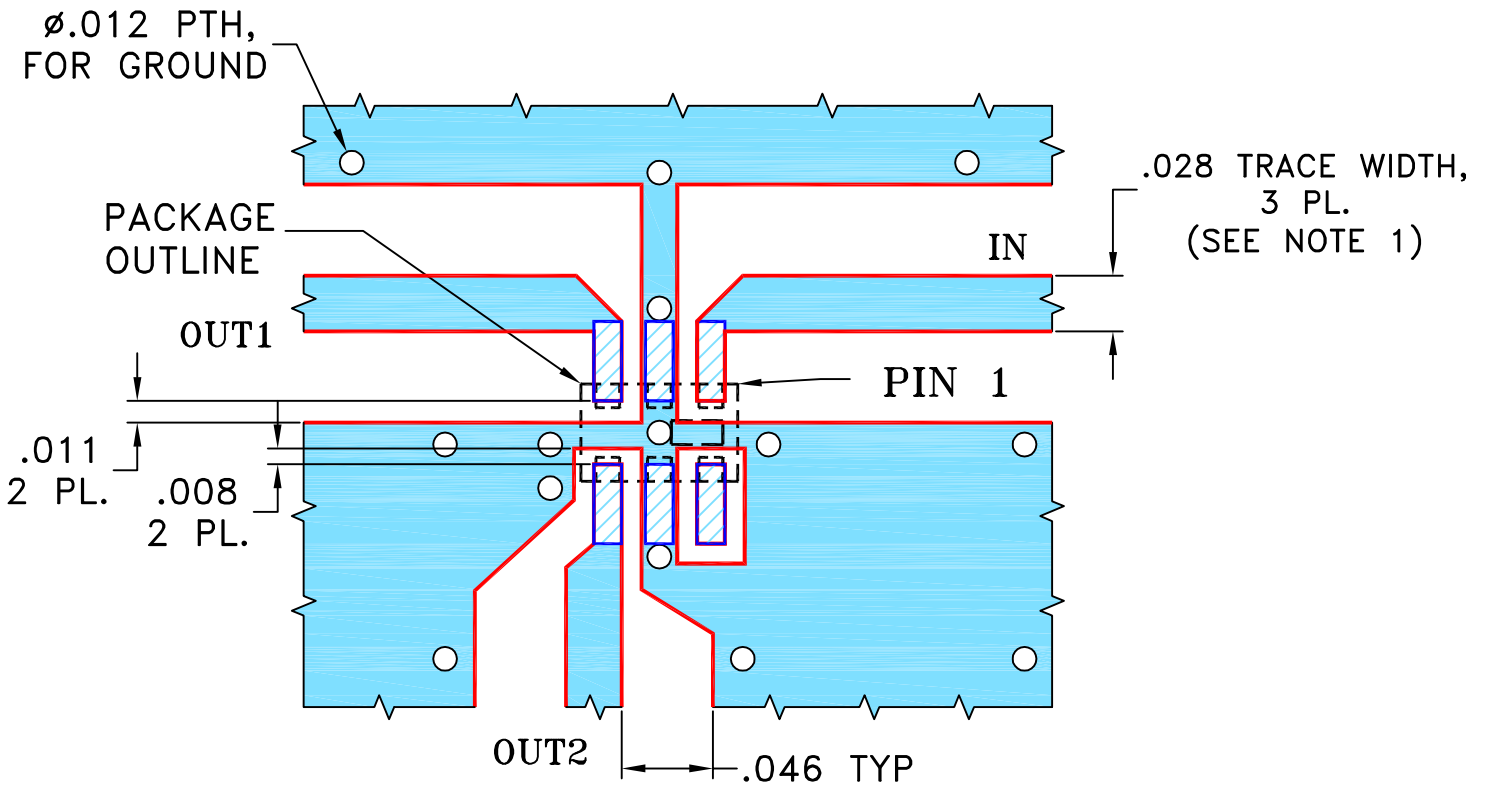
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M168200	NEW RELEASE	05/31/18	NP	SL

SUGGESTED MOUNTING CONFIGURATION
FOR GE0805C-9 CASE STYLE, "06TJ08" PIN CONNECTION



NOTES:

1. TRACE WIDTH IS SHOWN FOR FR4, GRADE IT-180TC (ITEQ CORP.) WITH DIELECTRIC THICKNESS $.016 \pm .0015$. COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.
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- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED	INITIALS	DATE
DRAWN	NP	05/30/18
CHECKED	GF	05/30/18
APPROVED	SL	05/31/18

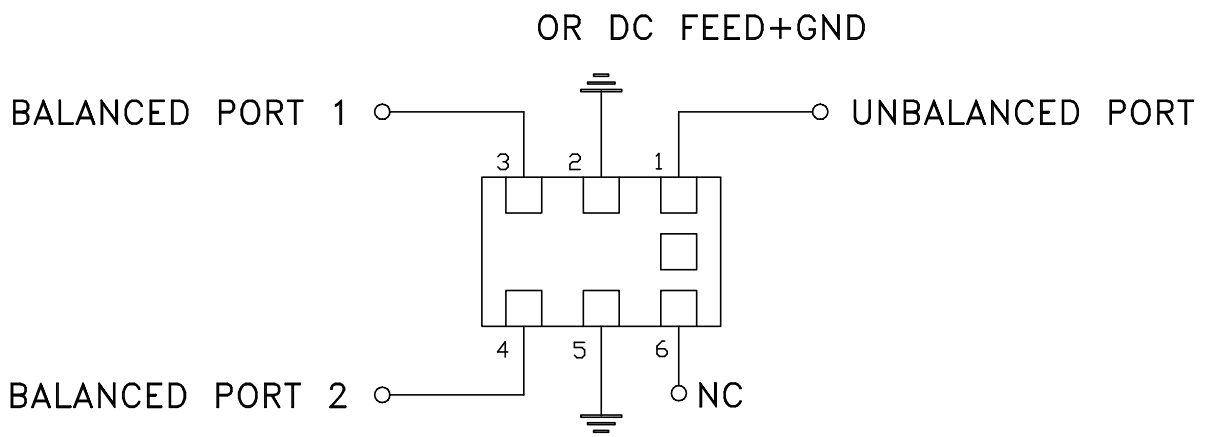
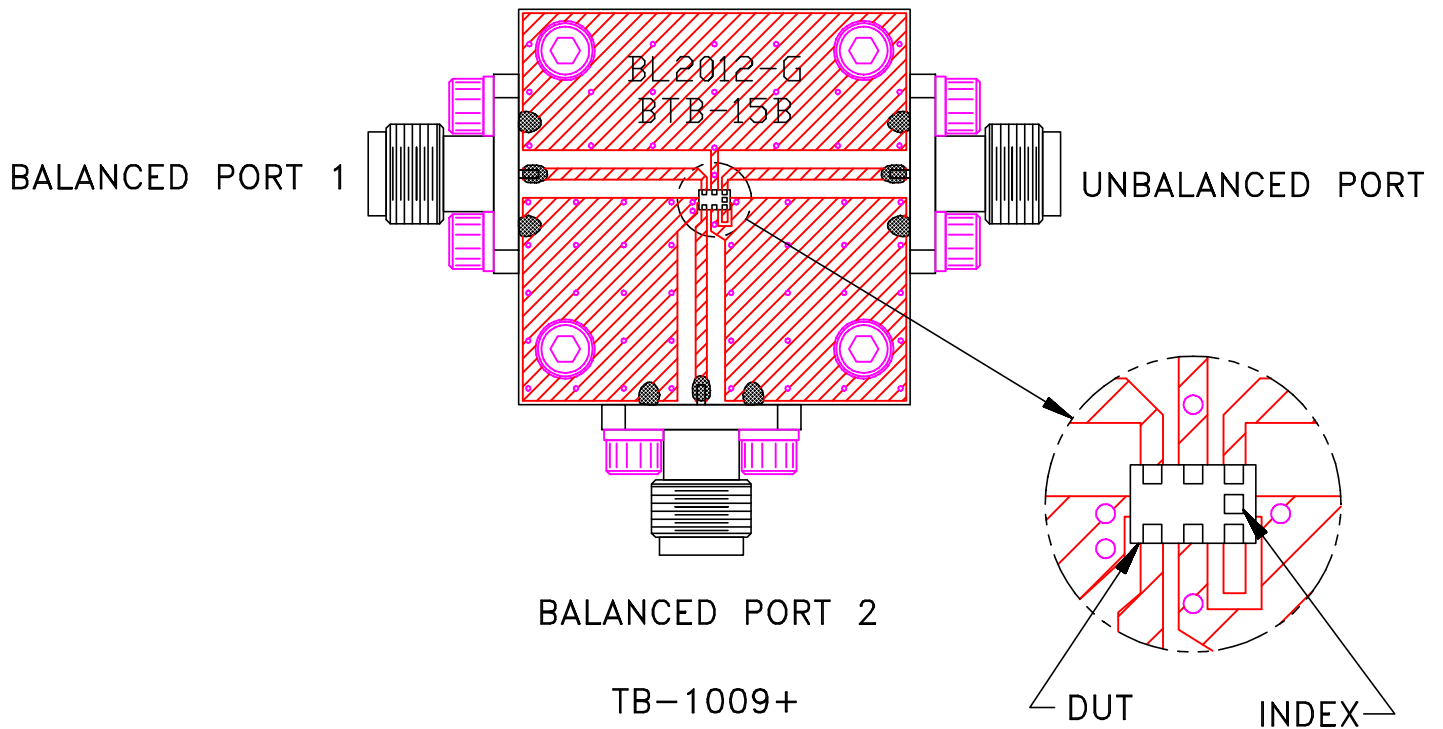
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PL, 06TJ08, GE0805C-9, TB-1009+

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SIZE A	CODE IDENT 15542	DRAWING NO: 98-PL-556	REV: OR
FILE: 98PL556	SCALE: 10:1	SHEET: 1 OF 1	


Evaluation Board and Circuit



Schematic Diagram

Notes:

1. 50 Ohm SMA Female connectors.
2. PCB Material: FR4 or equivalent,
Dielectric Constant=4.5, Thickness=.016 inch.

 **Mini-Circuits®**



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-40° to 85° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process: 250°C peak	J-STD-020C, Table 4-1, 4-2 and 5-2; Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A