

LTCC Balun RF Transformer

50Ω 4900 to 5875 MHz 1:1 Ratio

BLGE1-542R+



Generic photo used for illustration purposes only
CASE STYLE: GE0805C-9

Maximum Ratings

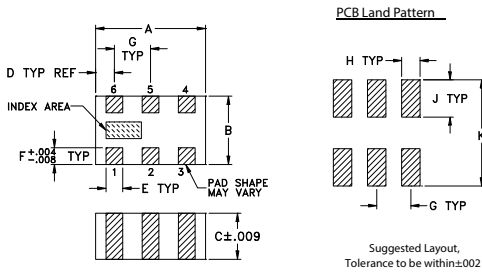
Operating Temperature	-40°C to 85°C
Storage Temperature*	-55°C to 100°C
Input RF Power	2W

*Refer to product storage temperature after installation.
Suggestion for T&R unused product storage condition: +5--+35°C,
Humidity 45-75%RH, 12 Month max.
Permanent damage may occur if any of these limits are exceeded.

Pad Connections

PRIMARY DOT (Unbalanced Port)	1
PRIMARY (GND) DC Feed	2
SECONDARY DOT (Balanced)	4
SECONDARY (Balanced)	3
NO CONNECTION	6
NOT USED (GND Externally)	5

Outline Drawing



Outline Dimensions (inch/mm)

A	B	C	D	E	F
.079	.049	.033	.014	.012	.012
2.0	1.24	0.84	0.36	0.30	0.30
G	H	J	K	wt	
.026	.014	.039	.110	grams	
0.66	0.36	1.00	2.80	.008	

Features

- Miniature size 0805 (0.079"[2.0mm]) x 0.049"[1.25mm] x 0.037"[0.95mm])
- low cost
- aqueous washable

Applications

- ISM Band
- WLAN/Wi-Fi
- Bluetooth
- Zigbee

+RoHS Compliant
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Available Tape and Reel at no extra cost

Reel Size	Devices/Reel
7"	20, 50, 100, 200, 500, 1000, 4000

Electrical Specifications at 25°C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Unit
Impedance Ratio			1		
Frequency Range		4900	—	5875	MHz
Insertion Loss*	4900 - 5875	—	0.8	2.3	dB
Amplitude Unbalance	4900 - 5875	—	0.7	2.0	dB
Phase Unbalance†	4900 - 5875	—	11	—	Degree
Primary Port Return Loss	4900 - 5875	8	16	—	dB

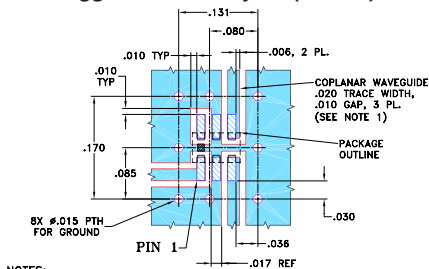
* Tested on Evaluation Board TB-BLGE1-542R+
† Relative to 180°

Typical Performance Data at 25°C**

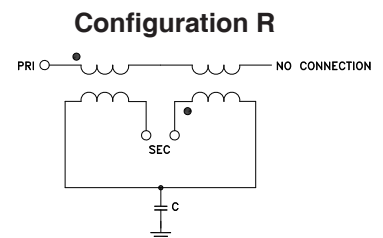
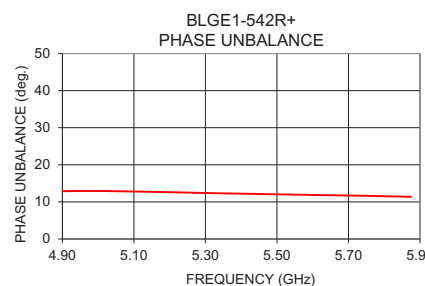
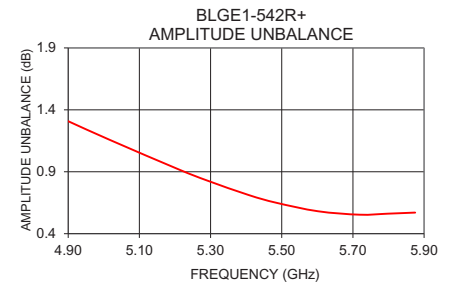
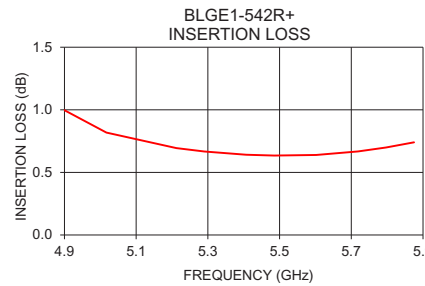
FREQUENCY (GHz)	INSERTION LOSS (dB)	INPUT R. LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE (Deg.)
4.900	1.00	15.57	1.31	12.88
5.017	0.82	15.70	1.16	12.93
5.212	0.69	17.32	0.92	12.59
5.290	0.67	17.88	0.83	12.42
5.407	0.64	18.31	0.71	12.21
5.485	0.63	18.09	0.65	12.07
5.602	0.64	17.16	0.58	11.87
5.719	0.67	15.78	0.55	11.69
5.797	0.70	14.71	0.56	11.53
5.875	0.74	13.73	0.57	11.37

** Measured with Agilent E5071B network analyzer using impedance conversion and port extension.

Evaluation Board MCL P/N: TB-BLGE1-542R+ Suggested PCB Layout (PL-264)



- NOTES:
1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .010" ± .001". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).
- DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK



Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

REV. OR
M172548
BLGE1-542R+
AV/CP/AM
200918



Typical Performance Data

FREQUENCY (MHz)	AVERAGE INSERTION LOSS (dB)	INPUT RETURN LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE ⁽¹⁾ (deg.)
4900	1.00	15.57	1.31	12.88
4939	0.91	15.49	1.25	12.93
4978	0.86	15.59	1.20	12.95
5017	0.82	15.70	1.16	12.93
5056	0.79	15.91	1.11	12.87
5095	0.76	16.29	1.06	12.82
5134	0.73	16.72	1.01	12.73
5173	0.71	17.07	0.96	12.66
5212	0.69	17.32	0.92	12.59
5251	0.68	17.51	0.88	12.49
5290	0.67	17.88	0.83	12.42
5329	0.65	18.18	0.79	12.33
5368	0.64	18.32	0.75	12.24
5407	0.64	18.31	0.71	12.21
5446	0.64	18.17	0.68	12.13
5485	0.63	18.09	0.65	12.07
5524	0.63	17.98	0.62	11.99
5563	0.63	17.65	0.60	11.92
5602	0.64	17.16	0.58	11.87
5641	0.65	16.71	0.57	11.81
5680	0.66	16.27	0.56	11.74
5719	0.67	15.78	0.55	11.69
5758	0.68	15.26	0.55	11.63
5797	0.70	14.71	0.56	11.53
5836	0.72	14.19	0.57	11.44
5875	0.74	13.73	0.57	11.37

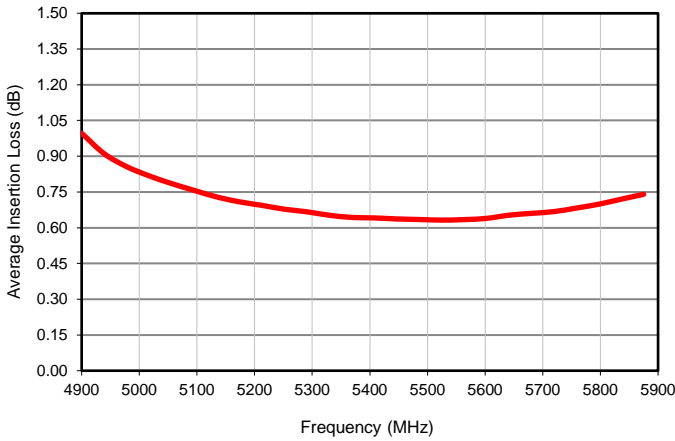
⁽¹⁾ Relative to 180°

LTCC Balun RF Transformer

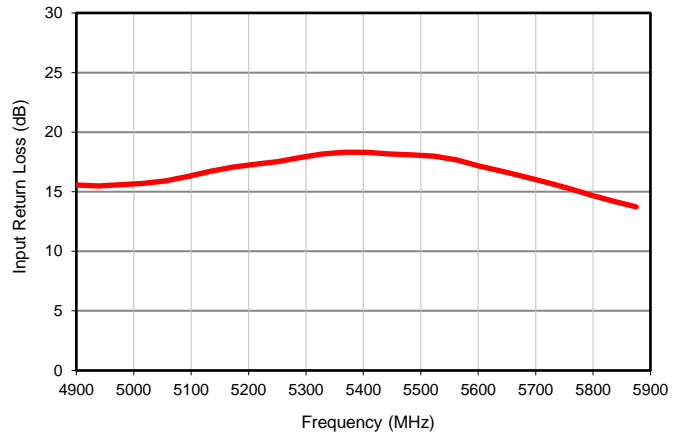
BLGE1-542R+

Typical Performance Data

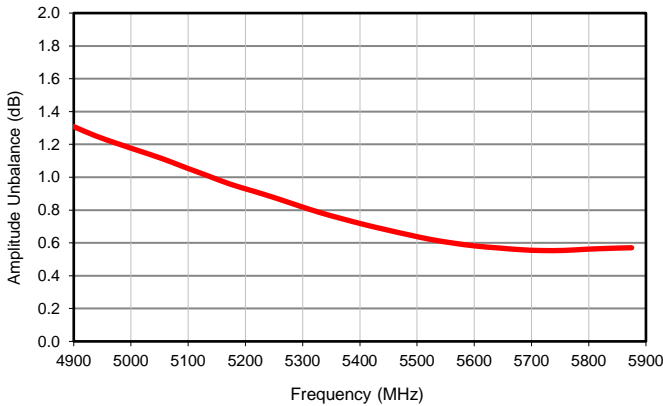
Average Insertion Loss



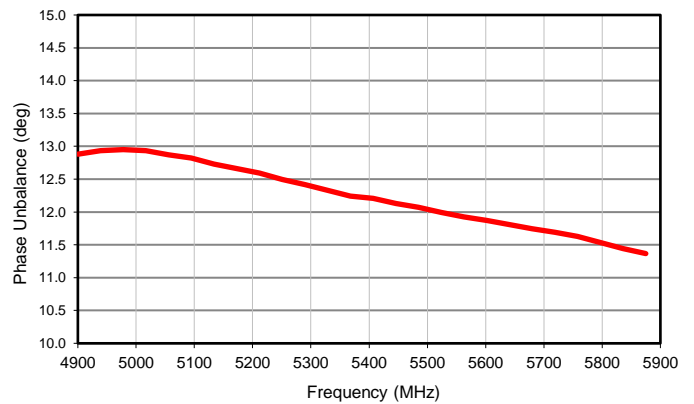
Input Return Loss



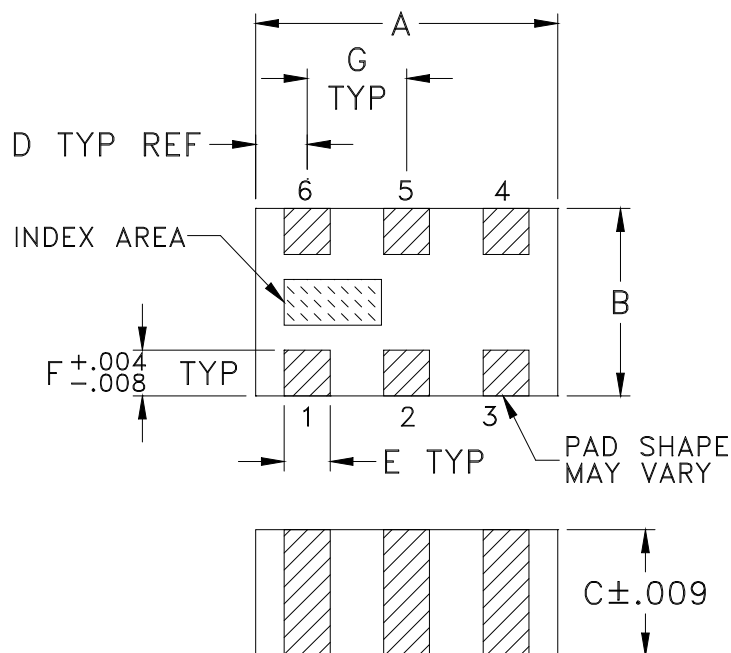
Amplitude Unbalance



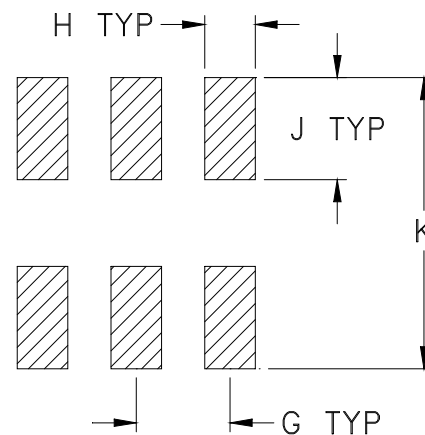
Phase Unbalance



Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	WT.GRAM
GE0805C-9	.079 (2.00)	.049 (1.25)	.037 (0.95)	.014 (0.35)	.012 (0.30)	.012 (0.30)	.026 (0.65)	.014 (0.35)	.039 (1.00)	.110 (2.80)	.008

Dimensions are in inches (mm). Tolerances: 2Pl. $\pm .01$; 3 Pl. $\pm .005$

Notes:

- Open style, ceramic base.
- Termination finish: For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
For RoHS-5 Case Styles: Tin-Lead plate over Nickel plate. All models, no (+) suffix.



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F114

DEVICE ORIENTATION IN T&R

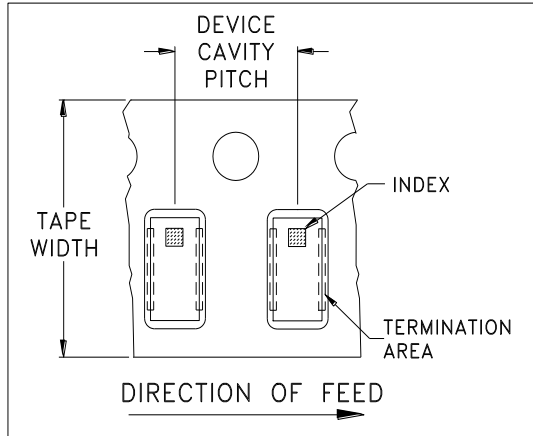


ILLUSTRATION 1

Applicable Case Styles	
GE0805C	JC0603C
GE0805C-1	JC0603C-4
GE0805C-1AP	JC0603C-6
GE0805C-7	
GE0805C-9	
GE0805C-10	
GE0805C-11	
GE0805C-12	

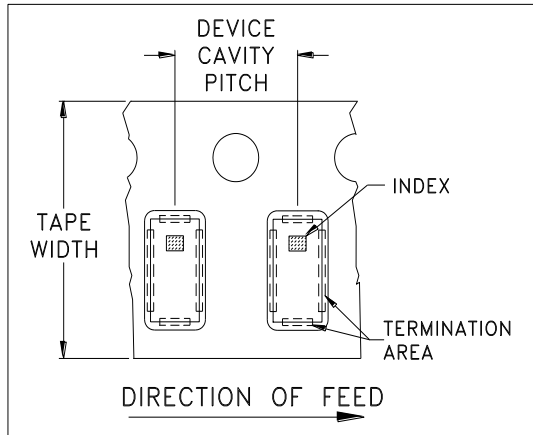


ILLUSTRATION 2

Applicable Case Styles	
GE0805C-2	JC0603C-1
GE0805C-3	JC0603C-2
GE0805C-4	JC0603C-3
GE0805C-5	JC0603C-5
GE0805C-6	JC0603C-7
GE0805C-8	
GE0805C-15	

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	4000

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



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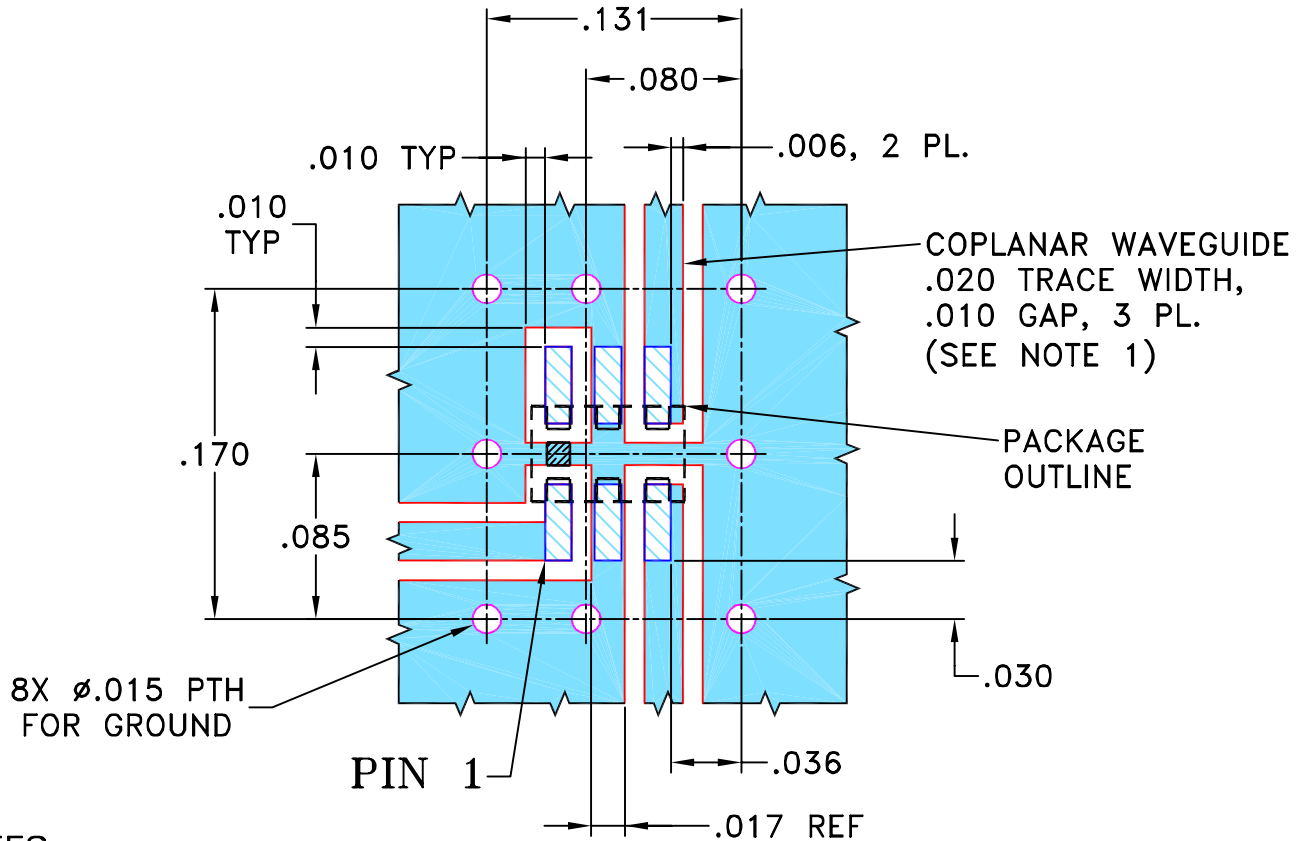
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M109549	NEW RELEASE	01/31/07	PW	DJ

SUGGESTED MOUNTING CONFIGURATION
FOR GE0805C-1 CASE STYLE, "ry" PIN CONNECTION.



NOTES:

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UNLESS OTHERWISE SPECIFIED	INITIALS		DATE
DIMENSIONS ARE IN INCHES TOLERANCES ON: 2 PL DECIMALS ± 3 PL DECIMALS ± .005 ANGLES ± FRACTIONS ±	DRAWN	PW	01/30/07
	CHECKED	IL	01/31/07
	APPROVED	DJ	01/31/07

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Brooklyn NY 11235

PL, ry, GE0805C-1, NCS, TB-419+

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SIZE A	CODE IDENT 15542	DRAWING NO: 98-PL-264	REV: OR
FILE: 98PL264	SCALE: 10:1	SHEET: 1 OF 1	

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A