LTCC Balun

RF Transformer

BLJC2-542R+

50Ω 4900 to 5875 MHz 1:2 Ratio



Generic photo used for illustration purposes only

CASE STYLE: JC0603C

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Available Tape and Reel at no extra cost

Reel Size	Devices/Reel
7"	20, 50, 100, 200, 500,1000, 4000

Maximum Ratings

Operating Temperature	-55 °C to +100 °C
Storage Temperature*	-55 °C to +100 °C
Input RF Power	0.5 W

*Refer to product storage temperature after installation.
Suggestion for T&R unused product storage condition: +5 ~ +35 °C, Humidity 45 ~ 75%RH, 12 Month max.
Permanent damage may occur if any of these limits are exceeded.

Pad Connections

PRIMARY DOT (Unbalanced Port)	1
GND or DC FEED	2
SECONDARY DOT (Balanced)	3
SECONDARY (Balanced)	4
NO CONNECTION	6
GND	5

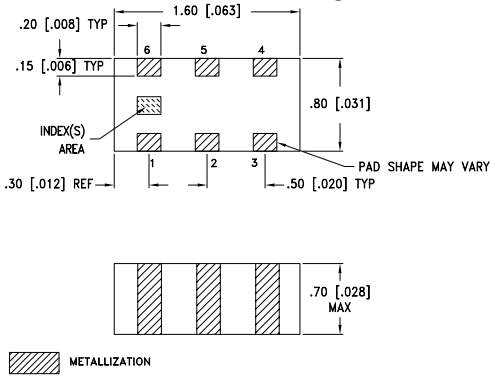
Features

- low phase unbalance, 1.5 deg. and amplitude unbalance, 0.2 dB typ.
- miniature size 0603 (0.063"[1.6mm]) x 0.031"[0.8mm] x 0.024"[0.6mm])
- low cost
- aqueous washable

Applications

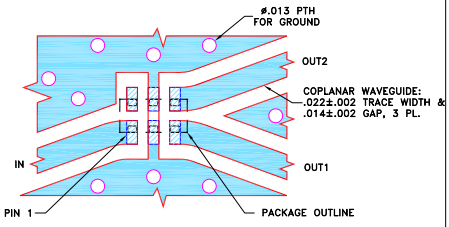
- ISM Band
- WLAN
- Bluetooth
- Zigbee

Outline Drawing



Weight: .005 grams
Dimensions are in mm [inch]. Tolerances: ±0.13 mm

Evaluation Board MCL P/N: TB-BLJC2-542R+
Suggested PCB Layout (PL-513)



1. TRACE WIDTH AND GAP PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .010"±.001". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

Electrical Specifications at +25 °C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Unit
Impedance Ratio			2		
Frequency Range		4900	—	5875	MHz
Insertion Loss*	4900 - 5875	—	0.9	1.3	dB
Amplitude Unbalance	4900 - 5875	—	0.2	1.5	dB
Phase Unbalance†	4900 - 5875	—	1.5	15	Degree
Unbalance Return Loss	4900 - 5875	9.5	14	—	dB

* Tested on Evaluation Board TB-BLJC2-542R+
† Relative to 180°

Typical Performance Data at +25 °C**

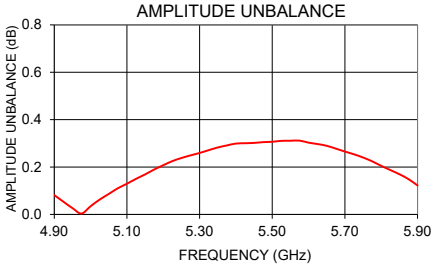
FREQUENCY (GHz)	INSERTION LOSS (dB)	INPUT R. LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE (Deg.)
4.90	1.14	10.25	0.14	1.48
5.00	1.09	10.70	0.01	1.35
5.10	1.04	11.22	0.10	1.19
5.20	1.00	11.79	0.20	0.96
5.30	0.96	12.44	0.28	0.64
5.40	0.92	13.19	0.35	0.25
5.50	0.88	14.06	0.40	0.21
5.60	0.84	15.05	0.43	0.71
5.70	0.81	16.19	0.44	1.27
5.80	0.79	17.41	0.43	1.91
5.875	0.78	18.30	0.41	2.45

** Measured with Agilent E5071B network analyzer using impedance conversion and port extension.

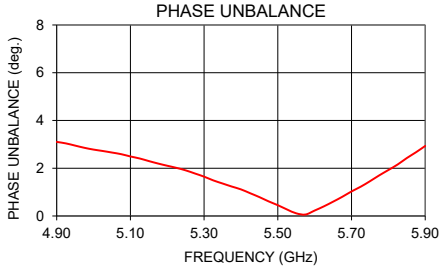
BLJC2-542R+
INSERTION LOSS



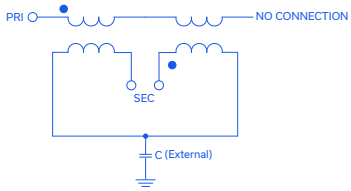
BLJC2-542R+
AMPLITUDE UNBALANCE



BLJC2-542R+
PHASE UNBALANCE



Configuration R



Notes

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

REV. A
ECO-030244
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RF Transformer BLJC2-542R+

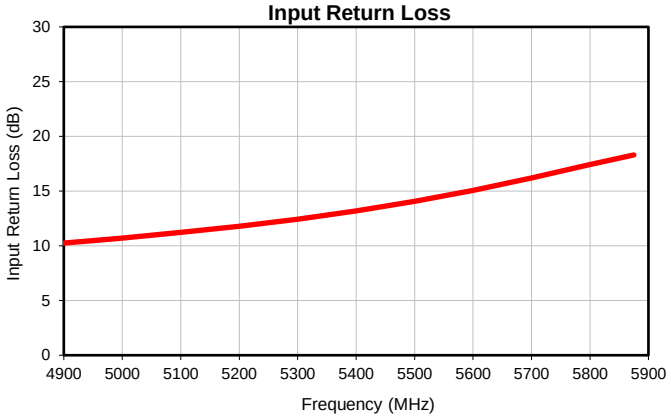
Typical Performance Data

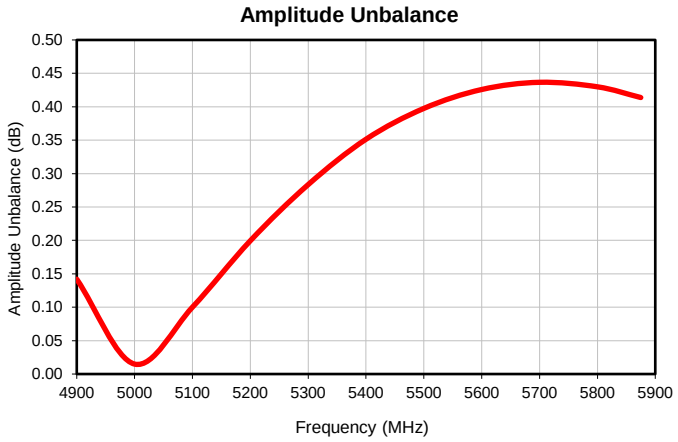
FREQUENCY (MHz)	AVERAGE INSERTION LOSS (dB)	INPUT RETURN LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE[1] (deg.)
4900	1.14	10.25	0.14	1.48
5000	1.09	10.70	0.01	1.35
5100	1.04	11.22	0.10	1.19
5200	1.00	11.79	0.20	0.96
5300	0.96	12.44	0.28	0.64
5400	0.92	13.19	0.35	0.25
5500	0.88	14.06	0.40	0.21
5600	0.84	15.05	0.43	0.71
5700	0.81	16.19	0.44	1.27
5800	0.79	17.41	0.43	1.91
5875	0.78	18.30	0.41	2.45

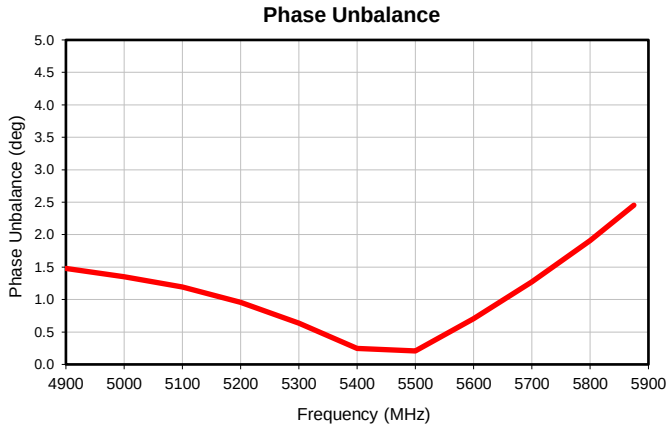
[1] Relative to 180°

Typical Performance Data



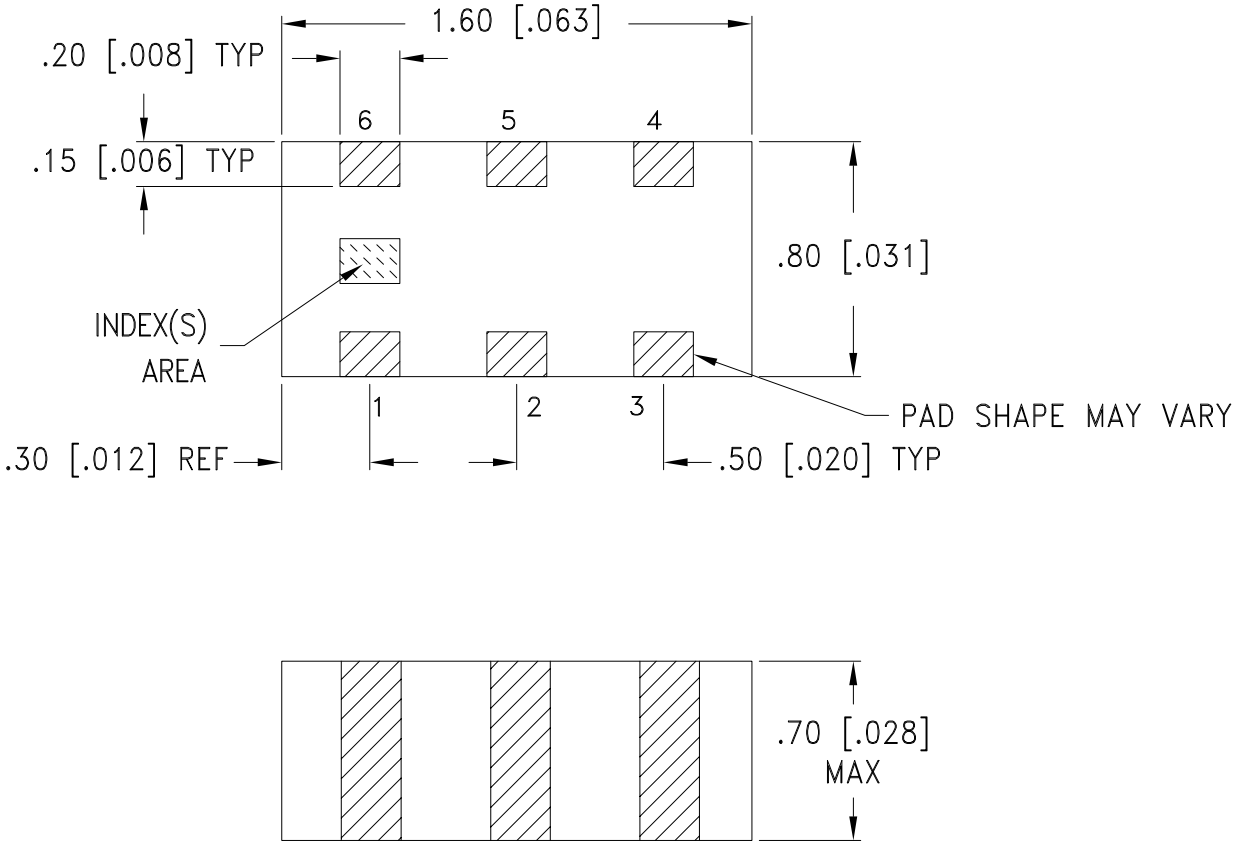






Case Style JC

Outline Dimensions JC0603C



METALLIZATION

Weight: .005 grams

Dimensions are in mm [inch]. Tolerances: ±0.13 mm

Notes:

1. Open style, ceramic base.
2. Termination finish:
   For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.

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The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com
RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F114

DEVICE ORIENTATION IN T&R

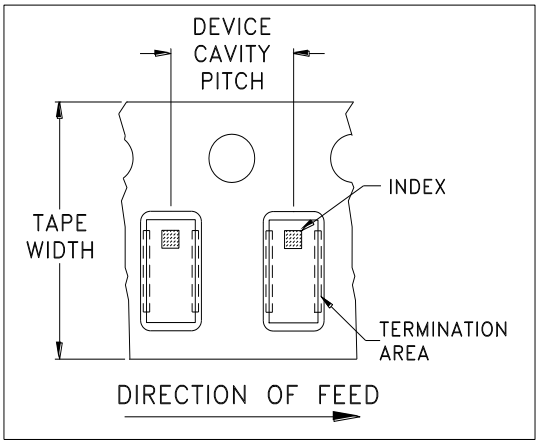


ILLUSTRATION 1

Applicable Case Styles	
GE0805C	JC0603C
GE0805C-1	JC0603C-4
GE0805C-1AP	JC0603C-6
GE0805C-7	
GE0805C-9	
GE0805C-10	
GE0805C-11	
GE0805C-12	

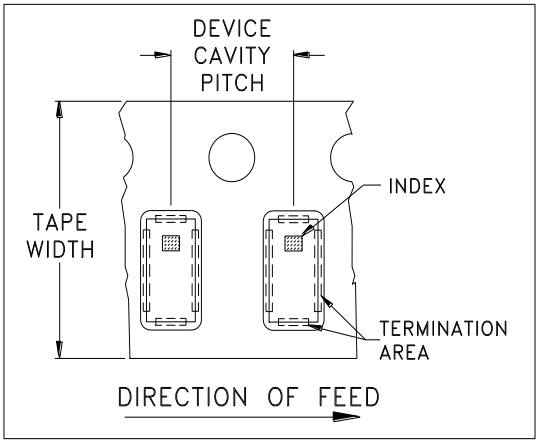


ILLUSTRATION 2

Applicable Case Styles	
GE0805C-2	JC0603C-1
GE0805C-3	JC0603C-2
GE0805C-4	JC0603C-3
GE0805C-5	JC0603C-5
GE0805C-6	JC0603C-7
GE0805C-8	
GE0805C-15	

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	4000

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

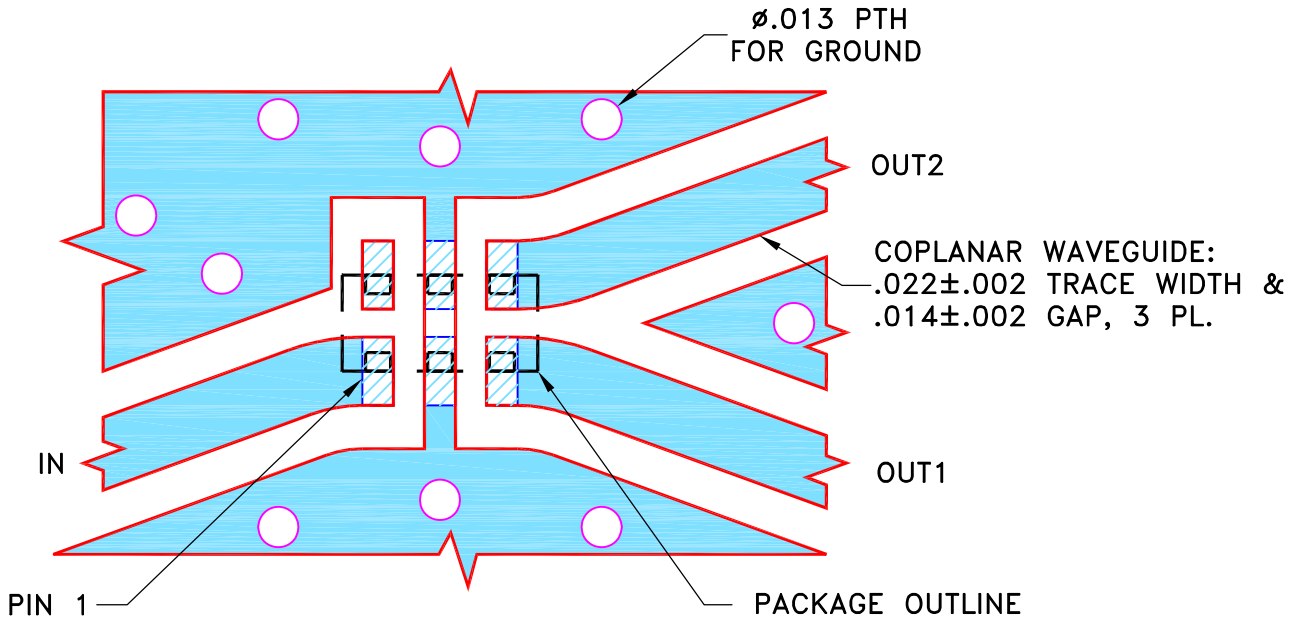
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THIRD ANGLE PROJECTION

REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M162337	NEW RELEASE	06/15/17	ITG	AVB

SUGGESTED MOUNTING CONFIGURATION
FOR JC0603C CASE STYLE, "06TR01" PIN CODE



1. TRACE WIDTH AND GAP PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .010"±.001". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	ITG	06/14/17
TOLERANCES ON:	CHECKED	GF	06/15/17
2 PL DECIMALS ±	APPROVED	AVB	06/15/17
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

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ASHEETA1.DWG REV:A DATE:01/12/95

Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

PL, 06TR01, JC0603C, TB-828+

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-513	OR

FILE:	SCALE:	SHEET:
98PL513	16:1	1 OF 1

Mini-Circuits

Environmental Specifications ENV06

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A

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