Ceramic Balun

RF Transformer

BLNK1-542R+

50Ω 4900 to 5950 MHz 1:1 Ratio

Generic photo used for illustration purposes only

CASE STYLE: NK0402C

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Available Tape and Reel at no extra cost

Reel Size	Devices/Reel
7"	20, 50, 100, 200, 500,1000, 4000

Maximum Ratings

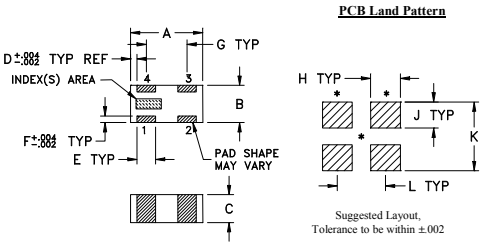
Operating Temperature	-40°C to 85°C
Storage Temperature*	-40°C to 85°C
Input RF Power**	2W at 25°C

*Refer to product storage temperature after installation.
Suggestion for T&R unused product storage condition: +5~+35°C, Humidity 45~75%RH, 12 Month max.
Permanent damage may occur if any of these limits are exceeded.
**Derate linearly to 1W at 85°C.

Pad Connections

PRIMARY DOT (Unbalanced Port)	1
GND	4
SECONDARY DOT (Balanced)	3
SECONDARY (Balanced)	2

Outline Drawing



Outline Dimensions (inch / mm)

A	B	C	D	E	F	G	H
.039	.020	.015	.004	.010	.004	.022	.016
1.0	0.51	0.38	0.10	0.25	0.10	0.56	0.41

J	K	L	wt
.014	.037	.026	grams
0.36	0.94	0.66	.0007

Features

- miniature size 0402 (0.039" [1.0 mm] x 0.020" [0.5 mm] x 0.015" [0.37 mm])
- low insertion loss, 0.8 dB typ.
- LTCC construction
- low cost
- aqueous washable

Applications

- ISM Band
- WLAN/Wi-Fi
- Bluetooth
- Zigbee

Electrical Specifications at 25°C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Unit
Impedance Ratio			2		
Frequency Range		4900	—	5950	MHz
Insertion Loss*	4900 - 5950	—	0.8	1.2	dB
Amplitude Unbalance	4900 - 5950	—	0.2	2	dB
Phase Unbalance†	4900 - 5950	—	3.8	10	Degree
Unbalance Return Loss	4900 - 5950	9.5	14	—	dB

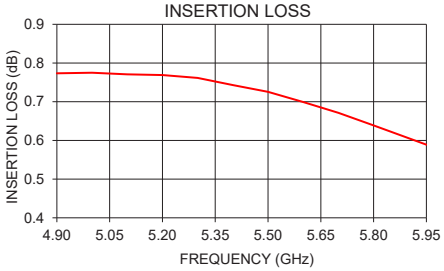
* Tested on Evaluation Board TB-1012-1542+
† Relative to 180°

Typical Performance Data at 25°C**

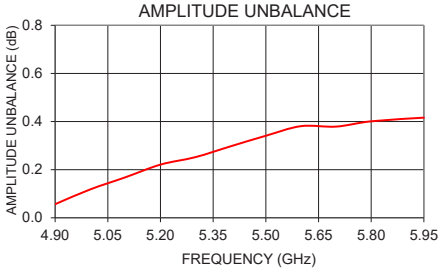
FREQUENCY (GHz)	INSERTION LOSS (dB)	INPUT R. LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE (Deg.)
4.90	0.77	15.55	0.06	1.32
5.00	0.77	15.04	0.12	1.81
5.10	0.77	14.66	0.17	2.35
5.20	0.77	14.33	0.22	2.91
5.30	0.76	14.10	0.25	3.54
5.40	0.74	14.05	0.30	4.20
5.50	0.73	14.11	0.34	4.88
5.60	0.70	14.18	0.38	5.54
5.70	0.67	14.40	0.38	6.19
5.80	0.64	14.62	0.40	6.92
5.95	0.59	15.35	0.42	8.02

** Measured with Agilent E5071B network analyzer using impedance conversion and port extension.

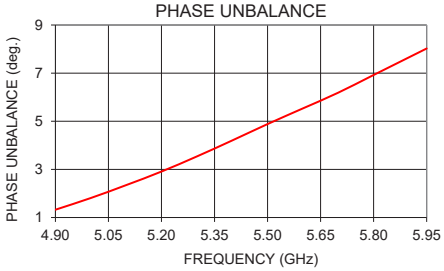
BLNK1-542R+
INSERTION LOSS



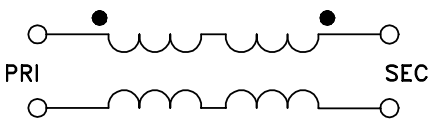
BLNK1-542R+
AMPLITUDE UNBALANCE



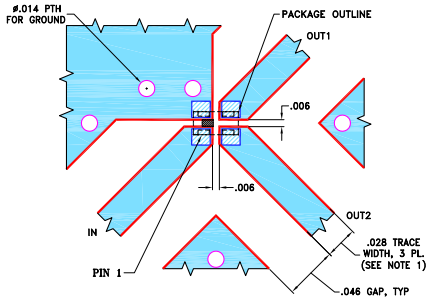
BLNK1-542R+
PHASE UNBALANCE



Configuration G



Evaluation Board MCL P/N: TB-1012-1542+
Suggested PCB Layout (PL-624)



NOTES:
1. TRACE WIDTH & GAP ARE SHOWN FOR FR4, GRADE IT-180TC (ITEQ CORP.) WITH DIELECTRIC THICKNESS .016±.0015. COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

Notes
A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

REV.OR
M172548
BLNK1-252R+
AV/CP/AM
200924

LTCC Balun

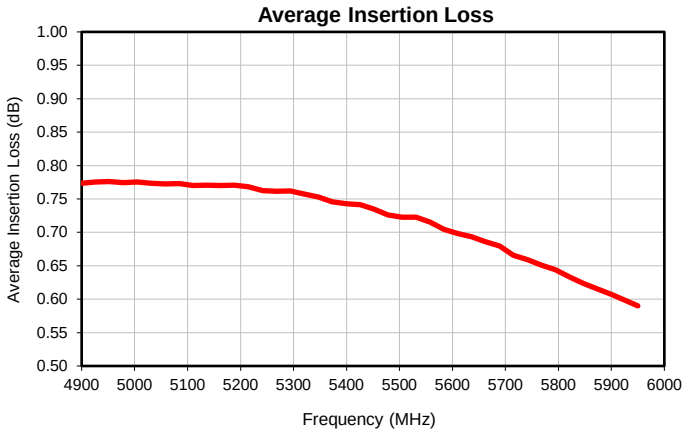
RF Transformer BLNK1-542R+

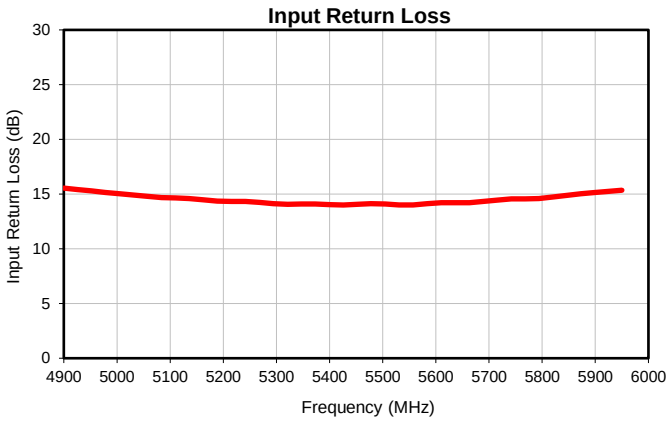
Typical Performance Data

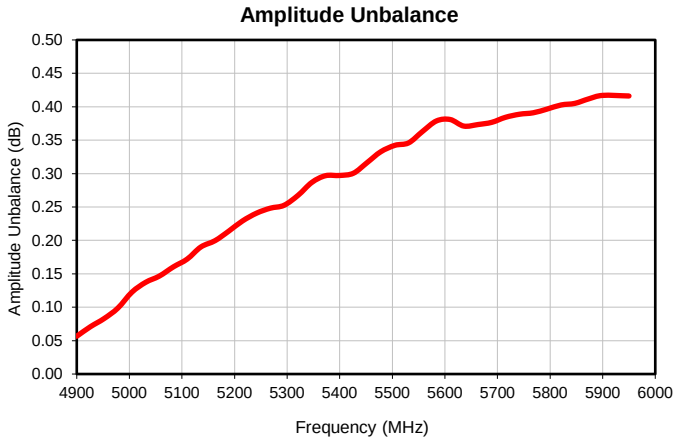
FREQUENCY (MHz)	AVERAGE INSERTION LOSS (dB)	INPUT RETURN LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE[1] (deg.)
4900	0.77	15.55	0.06	1.32
4926	0.78	15.42	0.07	1.46
4952	0.78	15.29	0.08	1.51
4978	0.77	15.15	0.10	1.69
5005	0.78	15.02	0.12	1.83
5031	0.77	14.92	0.14	1.98
5057	0.77	14.80	0.15	2.12
5084	0.77	14.69	0.16	2.24
5110	0.77	14.64	0.17	2.38
5136	0.77	14.60	0.19	2.54
5163	0.77	14.47	0.20	2.71
5189	0.77	14.36	0.21	2.87
5215	0.77	14.32	0.23	3.00
5242	0.76	14.32	0.24	3.19
5268	0.76	14.24	0.25	3.37
5294	0.76	14.12	0.25	3.50
5321	0.76	14.07	0.27	3.69
5347	0.75	14.10	0.29	3.84
5373	0.75	14.11	0.30	4.02
5400	0.74	14.05	0.30	4.20
5426	0.74	14.00	0.30	4.35
5452	0.73	14.06	0.32	4.54
5478	0.73	14.13	0.33	4.73
5505	0.72	14.10	0.34	4.91
5531	0.72	14.02	0.35	5.08
5557	0.72	14.02	0.36	5.22
5584	0.70	14.12	0.38	5.45
5610	0.70	14.21	0.38	5.60
5636	0.69	14.22	0.37	5.77
5663	0.69	14.22	0.37	5.94
5689	0.68	14.34	0.38	6.10
5715	0.67	14.46	0.38	6.31
5742	0.66	14.55	0.39	6.53
5768	0.65	14.57	0.39	6.70
5794	0.64	14.60	0.40	6.88
5821	0.63	14.74	0.40	7.08
5847	0.62	14.89	0.41	7.31
5873	0.62	15.02	0.41	7.49
5900	0.61	15.16	0.42	7.67
5950	0.59	15.35	0.42	8.02

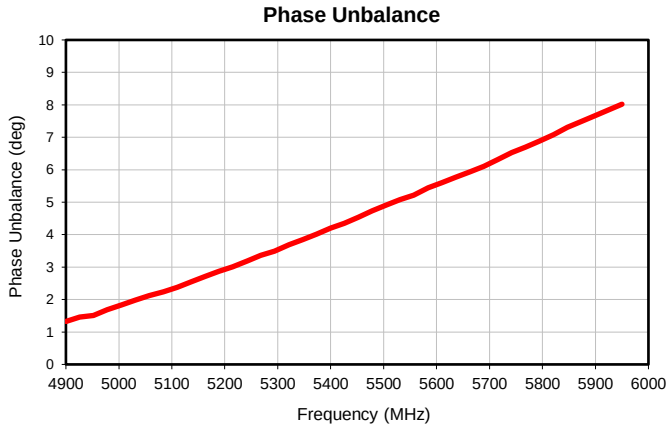
[1] Relative to 180°

Typical Performance Data





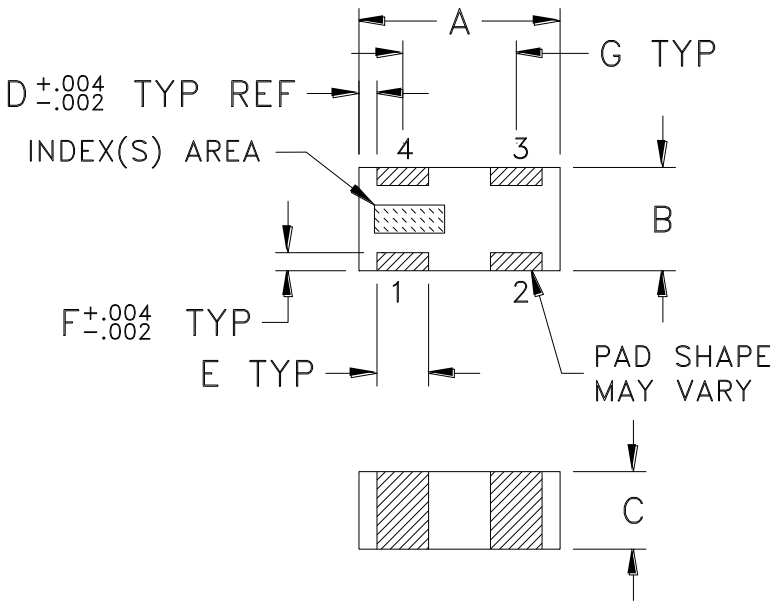




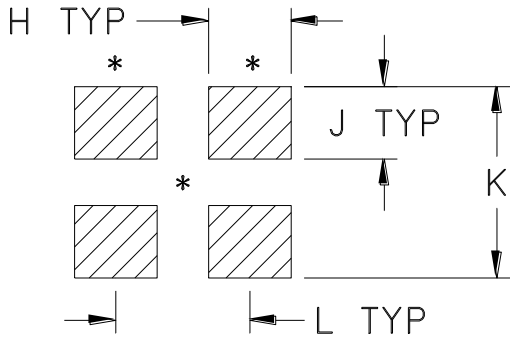
Case Style NK

NK0402C

Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE#	A	B	C	D	E	F	G	H	J	K	L	WT.GRAMS
NK0402C	.039 (1.00)	.020 (.50)	.015 (0.37)	.004 (0.10)	.010 (0.25)	.004 (0.10)	.022 (0.55)	.016 (0.41)	.014 (0.36)	.037 (0.94)	.026 (0.65)	.0007

Dimensions are in inches (mm). Tolerances: 2 Pl. ± .01; 3 Pl. ± .005

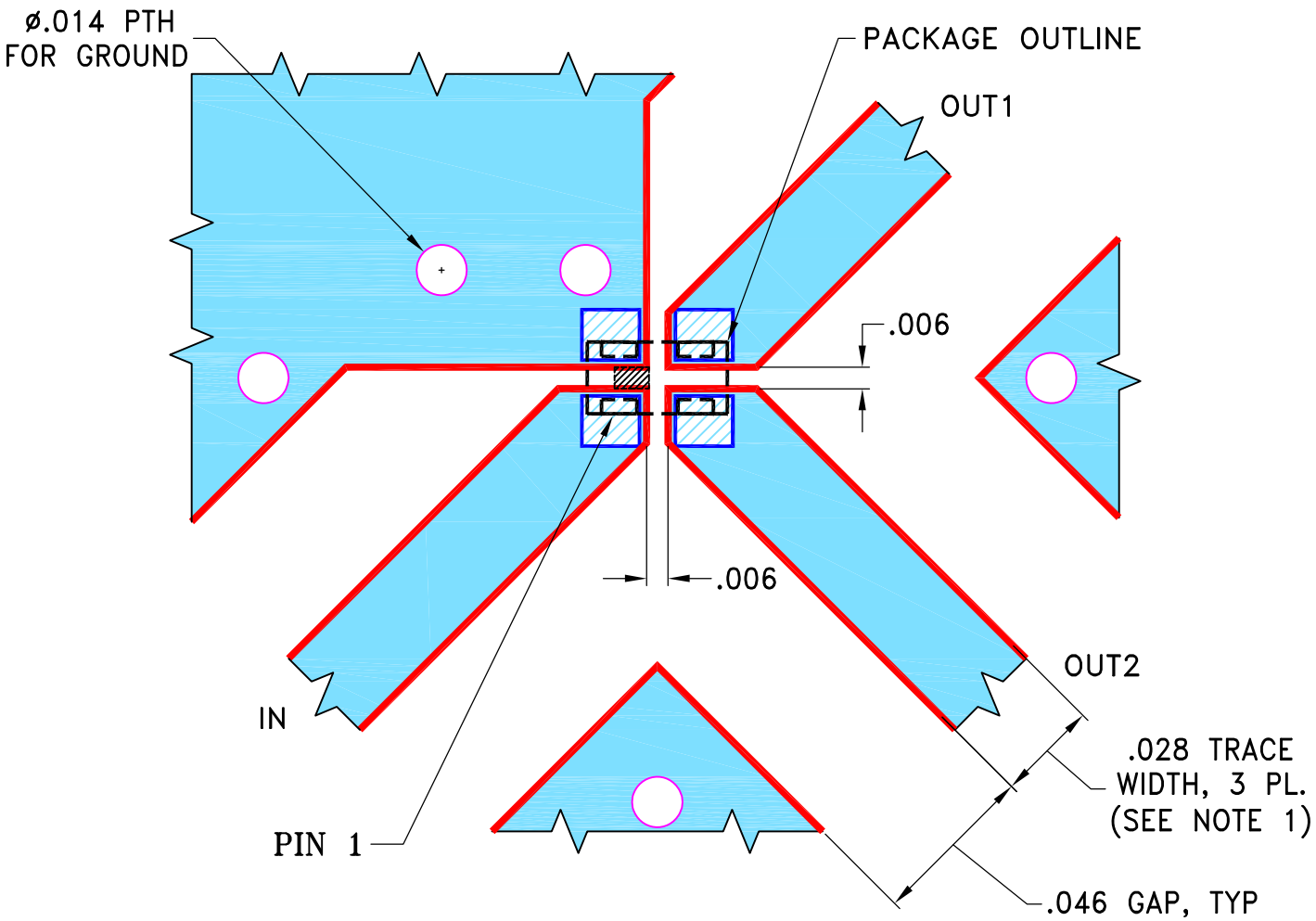
Notes:

1. Open style, ceramic base.
2. Termination finish:
   For RoHS Case Styles: Matte Tin over Nickel plating. Models with (+) suffix.
3. *Line width should be designed to match 50Ω characteristic impedance, depending on PCB material and thickness.

THIRD ANGLE PROJECTION

REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M173420	NEW RELEASE	03/26/19	ITG	SL

SUGGESTED MOUNTING CONFIGURATION
FOR NK0402C CASE STYLE, "04TG01" PIN CODE



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UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	ITG	03/22/19
TOLERANCES ON:	CHECKED	GF	03/22/19
2 PL DECIMALS ±	APPROVED	SL	03/26/19
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

ASHEETA1.DWG REV:A DATE:01/12/95

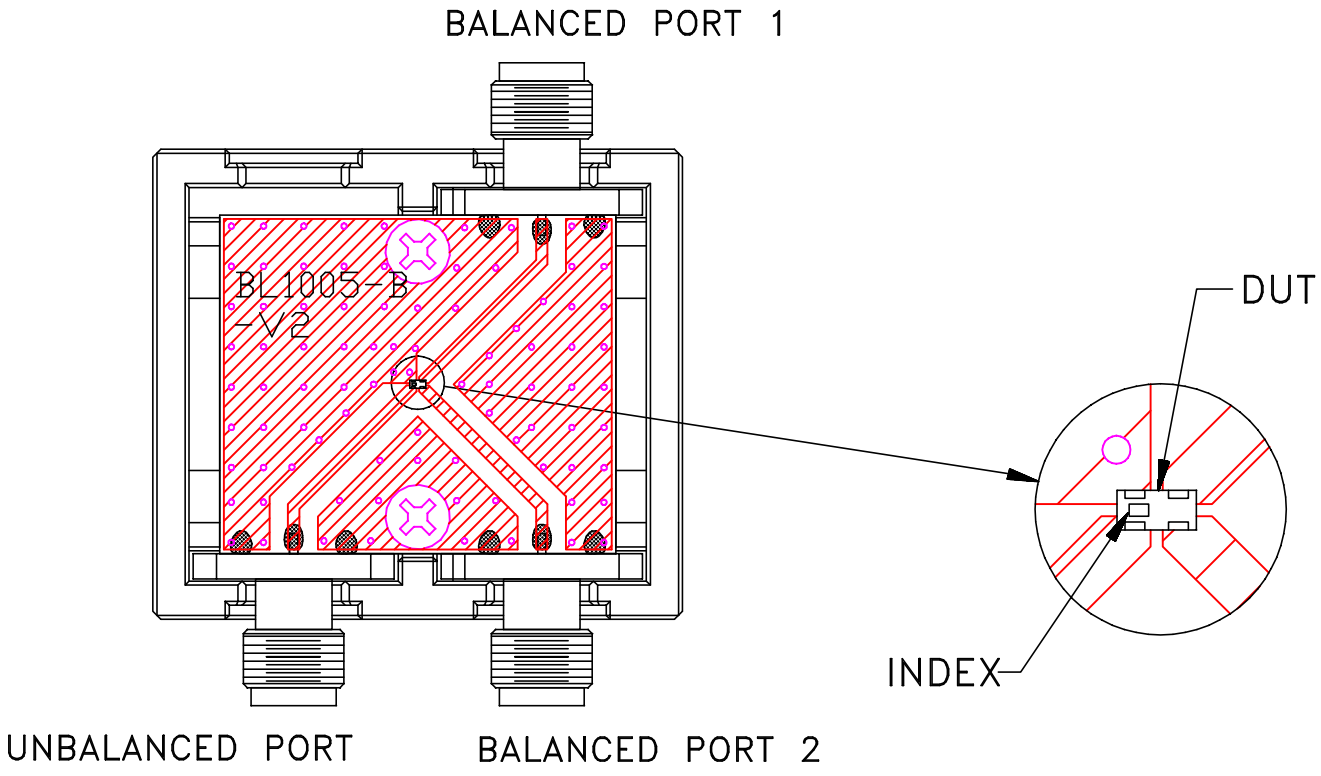
Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

PL, 04TG01, NK0402C, TB-1012-X542+

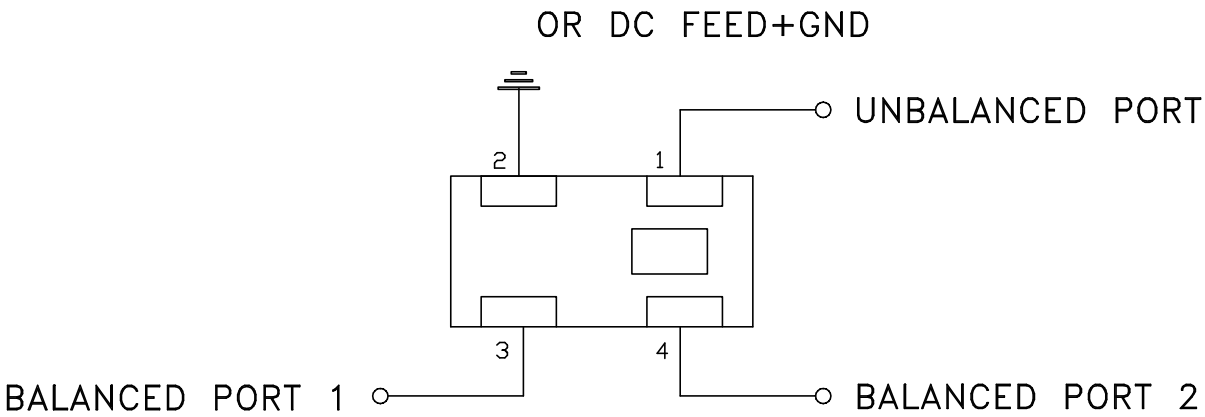
SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-624	OR

FILE:	SCALE:	SHEET:
98PL624	20:1	1 OF 1

Evaluation Board and Circuit



TB-1012-1542+



Schematic Diagram

Notes:

1. 50 Ohm SMA Female connectors.
2. PCB Material: FR4 or equivalent,
   Dielectric Constant=4.5, Thickness=.016 inch.

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Environmental Specifications **ENV107**

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-40° to 85° C Ambient Environment	Individual Model Data Sheet
Thermal Shock	-40° to 125°C, 100 cycles	MIL-STD-202 Method 107, Condition A-3 except -40°C instead of -55° C and +125° C instead of -85° C
Solder Reflow Heat	Pb-Free Process 245° -250°C peak,	J-STD-020, 4-2 and 5-2,Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Shelf Life	Shelf life is 12 months when kept in sealed bags. Unused parts are to be resealed to preseve shelf life for proper solderability.	