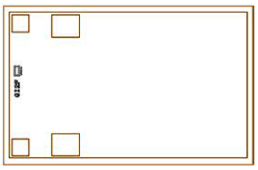
Microwave

Gain Equalizer Die

EQY-D-SERIES

50Ω DC to 6 GHz



The Big Deal

- Excellent Return Loss, 20dB typ.
- Wide bandwidth, DC - 6 GHz
- Excellent power handling 31 dBm

Product Overview

EQY Series Dice of absorptive Gain Equalizers are fabricated using highly repetitive GaAs IPD* MMIC process incorporating resistors, capacitors and inductors having negative insertion loss slope. EQYs are available with nominal attenuation slope of 1,2,3,4,5,6,8 & 10 dB.

Key Features

Feature	Advantages
Negative Insertion Loss Slope vs. Frequency	Useful for compesating negative gain slope of amplifiers, receivers, transmitters to achieve flat gain versus frequency.
Wide range of values 1,2,3,4,5,6,8 & 10 dB	Enables circuit designer to change nominal insertion loss values without mother-board redesign making the EQY series ideal for select at test application.
Wideband operation, DC to 6 GHz	Supports a wide array of applications including wireless cellular, microwave communi-cations, satellite, defense and aerospace, medical broadband and optic applications.
Excellent Power Handling Capability 31/32 dBm	Enables its use at the output of a variety of amplfiers
Unpackaged Die	Enables the user to integrate the gain equalizer directly into hybrids

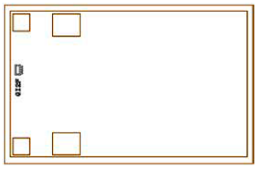
*GaAs IPD (Gallium Arsenide Integrated Passive Device)

Microwave

Gain Equalizer Die

EQY-2-63-D+

50Ω 2dB DC to 6 GHz



Product Features

- 2.1 dB Slope
- Excellent power handling 31 dBm
- Wide Bandwidth, DC-6 GHz
- Excellent Return Loss, 20 dB typ.

Typical Applications

- Cellular
- PCS
- Communications
- Radar
- Defense

+RoHS Compliant

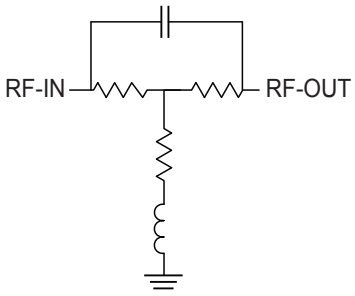
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Ordering Information: Refer to Last Page

General Description

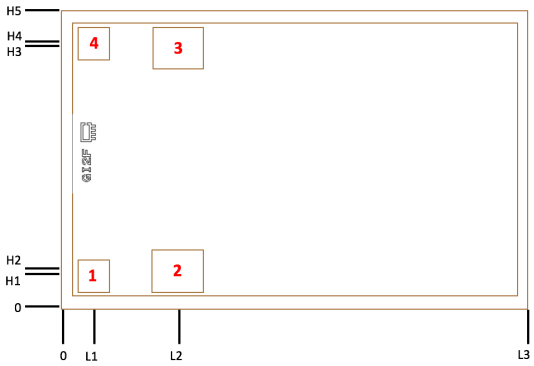
EQY-2-63-D+ is an absorptive Gain Equalizer Die fabricated using highly repetitive GaAs IPD MMIC process incorporating resistors, capacitors and inductors having negative insertion loss slope. EQY-2-63-D+ has a nominal attenuation slope of 2.1 dB.

Simplified Schematic and Pad description



Pad Number	Function	Description
2	RF-IN	RF-Input pad
3	RF-OUT	RF-Output pad
1,4 & Bottom of Die	GND	Ground

Bonding Pad Position



Dimensions in µm, Typical

L1	L2	L3	H1	H2	H3	H4	H5	Thickness	Die Size	Bond Pad #1, #4	Bond Pad #2, #3
74	274	1107	74	86	613	626	700	100	1107x700	67 X 67	117 X 92

Electrical Specifications[1] at 25°C, 50Ω, unless otherwise noted.

Parameter	Condition (GHz)	Min.	Typ.	Max.	Units
Frequency Range		DC		6	GHz
Insertion Loss	0.01		2.5		dB
	1		2.4		
	2		2.1		
	3		1.5		
	4		1.0		
	5		0.6		
	6		0.4		
VSWR	0.01 -1		1.02		:1
	1 - 2		1.03		
	2 - 3		1.04		
	3 - 4		1.08		
	4 - 5		1.17		
	5 - 6		1.29		

1. Measured on Mini-Circuits Characterization Test Board. Die is packaged in 2x2mm, 8-lead MCLP and soldered on testboard TB-1041-2-63+ See Characterization Test Circuit (Fig. 1)

Absolute Maximum Ratings[2]

Operating Case Temperature	-40°C to 85°C
RF Input Power	31 dBm

2. Permanent damage may occur if any of these limits are excedeed.

Characterization Test Circuit

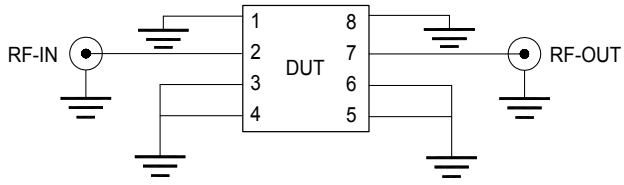
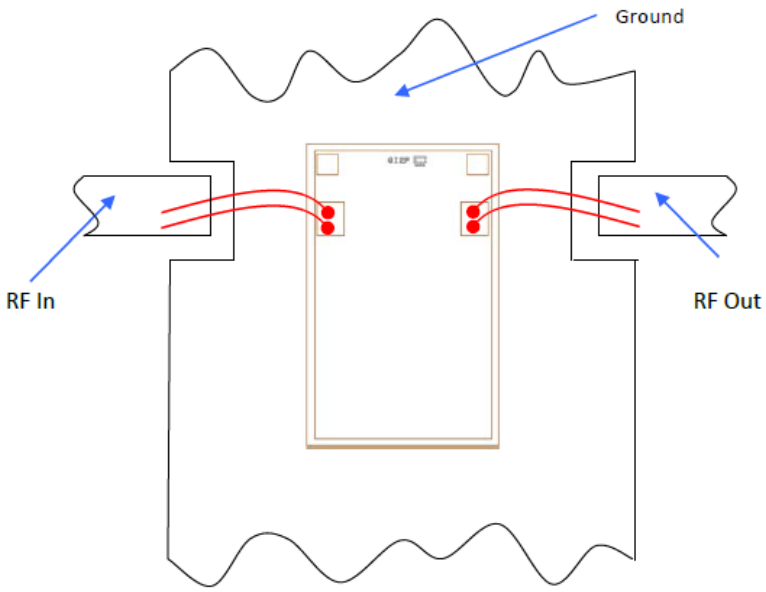


Fig 1. Block Diagram of Test Circuit used for characterization. Die is packaged in 2x2mm, 8-lead MCLP and soldered on testboard TB-1041-2-63+
Conditions: Attenuation & Return Loss Pin=0 dBm

Assembly Diagram



Assembly and Handling Procedure

1. Storage
   Dice should be stored in a dry nitrogen purged desiccators or equivalent.
2. ESD
   MMIC GaAs Gain equalizer dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.
3. Die Attach
   The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030HK-PT/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic die pick up tools only.
4. Wire Bonding
   Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.

Additional Detailed Technical Information

additional information is available on our dash board.

Performance Data	Data Table	
	Swept Graphs	
	S-Parameter (S2P Files) Data Set with and without port extension(.zip file)	
Case Style	Die	
Die Ordering and packaging information	Quantity, Package	Model No.
	Small, Gel - Pak: 5,10,50,100 KGD*	EQY-2-63-DG+
	Medium†, Partial wafer: KGD*<1690	EQY-2-63-DP+
	Large†, Full Wafer	EQY-2-63-DF+
	†*Available upon request contact sales representative*	
	Refer to AN-60-067	
Environmental Ratings	ENV80	

*Known Good Dice ("KGD") means that the dice in question have been subjected to Mini-Circuits DC test performance criteria and measurement instructions and that the parametric data of such dice fall within a predefined range. While DC testing is not definitive, it does help to provide a higher degree of confidence that dice are capable of meeting typical RF electrical parameters specified by Mini-Circuits.

ESD Rating**

Human Body Model (HBM): Class 2 (Pass 2000V) in accordance with ANSI/ESD STM 5.1 - 2001

** Tested in industry standard 2 x 2mm, 8-lead MCLP package

Additional Notes

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.

B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.

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MMIC Gain Equalizer Die EQY-2-63-D+

Typical Performance Data

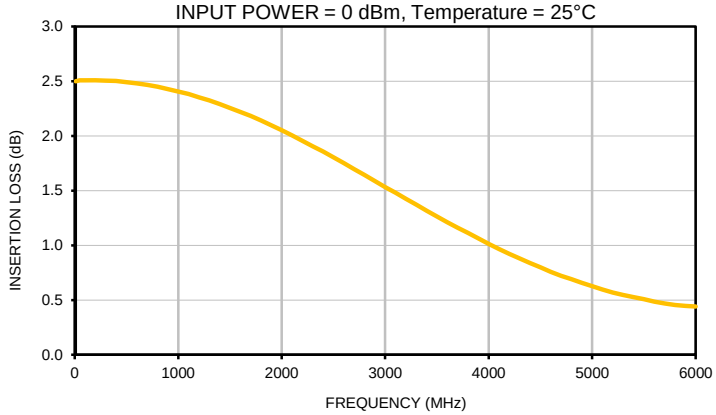
Temperature = 25°C

FREQUENCY (MHz)	INSERTION LOSS (dB)	INPUT VSWR (:1)	OUTPUT VSWR (:1)
10	2.50	1.02	1.02
50	2.51	1.01	1.02
100	2.51	1.02	1.02
200	2.51	1.02	1.02
300	2.51	1.02	1.02
400	2.50	1.02	1.02
500	2.49	1.02	1.02
600	2.48	1.02	1.02
700	2.47	1.02	1.02
800	2.45	1.02	1.02
900	2.43	1.02	1.03
1000	2.40	1.02	1.03
1100	2.38	1.02	1.03
1200	2.35	1.03	1.03
1300	2.32	1.03	1.02
1400	2.29	1.03	1.02
1500	2.26	1.03	1.02
1600	2.22	1.03	1.02
1700	2.18	1.03	1.03
1800	2.14	1.03	1.03
1900	2.10	1.03	1.02
2000	2.05	1.03	1.02
2100	2.01	1.03	1.02
2200	1.96	1.03	1.02
2300	1.91	1.03	1.02
2400	1.86	1.04	1.02
2500	1.80	1.05	1.02
2600	1.75	1.05	1.02
2700	1.70	1.06	1.02
2800	1.64	1.06	1.03
2900	1.59	1.06	1.03
3000	1.53	1.06	1.03
3100	1.48	1.06	1.04
3200	1.43	1.07	1.04
3300	1.37	1.07	1.05
3400	1.32	1.08	1.06
3500	1.26	1.08	1.07
3600	1.21	1.09	1.08
3700	1.16	1.10	1.09
3800	1.11	1.11	1.10
3900	1.06	1.12	1.10
4000	1.01	1.12	1.11
4100	0.97	1.13	1.12
4200	0.92	1.14	1.14
4300	0.88	1.16	1.16
4400	0.84	1.17	1.17
4500	0.80	1.18	1.18
4600	0.76	1.19	1.19
4700	0.72	1.20	1.20
4800	0.69	1.21	1.22
4900	0.66	1.23	1.23
5000	0.63	1.23	1.24
5100	0.60	1.24	1.25
5200	0.57	1.24	1.26
5300	0.55	1.26	1.27
5400	0.53	1.28	1.29
5500	0.51	1.29	1.30
5600	0.49	1.29	1.30
5700	0.47	1.29	1.30
5800	0.45	1.30	1.31
5900	0.45	1.31	1.32
6000	0.44	1.33	1.33

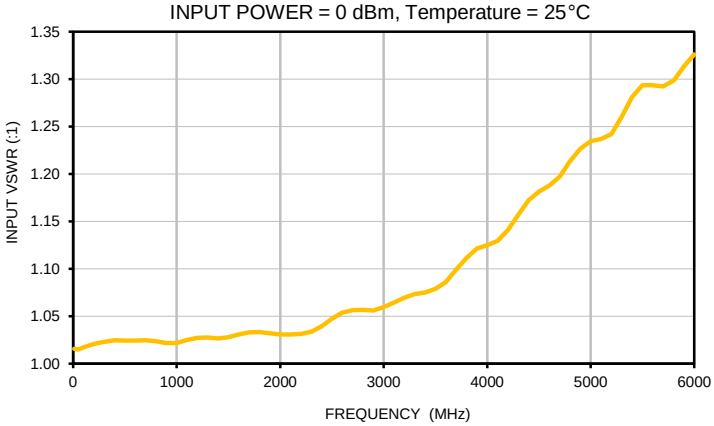
Note: Test data of Die packaged in industry standard 2x2mm 8-Lead MCLP Package

Typical Performance Curves

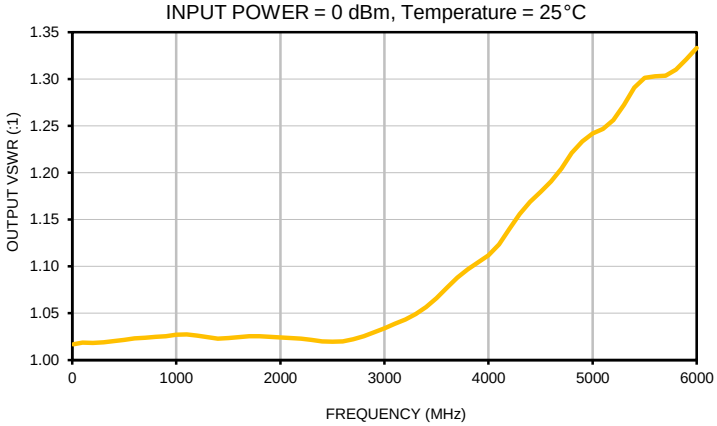
INSERTION LOSS
INPUT POWER = 0 dBm, Temperature = 25°C



INPUT VSWR
INPUT POWER = 0 dBm, Temperature = 25°C



OUTPUT VSWR
INPUT POWER = 0 dBm, Temperature = 25°C



Note: Test data of Die packaged in industry standard 2x2mm 8-Lead MCLP Package

Mini-Circuits

Environmental Specifications ENV80

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C or -40° to 105° C or -55° to 105° C or -45° to 105° C Ambient Environment	Refer to Individual Model Data Sheet
Storage Environment (Die)	-65° to 150°C	Individual Model Data Sheet
Storage Environment(Packaging)	-40° to 70°C and 40 to 60% humidity (In Factory Shipped Package)	

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