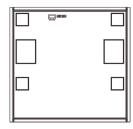
# *Microwave*

# Gain Equalizer Die EQY-XX-453-D+ Series

50 $\Omega$  DC to 45 GHz

# **The Big Deal**

- Excellent Return Loss, 20dB typ.
- Wide bandwidth, DC 45 GHz



# **Product Overview**

EQY-XX-453-D+ series of absorptive Gain Equalizer dice are fabricated using highly repetitive GaAs IPD\* MMIC process incorporating resistors, capacitors and inductors having negative insertion loss slope. EQY-XX-453-D+ are available with nominal attenuation slope of 3, 4, 5, 6, 7, 8, 9 &10dB.

# **Key Features**

Feature	Advantages
Negative Insertion Loss Slope vs. Frequency	Useful for compesating negative gain slope of amplifiers, receivers, transmitters to achieve flat gain versus frequency.
Wide range of values 3,4,5,6,7, 8, 9 &10dB	Enables circuit designer to change nominal insertion loss values without motherboard redesign making the EQY-XX-453-D+ Series ideal for select at test application.
Wideband operation, DC to 45 GHz	Supports a wide array of applications including wireless cellular, microwave communications, satellite, defense and aerospace, medical broadband and optic applications.
Excellent Power Handling Capability	Enables its use at the output of a variety of amplifiers
Unpackaged Die	Enables the user to intergrate the gain equalizer directly into hybrids.

<sup>\*</sup>GaAs IPD (Gallium Arsenide Integrated Passive Device)

# *Microwave*

# Gain Equalizer Die

EQY-8-453-D+

 $50\Omega$  8.2dB DC to 45 GHz

#### **Product Features**

- 8.2 dB Slope
- Wide Bandwidth, DC-45 GHz
- Excellent Return Loss, 20 dB typ.

# WE HABY

## **Typical Applications**

- Cellular Infrastructure
- 5G
- Wideband Communications
- Test Instrumentation
- Defense

#### +RoHS Compliant

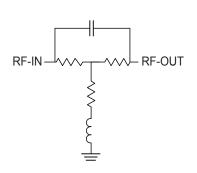
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Ordering Information: Refer to Last Page

#### **General Description**

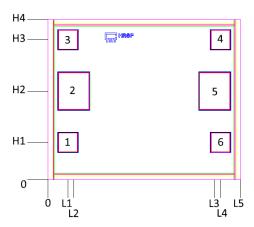
EQY-8-453-D+ is an absorptive Gain Equalizer Die fabricated using highly repetitive GaAs IPD MMIC process incorporating resistors, capacitors and inductors having negative insertion loss slope. EQY-8-453-D+ has a nominal attenuation slope of 8.2 dB.

## Simplified Schematic and Pad description



Pad Number	Function	Description
2	RF-IN	RF-Input pad
5	RF-OUT	RF-Output pad
1,3,4,6	GND	Ground

## **Bonding Pad Position**



Dimensions in µm, Typical

									, ,			
L1	L2	L3	L4	L5	H1	H2	НЗ	H4	Thickness	Die Size	Pad Size 2 & 5	Pad Size 1.3.4.6
											200	1,3,4,0
78	100	650	672	750	146	346	546	625	100	750x625	117x142	72x72

#### Electrical Specifications<sup>1</sup> at 25°C, $50\Omega$ , unless otherwise noted.

Parameter	Condition (GHz)	Min.	Тур.	Max.	Units
Frequency Range		DC		45	GHz
Insertion Loss	0.01	_	9.4	_	dB
	10	_	7.8	_	
	20	_	5.1	_	
	30	_	2.8	_	
	40	_	1.3	_	
	45	_	1.2	_	
VSWR	0.01 - 10	_	1.16	_	:1
	10 - 20	_	1.15	_	
	20 - 30	_	1.14	_	
	30 - 40	_	1.15	_	
	40 - 45	_	1.22	_	

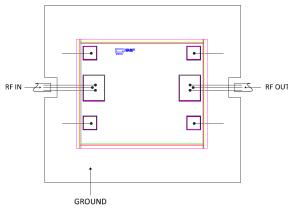
<sup>1.</sup> Electrical specifications are typical measured using MPI Titan Series 200um pitch GSG probe.

#### Absolute Maximum Ratings<sup>2</sup>

Operating Case Temperature	-55°C to 105°C
RF Input Power <sup>3</sup>	27 dBm

- 2. Permanent damage may occur if any of these limits are exceeded.
- 3. Derates linearly to 24 dBm at 105°C

#### **Assembly Diagram**



#### **Assembly and Handling Procedure**

#### Storage

Dice should be stored in a dry nitrogen purged desiccators or equivalent.

#### 2. ESD

MMIC GaAs Gain equalizer dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static worksta tion. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.

#### 3. Die Attach

The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030HK-PT/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic die pick up tools only.

#### 4. Wire Bonding

Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.



Additional Detailed Technical Information additional information is available on our dash board.						
	Data Table					
Performance Data	Swept Graphs	Swept Graphs				
	S-Parameter (S2P Files) Data Set with	S-Parameter (S2P Files) Data Set with and without port extension(.zip file)				
Case Style	Die	Die				
	Quantity, Package	Model No.				
Die Ordering and packaging	Small, Gel - Pak: 5,10,50,100 KGD* Medium <sup>†</sup> , Partial wafer: KGD*<2250					
information	<sup>†</sup> Available upon request contact sales representative					
	Refer to AN-60-067					
Environmental Ratings	ENV80					

<sup>\*</sup>Known Good Die (KGD) means that the die are taken from PCM good wafer and then visually inspected per Mini-Circuits' criteria. Though this is not definitive, it does provide a higher degree of confidence that die are capable of meeting typical RF electrical parameters specified by Mini-Circuits.

#### **ESD Rating\*\***

Human Body Model (HBM): Class 2 (Pass 2000V) in accordance with ANSI/ESD STM 5.1 - 2001

#### **Additional Notes**

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp
- D. Mini-Circuits does not warrant the accuracy or completeness of the information, text, graphics and other items contained within this document and same are provided as an accommodation and on an "As is" basis, with all faults.
- E. Purchasers of this part are solely responsible for proper storing, handling, assembly and processing of Known Good Dice (including, without limitation, proper ESD preventative measures, die preparation, die attach, wire bond ing and related assembly and test activities), and Mini-Circuits assumes no responsibility therefor or for environmental effects on Known Good Dice.
- F. Mini-Circuits and the Mini-Circuits logo are registered trademarks of Scientific Components Corporation d/b/a Mini-Circuits. All other third-party trademarks are the property of their respective owners. A reference to any third-party trademark does not constitute or imply any endorsement, affiliation, sponsorship, or recommendation by any such third-party of Mini-Circuits or its products.



<sup>\*\*</sup> Tested in industry standard 2 x 2mm, 6-lead MCLP package

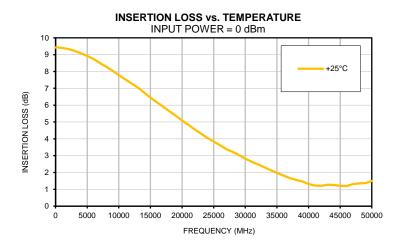
Typical Performance Data

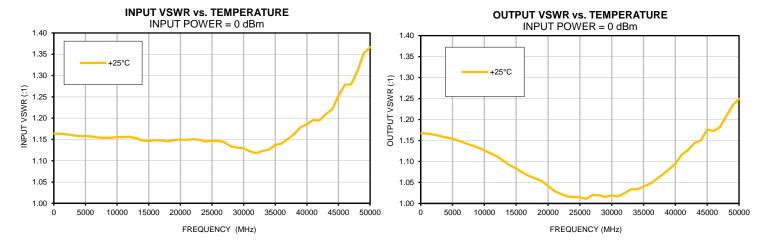
FREQ.	INSERTION LOSS	INPUT VSWR	OUTPUT VSWR	
	(dB)	(:1)		
(MHz)	@25°C	@25°C	@25°C	
10	9.43	1.16	1.17	
20	9.42	1.16	1.17	
30	9.43	1.16	1.17	
40	9.43	1.16	1.17	
50	9.43	1.16	1.17	
60	9.43	1.16	1.17	
70	9.43	1.16	1.17	
80	9.43	1.16	1.17	
90	9.43	1.16	1.17	
100	9.43	1.16	1.17	
200	9.43	1.16	1.17	
400	9.43	1.16	1.17	
600	9.42	1.16	1.17	
800	9.41	1.16	1.17	
1000	9.41	1.16	1.17	
2000	9.34	1.16	1.16	
3000	9.23	1.16	1.16	
4000	9.08	1.16	1.16	
5000	8.92	1.16	1.15	
6000	8.75	1.16	1.15	
7000	8.51	1.15	1.14	
8000	8.29	1.15	1.14	
9000	8.05	1.15	1.13	
10000	7.79	1.16	1.13	
11000	7.53	1.16	1.12	
12000	7.29	1.16	1.11	
13000	7.04	1.15	1.10	
14000	6.75	1.15	1.09	
15000	6.45	1.15	1.08	
16000	6.18	1.15	1.07	
17000	5.91	1.15	1.07	
18000	5.64	1.15	1.06	
19000	5.37	1.15	1.05	
20000	5.09	1.15	1.04	
21000	4.82	1.15	1.03	
22000	4.56	1.15	1.02	
23000	4.30	1.15	1.02	
24000	4.06	1.15	1.02	
25000	3.84	1.15	1.01	
26000	3.62	1.15	1.01	
27000	3.39	1.14	1.02	
28000	3.22	1.13	1.02	
29000	3.04	1.13	1.02	
30000	2.82	1.13	1.02	
31000	2.64	1.12	1.02	
32000	2.48	1.12	1.02	
33000	2.31	1.12	1.03	
34000	2.14	1.13	1.03	
35000	1.98	1.14	1.04	
36000	1.83	1.14	1.05	
37000	1.67	1.15	1.06	
38000	1.56	1.16	1.07	
39000	1.47	1.18	1.08	
40000	1.32	1.19	1.09	
41000	1.22	1.20	1.12	
42000	1.20	1.19	1.13	
43000	1.27	1.21	1.14	
44000	1.26	1.22	1.15	
45000	1.20	1.25	1.18	
46000	1.19	1.28	1.17	
47000	1.31	1.28	1.17	
48000	1.35	1.20	1.16	
49000	1.35	1.35	1.21	
49000 50000	1.50	1.35	1.23	
ついいしし	1.50	1.37	1.25	





# Typical Performance Curves





Page 1 of 1







All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C or -40° to 105° C or -55° to 105° C or -45° to 105° C 105° C Ambient Environment	Refer to Individual Model Data Sheet
Storage Environment (Die)	-65° to 150°C	Individual Model Data Sheet
Storage Environment(Packaging)	-40° to 70°C and 40 to 60% humidity (In Factory Shipped Package)	

ENV80 Rev: C

06/10/24 DCO-1455 File: ENV80.pdf

This document and its contents are the property of Mini-Circuits.