



High Pass Filter

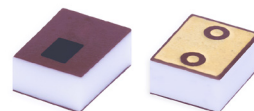
HFHK-3500+

50Ω

3900 to 12500 MHz

THE BIG DEAL

- Insertion Loss, Typ. 0.9 dB
- Stopband Rejection, Typ. 76 dB
- Passband Return Loss, Typ. 15 dB
- 1008 Surface Mount Footprint
- Power Handling: 6 W
- Shielded Construction: Prevents De-Tuning & EMI
- Protected by US Patents 11,638,370 and 11,744,057

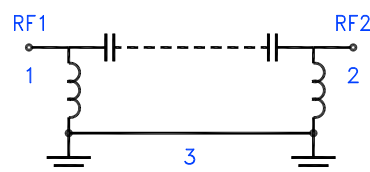


Generic photo used for illustration purposes only

APPLICATIONS

- 5G Sub- 6 GHz
- Radar, EW, ECM Defense Systems
- Test and Measurement Equipment
- Telecommunications and Broadband Wireless Systems
- WiFi 6E

FUNCTIONAL DIAGRAM



PRODUCT OVERVIEW

Mini-Circuits' HFHK-3500+ is a miniature low temperature co-fired ceramic (LTCC) high pass filter with a 3900 to 12500 MHz passband that supports a variety of applications. This model provides 0.9 dB typical insertion loss over a wide band due to its rugged monolithic construction. Housed in a small 1008 ceramic form factor, the filter is ideal for dense signal chain PCB layouts where it complements MMIC size and performance. The LTCC fabrication process assures minimal RF performance variation while delivering a product that is well suited for environmental extremes of high humidity and temperature.

KEY FEATURES

Features	Advantages
Wide Passband, 8.6 GHz	This filter has a very wide passband from, 3900 to 12500 MHz.
LTCC Construction	Provides repeatable performance in a rugged, ceramic package well suited for tough environments such as high humidity and temperature extremes.
Small Size, 1008	Saves space in dense circuit board layouts and minimizes the effects of parasitics.
Rugged Power Handling, 6 Watts	Handles up to 6 Watts in a small 1008 package.
Shielded Construction	Due to the novel construction, it is immune to EMI/EMC effects with other neighboring components/ devices.



LTCC SURFACE MOUNT

High Pass Filter

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ELECTRICAL SPECIFICATIONS^{1,2,3} AT +25°C

Parameter		F#	Frequency (MHz)	Min.	Typ.	Max.	Units
Passband	Insertion Loss	F4-F5	3900 - 5300	—	1.5	2.5	dB
		F5-F6	5300 - 11000	—	0.9	1.6	
		F6-F7	11000 - 12500	—	1.2	2.6	
	Return Loss	F4-F5	3900 - 5300	—	13	—	dB
		F5-F6	5300 - 11000	—	13	—	
		F6-F7	11000 - 12500	—	15	—	
Stopband	Rejection	DC-F1	DC - 1100	67	76	—	dB
		F1-F2	1100 - 1900	40	52	—	
		F2-F3	1900 - 2600	20	30	—	
	Freq. Cut-Off ⁴	Fc	3425	—	3	—	dB

1. Tested on Evaluation Board P/N TB-HFHK-3500+ with connectors and feedline de-embedded with thru-line compensation.

2. This filter is bi-directional RF1 and RF2 ports may be interchanged. See S-Parameters for actual performance.

3. This component should not be used as a DC-block. In applications where DC voltage and/or current is present at either the input or output ports, external DC blocking capacitors are required.

4. Typical variation ±5%.

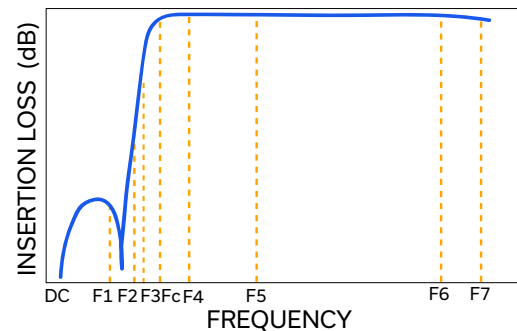
ABSOLUTE MAXIMUM RATINGS⁵

Parameter	Ratings
Operating Temperature	-55°C to +125°C
Storage Temperature	-55°C to +125°C
Input Power ⁶	6 W @ +25°C

5. Permanent damage may occur if any of these limits are exceeded.

6. Power rating applies only to signals within the passband. Power rating above +25°C operating temperature decreases linearly to 1 W at +125°C.

TYPICAL FREQUENCY RESPONSE AT +25°C





LTCC SURFACE MOUNT

High Pass Filter

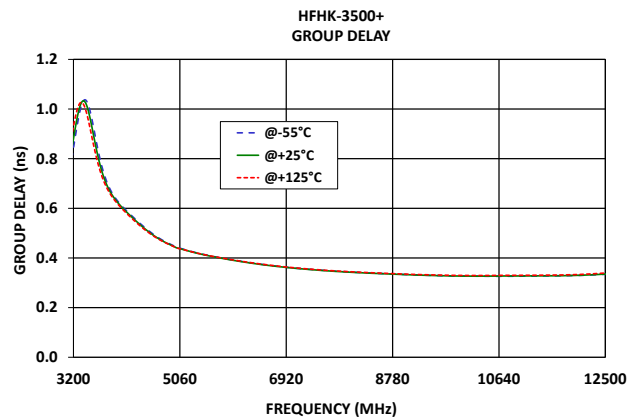
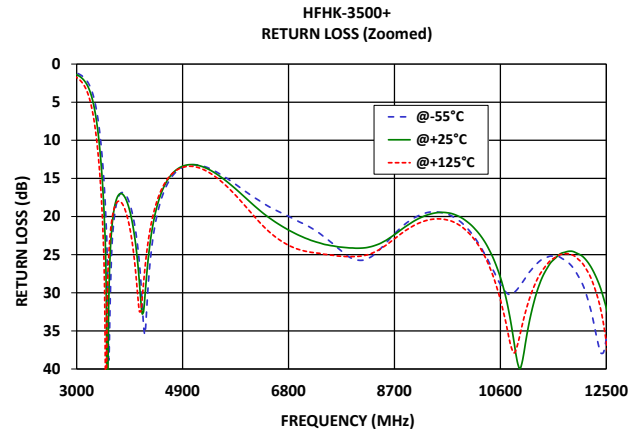
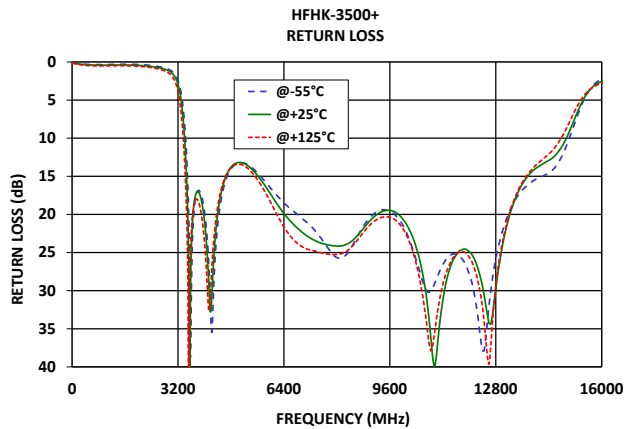
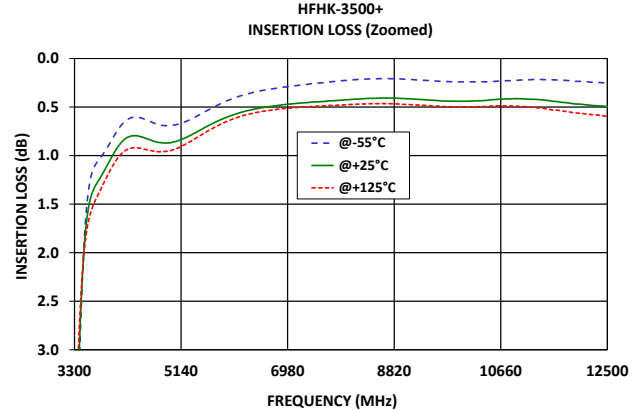
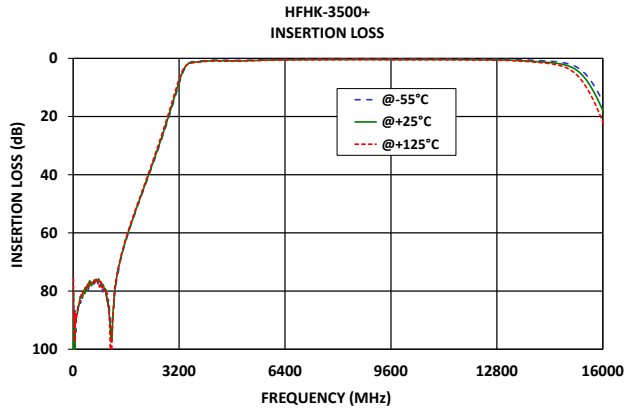
HFHK-3500+

Mini-Circuits

50Ω

3900 to 12500 MHz

TYPICAL PERFORMANCE GRAPHS





Mini-Circuits

LTCC SURFACE MOUNT

High Pass Filter

HFHK-3500+

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FUNCTIONAL DIAGRAM

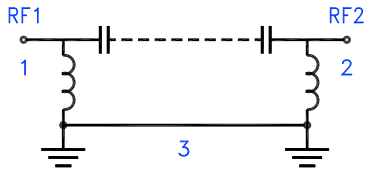
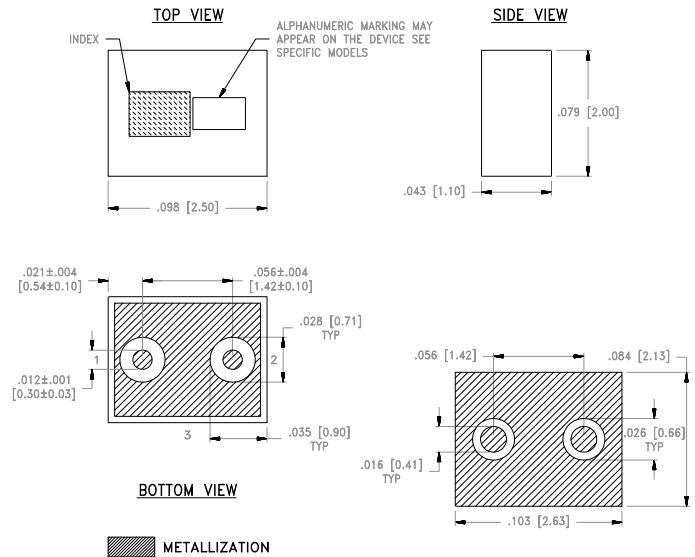


Figure 1. HFHK-3500+ Functional Diagram

PAD DESCRIPTION

Function	Pad Number	Description
RF1 ²	1	Connects to RF Input Port
RF2 ²	2	Connects to RF Output Port
GROUND	3	Connects to Ground on PCB, (See drawing PL-798)
NC	—	No connection, not used internally. See drawing PL-798 for connection to PCB

CASE STYLE DRAWING



Weight: .019 grams.

Dimensions are in inches (mm). Tolerances: 2Pl. ± .01; 3Pl. ± .005

PRODUCT MARKING*: J3

*Marking may contain other features or characters for internal lot control.



SUGGESTED PCB LAYOUT (PL-798)

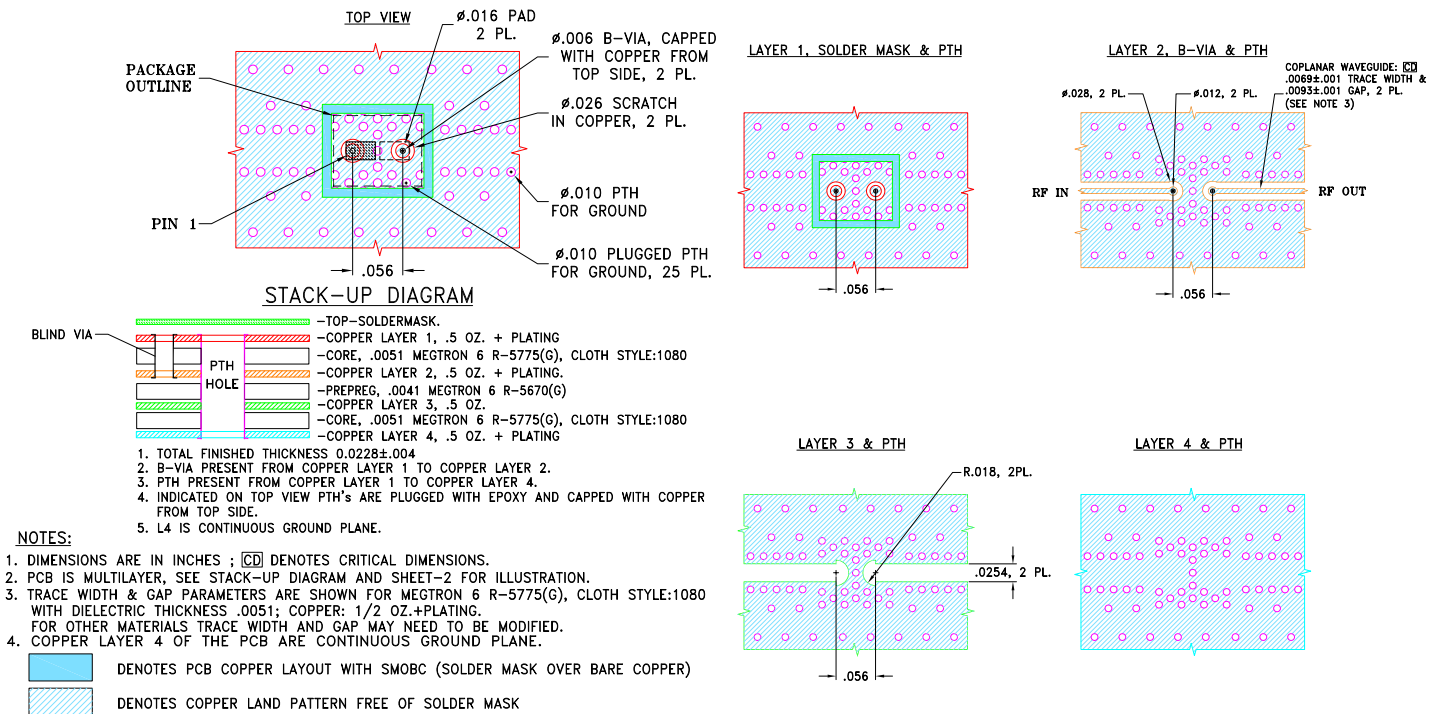


Figure 2. Suggested PCB Layout PL-798



Mini-Circuits

LTCC SURFACE MOUNT

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HFHK-3500+

50Ω

3900 to 12500 MHz

ADDITIONAL DETAILED INFORMATION IS AVAILABLE ON OUR DASHBOARD.

[CLICK HERE](#)

Performance Data and Graphs	Data
	Graphs
	S-Parameter (S2P Files) Data Set (.zip file) De-embedded to device pads
Case Style	NL1008C-10 Lead Finish: Gold over Electroless Nickel
RoHS Status	Compliant
Tape and Reel	TR-F75
Suggested Layout for PCB Design	PL-798
Evaluation Board	TB-HFHK-3500+
	Gerber File
Environmental Rating	ENV06T10

NOTES

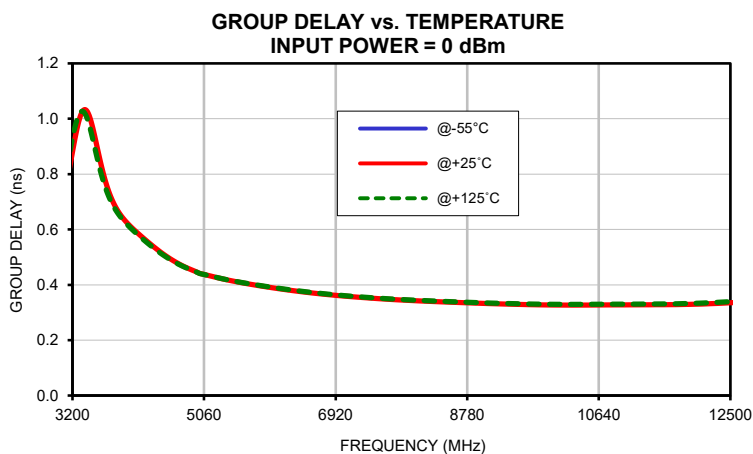
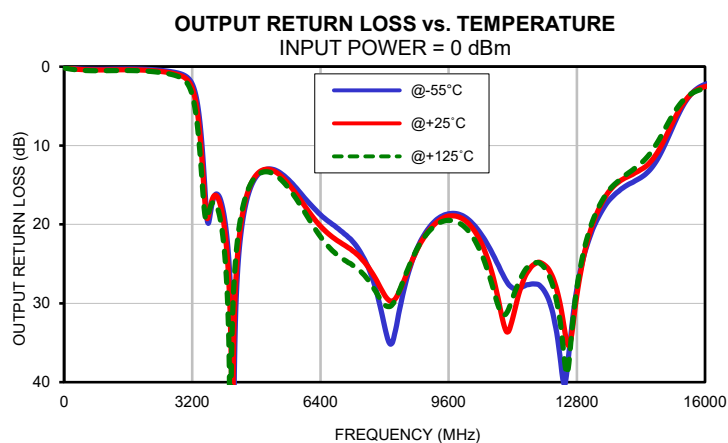
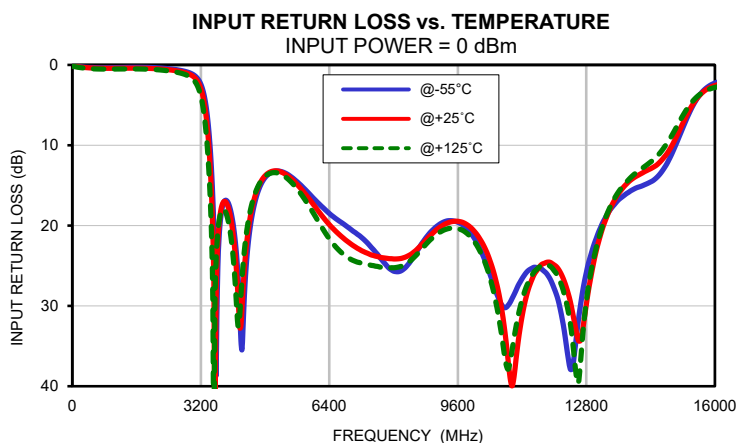
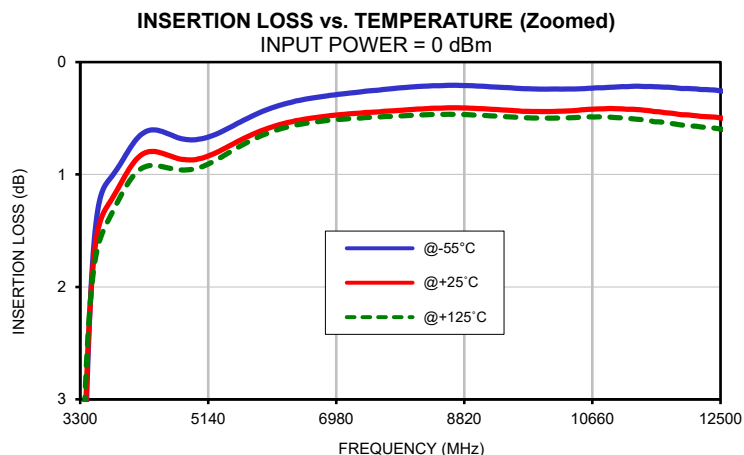
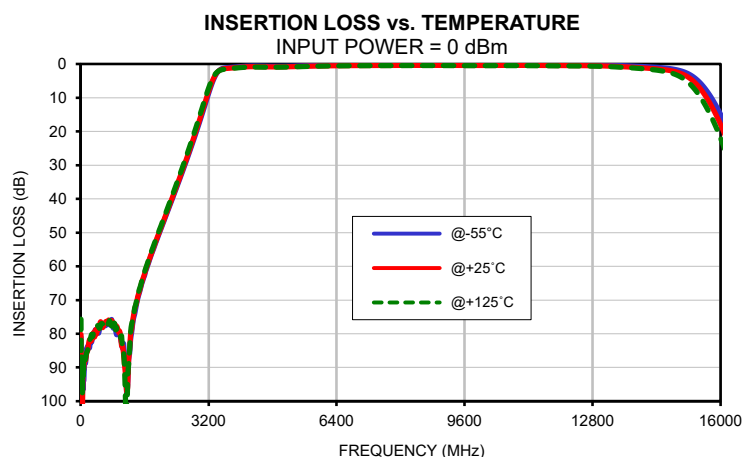
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Typical Performance Data

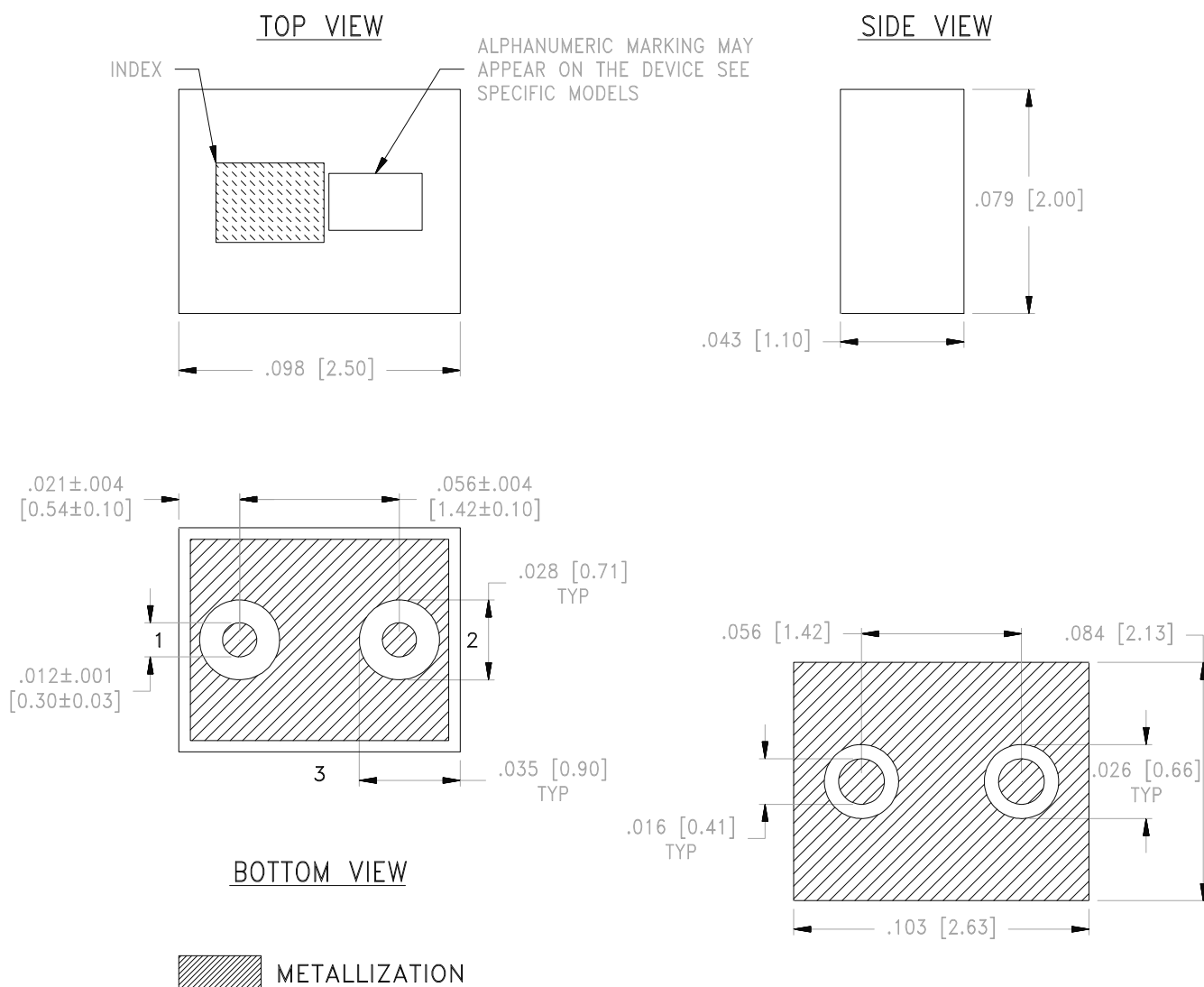
FREQ.	GROUP DELAY		
(MHz)	(nsec)		
	@-55°C	@+25°C	@+125°C
3900	0.65	0.65	0.64
4000	0.62	0.62	0.61
4100	0.60	0.59	0.59
4200	0.57	0.57	0.56
4400	0.53	0.53	0.52
4500	0.51	0.51	0.50
4600	0.50	0.49	0.49
4700	0.48	0.48	0.47
4800	0.47	0.46	0.46
4900	0.45	0.45	0.45
5000	0.44	0.44	0.44
5100	0.44	0.44	0.43
5200	0.43	0.43	0.43
5300	0.42	0.42	0.42
5400	0.42	0.42	0.42
5600	0.41	0.41	0.41
5800	0.40	0.40	0.40
6000	0.39	0.39	0.39
6200	0.38	0.38	0.38
6400	0.38	0.38	0.38
6600	0.37	0.37	0.37
6800	0.37	0.36	0.37
7000	0.36	0.36	0.36
7200	0.36	0.36	0.36
7400	0.35	0.35	0.35
7600	0.35	0.35	0.35
7800	0.35	0.35	0.35
8000	0.34	0.34	0.35
8200	0.34	0.34	0.34
8400	0.34	0.34	0.34
8600	0.34	0.34	0.34
9000	0.33	0.33	0.34
9400	0.33	0.33	0.33
9800	0.33	0.33	0.33
10000	0.33	0.33	0.33
10500	0.33	0.33	0.33
11000	0.33	0.33	0.33
11500	0.33	0.33	0.33
12000	0.33	0.33	0.33
12500	0.33	0.34	0.34

Typical Performance Curves



Outline Dimensions

NL1008C-10



SUGGESTED LAYOUT FOR PCB LAND PATTERN
PATTERN TO BE WITHIN ±.002

Dimensions are in inches (mm). Tolerances: 2Pl. ± .01; 3Pl. ± .005

Notes:

1. Open style, Ceramic base.
2. Termination finish: **as shown below or indicated on Data sheet.**
For RoHS Case Styles: Gold plate over nickel plate. All models, (+) suffix.
3. Weight: .019 grams.
4. Pad tolerance is non-cumulative.

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RFIIF MICROWAVE COMPONENTS

DEVICE ORIENTATION IN T&R

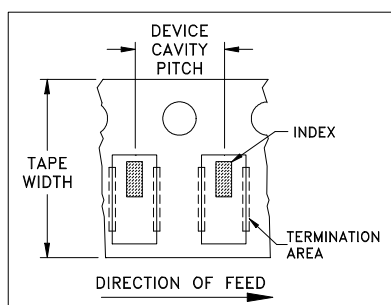


ILLUSTRATION 1

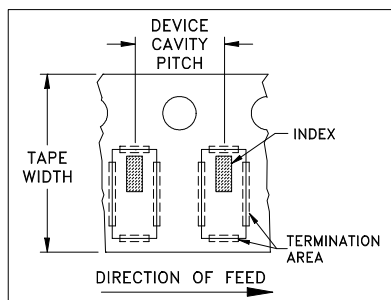


ILLUSTRATION 2

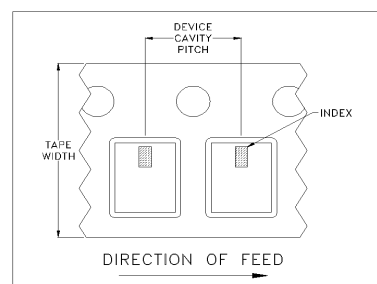


ILLUSTRATION 3

Applicable Case Styles

FV1206-1
FV1206-3

Applicable Case Styles

FV1206-4
FV1206-5
FV1206-6
FV1206-7
FV1206-9

Applicable Case Styles

FV1206-11
FV1206-12
GE0805C-18
NL1008C-6
NL1008C-7
NL1008C-9
NL1008C-10
NL1008C-12

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	3000

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



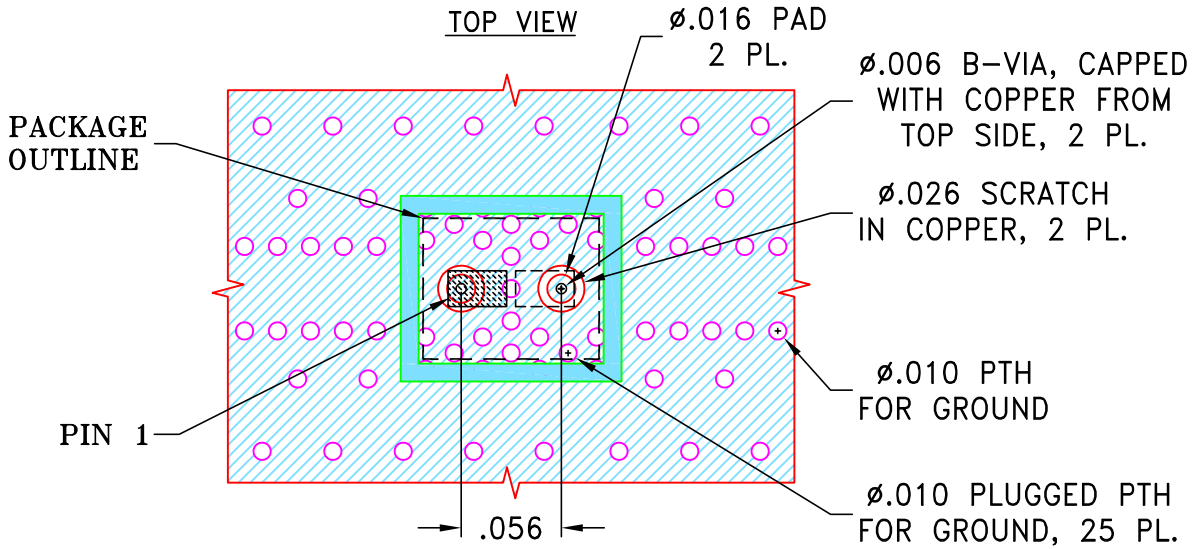
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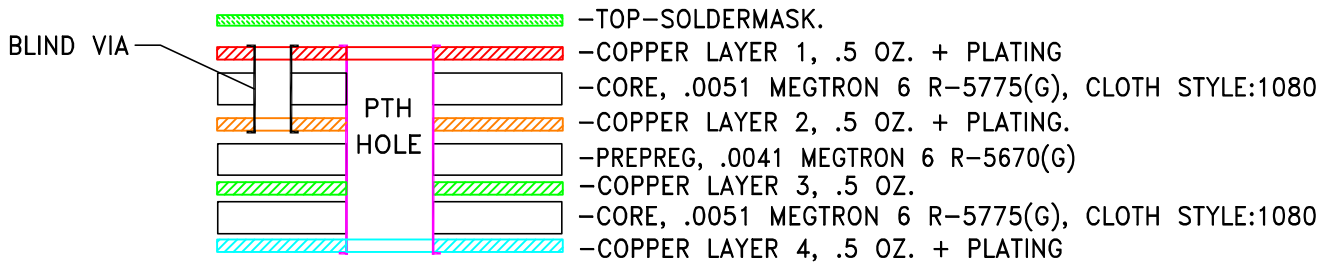
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THIRD ANGLE PROJECTION		REVISIONS					
		REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
		OR	ECO-023057	NEW RELEASE	SEP 24	AGS	GT
		OR1	NPO-004738	ADDED NL1008C-10 CASE STYLE	OCT 24	AGS	GT
		OR2	ECO-023820	NOTES & TOLERANCES UPDATED	NOV 24	AGS	GT

SUGGESTED MOUNTING CONFIGURATION FOR
NL1008C-9,NL1008C-10 CASE STYLE



STACK-UP DIAGRAM



1. TOTAL FINISHED THICKNESS 0.0228±.004
2. B-VIA PRESENT FROM COPPER LAYER 1 TO COPPER LAYER 2.
3. PTH PRESENT FROM COPPER LAYER 1 TO COPPER LAYER 4.
4. INDICATED ON TOP VIEW PTH'S ARE PLUGGED WITH EPOXY AND CAPPED WITH COPPER FROM TOP SIDE.
5. L4 IS CONTINUOUS GROUND PLANE.

NOTES:

1. DIMENSIONS ARE IN INCHES ; $\boxed{CD}$ DENOTES CRITICAL DIMENSIONS.
2. PCB IS MULTILAYER, SEE STACK-UP DIAGRAM AND SHEET-2 FOR ILLUSTRATION.
3. TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR MEGTRON 6 R-5775(G), CLOTH STYLE:1080 WITH DIELECTRIC THICKNESS .0051; COPPER: 1/2 OZ.+PLATING.
FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
4. COPPER LAYER 4 OF THE PCB ARE CONTINUOUS GROUND PLANE.



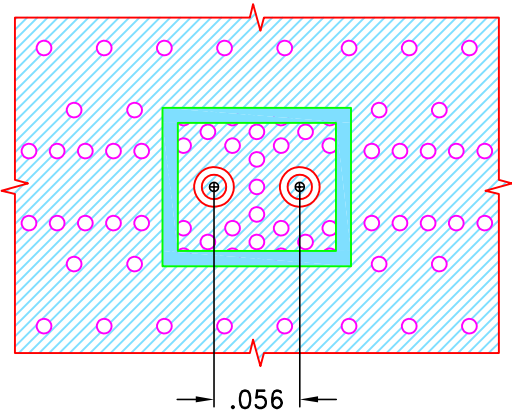
DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)



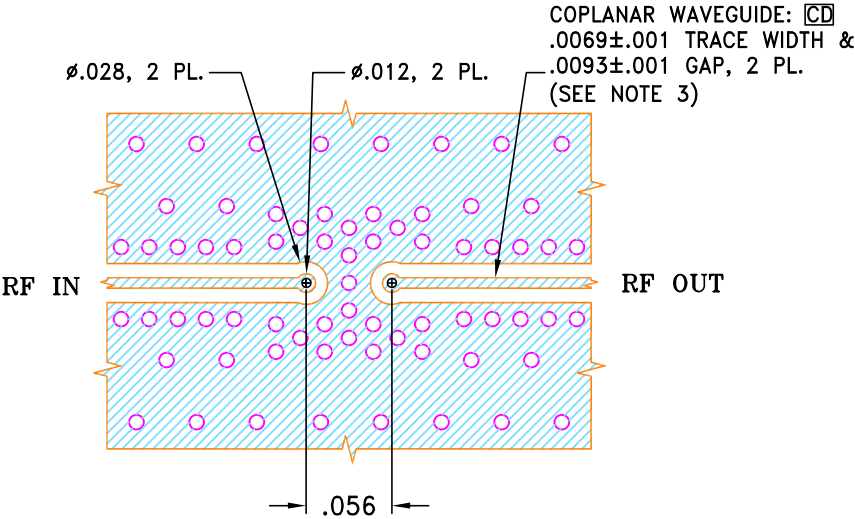
DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED	INITIALS		DATE	 Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235			
	DRAWN	AGS	14 SEP 24				
	CHECKED	MD	16 SEP 24				
	APPROVED	GTP	17 SEP 24				
DIMENSIONS ARE IN INCHES TOLERANCES ON: 2 PL DECIMALS ± 3 PL DECIMALS ± .002 ANGLES ± FRACTIONS ±				PL, NL1008C-9/10, TB-LFHK-XXXX+			
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ASHEETA1.DWG REV:A DATE:01/12/95				FILE: 98-PL-798	SCALE: 9:1	SHEET: 1 OF 2	

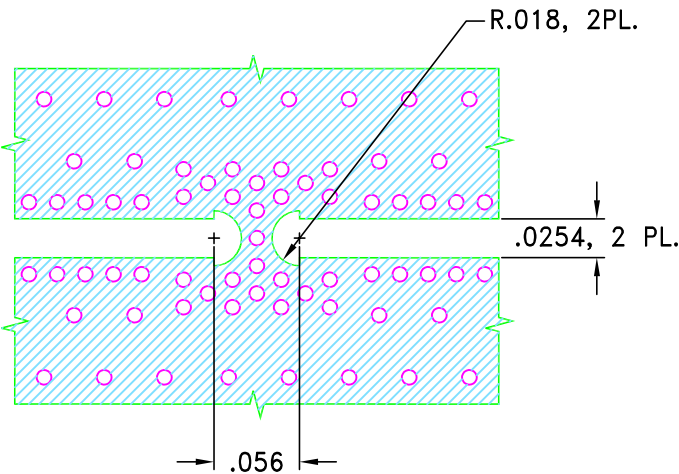
LAYER 1, SOLDER MASK & PTH



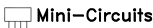
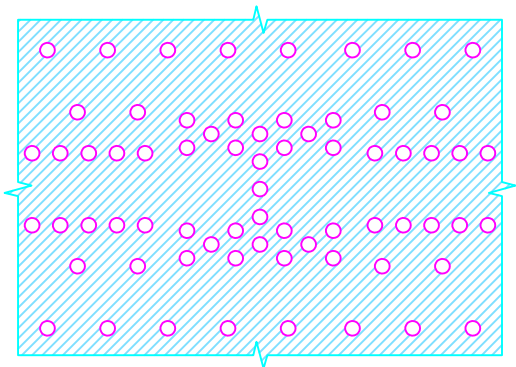
LAYER 2, B-VIA & PTH



LAYER 3 & PTH



LAYER 4 & PTH



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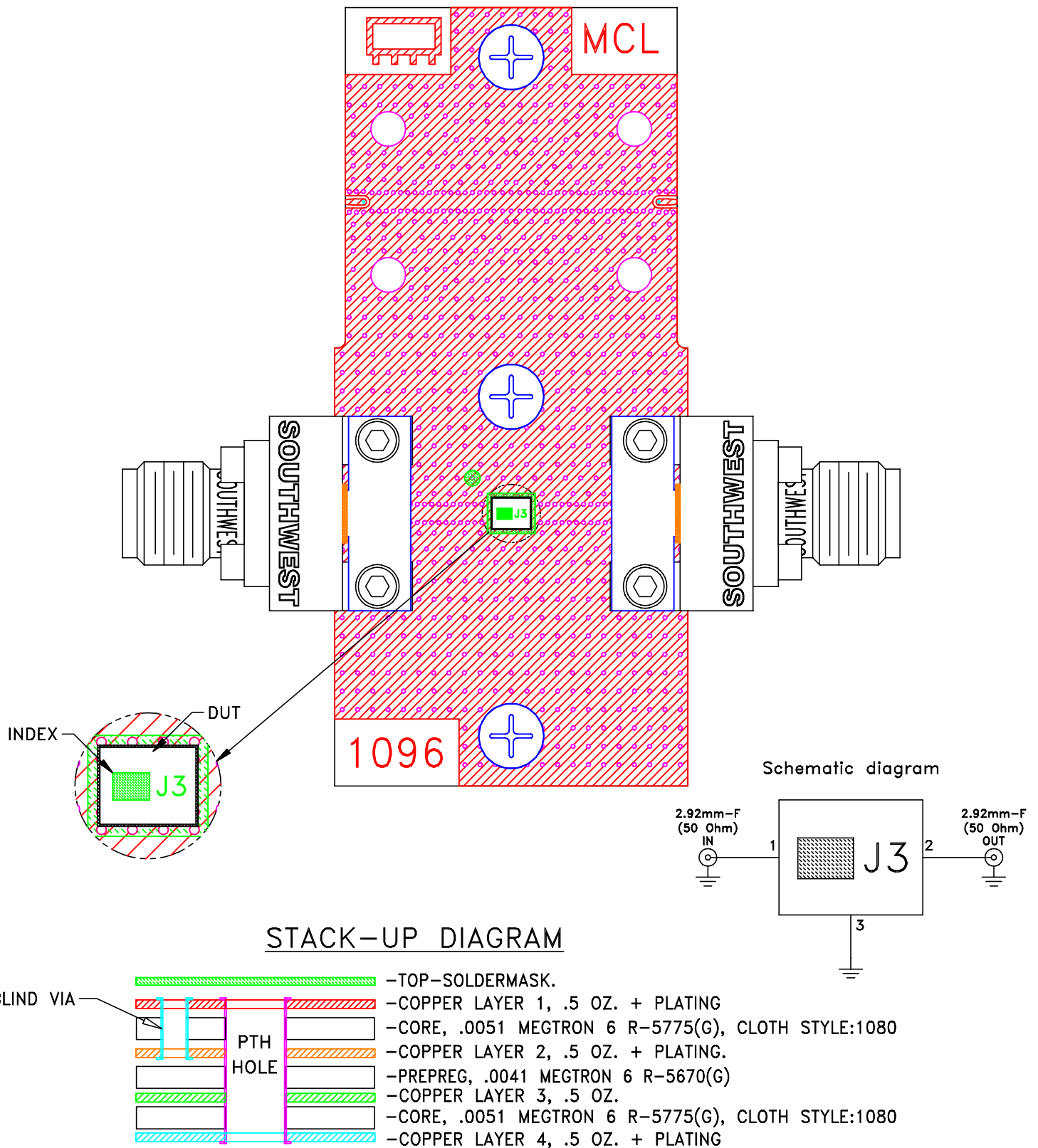
ASHEETA2.DWG REV:A DATE: 01/12/94

ALL DIMENSIONS ARE IN INCHES EXCEPT OTHERWISE SPECIFIED

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-798	OR2
FILE:	98-PL-798	SCALE: 7.7:1	SHEET: 2 OF 2

Evaluation Board and Circuit

TB-HFHK-3500+



Notes:

1. PCB Material: MEGTRON-6 R5775(G) OR Equivalent, Dielectric Constant=3.6
2. Total finished thickness: .023
3. 50 Ohm 2.92mm Female End Launch Connector.

Mini-Circuits®



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 125° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutectic Process 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020C, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Test B,B1, 95% Coverage
Thermal Shock	-55° to +125°C, 15 min dwell,250 cycles	MIL-STD-202, Method 107
Bend Test	1mm, deflection for 5 seconds Span of bending: 2.75"	--
High Temp Storage	125°C to 1000 Hrs	---