

Engineering Development Model

Voltage Controlled Oscillator

JCOS-ED6865/2

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : BG419

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	849		1013	MHz
Tuning Voltage	1		15	V
Power Output		+7		dBm
Phase Noise				
at 1 kHz offset		-90		dBc/Hz
at 10 KHz offset		-116		dBc/Hz
at 100 KHz offset		-127		dBc/Hz
at 1000 kHz offset		-155		dBc/Hz
Pulling at 12 dBm PK-PK all phases		1		MHz
Pushing at Vcc=5V±0.25V		0.02		MHz/V
Tuning Sensitivity		12 - 20		MHz/V
Harmonic Suppression		-40		dBc
3 dB Modulation Bandwidth		2000		kHz
Supply Voltage		11		V
Supply Current			30	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+13V
Absolute Tuning Voltage (Vtune)	+17V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	1
GROUND	2,3,4,5,6,9,10,11,12,13,14

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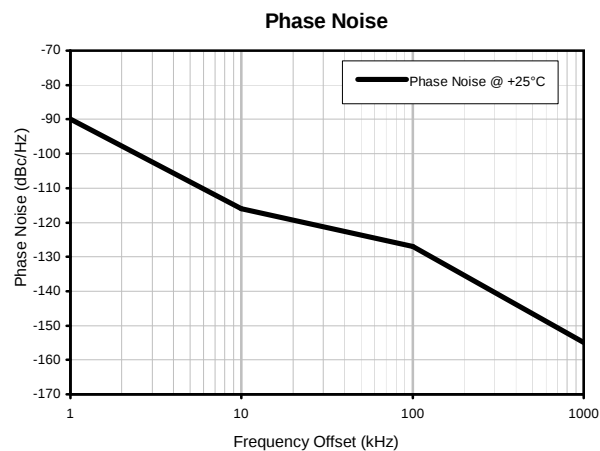
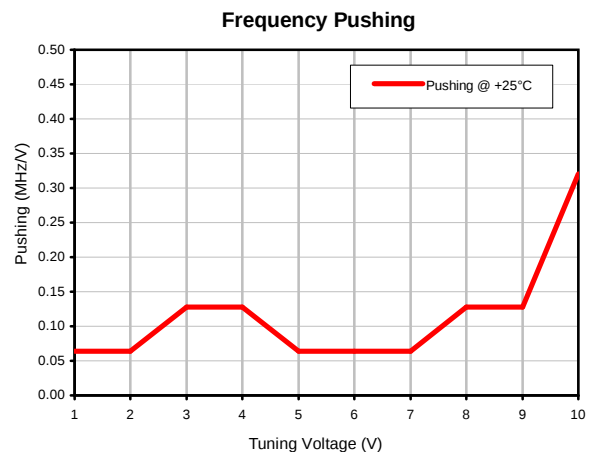
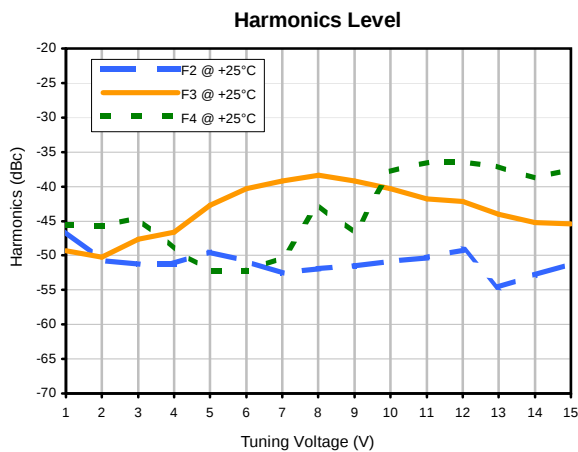
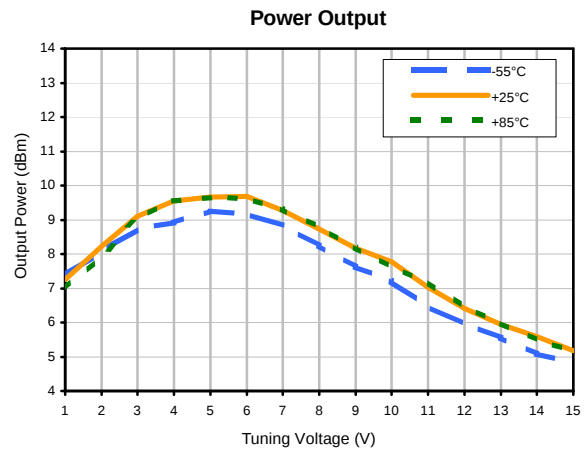
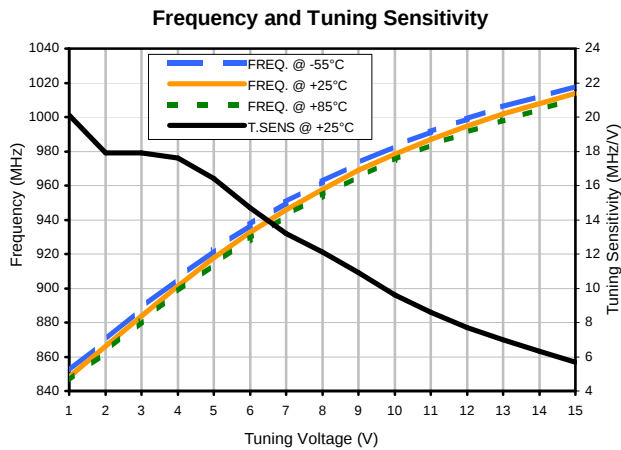
Typical Performance Data

V TUNE	TUNE SENS (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)			HARMONICS (dBc)			FREQ. PUSH (MHz/V)	FREQ OFFSET (KHz)	PHASE NOISE (dBc/Hz)
		-55°C	+25°C	+85°C	-55°C	+25°C	+85°C	F2	F3	F4			
1.0	20.10	851.5	848.1	845.8	7.38	7.25	6.99	-46.4	-49.3	-45.6	0.06	1	-90
2.0	17.90	869.9	866.0	863.1	8.07	8.23	7.95	-50.7	-50.2	-45.7	0.06	10	-116
3.0	17.90	888.2	883.9	880.6	8.72	9.10	9.04	-51.3	-47.6	-44.5	0.13	100	-127
4.0	17.60	905.9	901.5	898.0	8.92	9.55	9.56	-51.2	-46.6	-49.0	0.13	1000	-155
5.0	16.40	922.3	917.9	914.3	9.26	9.67	9.64	-49.5	-42.7	-52.3	0.06		
6.0	14.70	937.0	932.6	929.1	9.16	9.69	9.63	-50.8	-40.3	-52.3	0.06		
7.0	13.20	950.1	945.8	942.3	8.85	9.28	9.30	-52.6	-39.2	-50.2	0.06		
8.0	12.10	962.2	957.9	954.4	8.24	8.72	8.79	-51.9	-38.4	-42.6	0.13		
9.0	10.90	973.2	968.8	965.2	7.63	8.16	8.19	-51.5	-39.2	-46.7	0.13		
10.0	9.60	982.8	978.5	974.8	7.22	7.77	7.61	-50.8	-40.3	-37.8	0.32		
11.0	8.60	991.3	987.0	983.4	6.47	7.02	7.20	-50.3	-41.8	-36.5			
12.0	7.70	999.0	994.7	991.2	5.97	6.41	6.45	-49.2	-42.2	-36.4			
13.0	7.00	1005.9	1001.7	998.2	5.56	5.95	5.98	-54.6	-44.0	-37.1			
14.0	6.30	1012.1	1008.0	1004.6	5.09	5.59	5.49	-52.8	-45.2	-38.8			
15.0	5.70	1017.8	1013.7	1010.3	4.83	5.18	5.16	-51.2	-45.4	-37.4			

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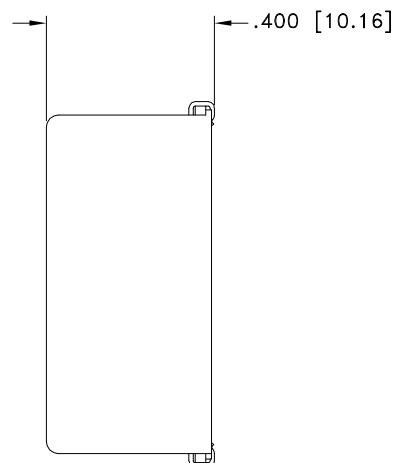
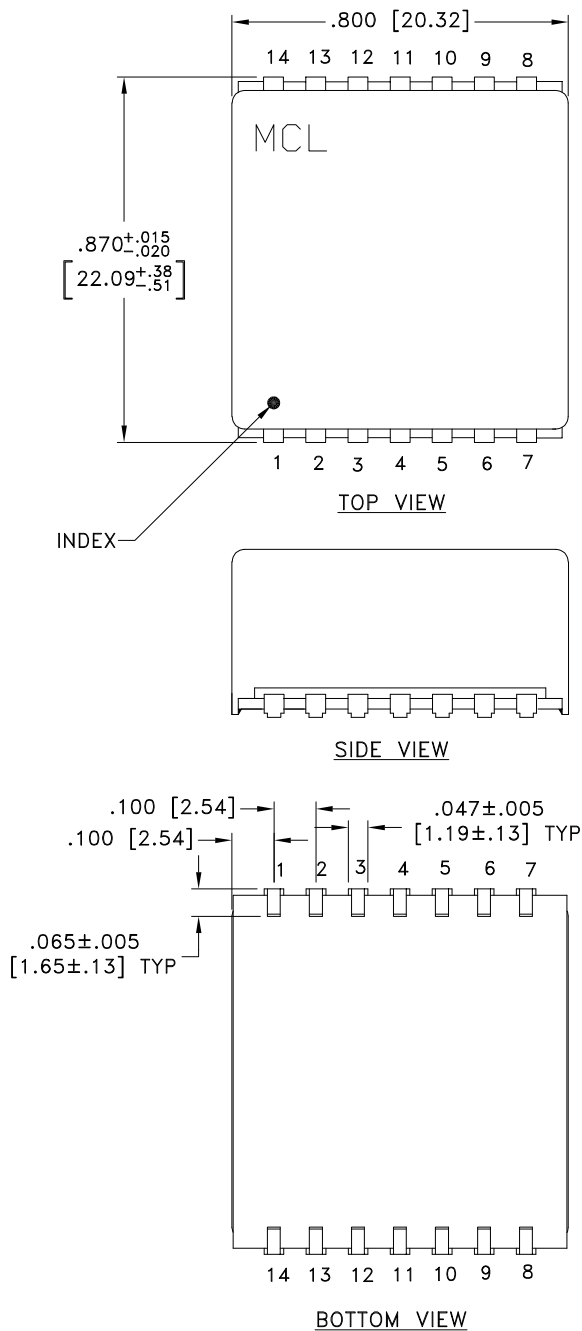
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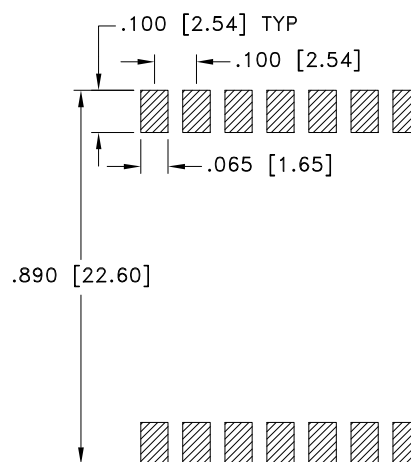


Outline Dimensions

BG419



SIDE VIEW



SUGGESTED LAYOUT
FOR PCB LAND PATTERN
(TOL $\pm$.002)

DENOTES METALLIZATION

Weight: 4 gram

Dimensions are in inches[mm]. Tolerances: 2PL $\pm$ 0.03[0.76]; 3 PL $\pm$ 0.015 [0.381] inches[mm], unless otherwise specified

Notes:

1. Case material: Copper-Nickel alloy.
2. Base material: Printed wiring laminate.
3. Termination finish:

For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.

For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.

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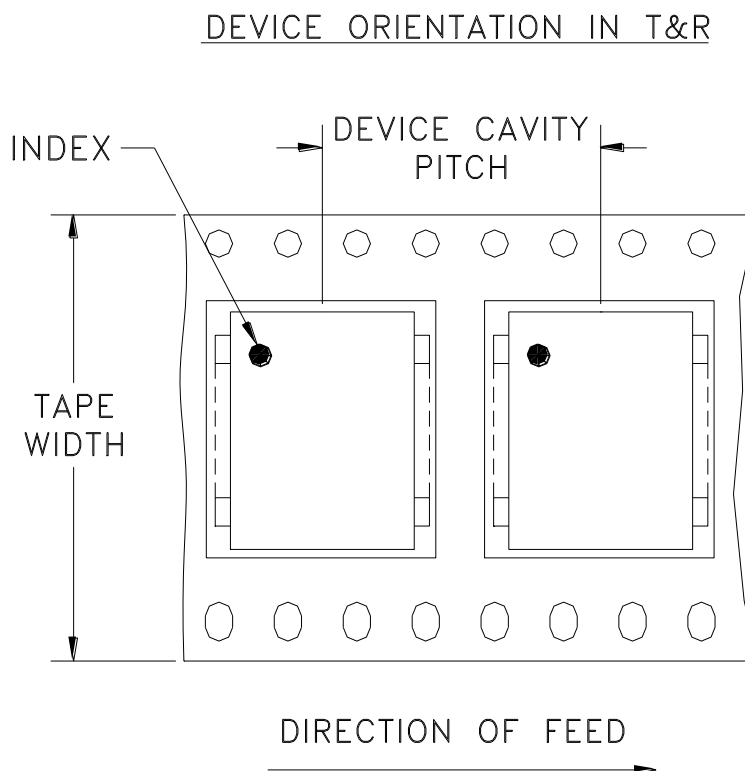
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RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F25



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
44	32	13	Small quantity standards (see note)	20
				50
				100
			Standard	125

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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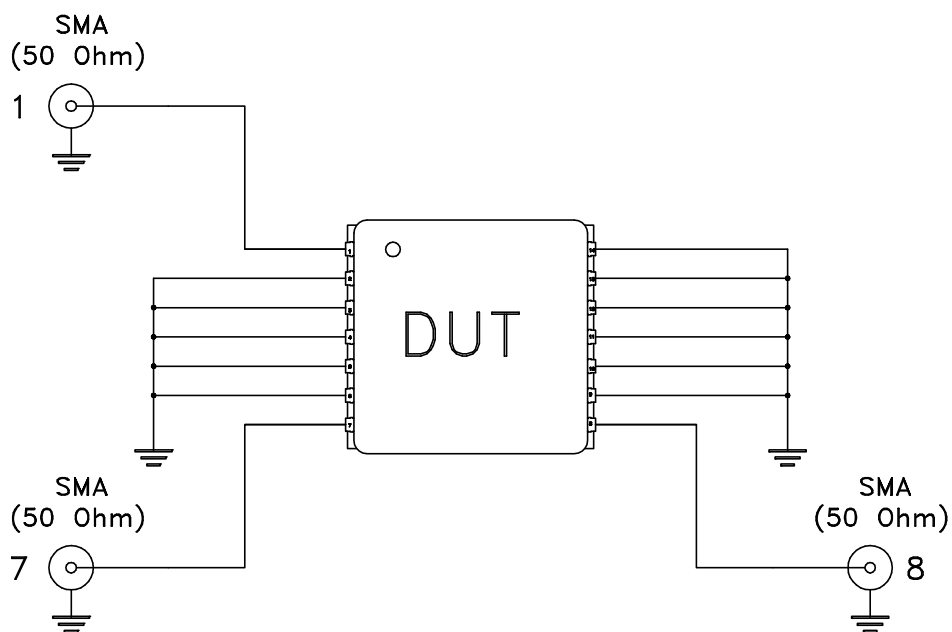
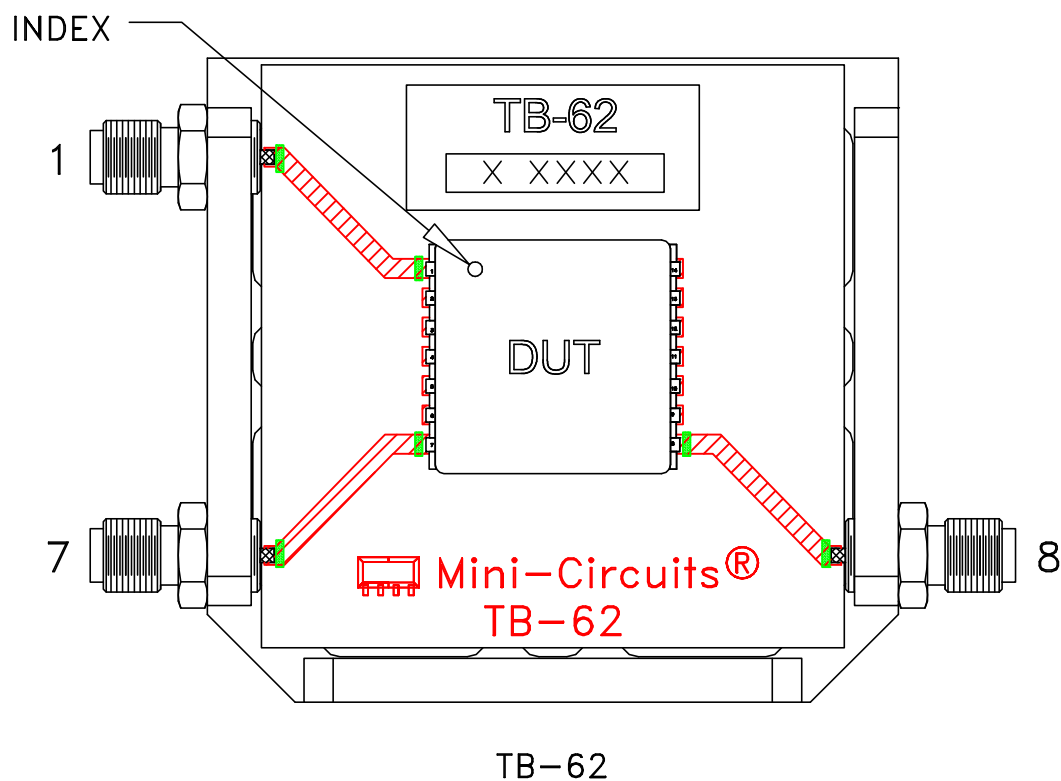
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Evaluation Board and Circuit

For Pin Connection Refer to
Data Sheet of the DUT



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent,
Dielectric Constant=3.5, Thickness=.030 inch.

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All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
HAST	130°C, 85% RH, 96 hours	JESD22-A110
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215