Engineering Development Model

Directional Coupler JDC-ED11944/2

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : BH292

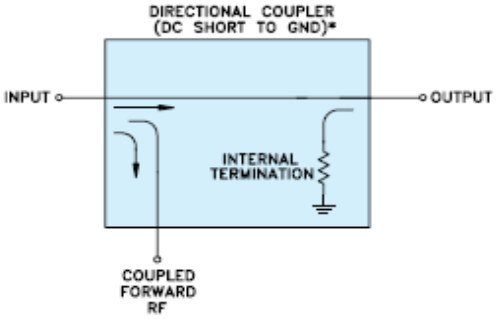
ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		1400		2800	MHz
Coupling	**Nominal**		18.43±1.8		dB
	Flatness		±1.98		dB
Mainline Loss **	**1400-2800MHz**		0.45		dB
Directivity	**1400-2800MHz**		19		dB
VSWR	**1400-2800MHz**		1.32		(:1)
RF Power Input	**1400-2800MHz**			0.5	W

Note: ** Mainline loss includes theoretical coupled power loss of 0.0628 dB at 18.43 dB coupling.

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
INPUT	1
OUTPUT	6
COUPLED FORWARD	3
GROUND	2, 5
NOT USED (ISOLATE)	4

Electrical Schematic



* ELECTRICAL SCHEMATIC IS FOR DIRECTIONAL COUPLER WITH INTERNAL TRANSFORMER(S) THAT ROUTES DC FROM RF PORTS TO GROUND.

Directional Coupler JDC-ED11944/2

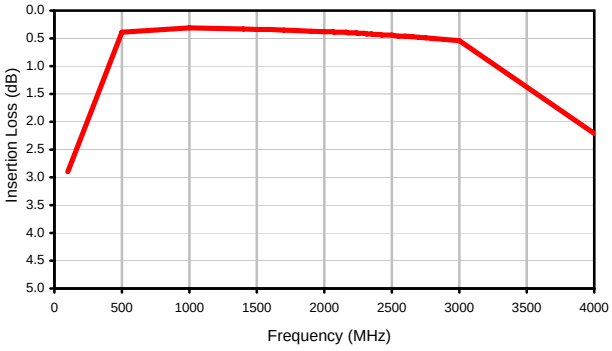
Typical Performance Data

FREQUENCY (MHz)	INSERTION LOSS (dB)	COUPLING (dB)	DIRECTIVITY (dB)	RETURN LOSS (dB)		
				IN	OUT	CPL
100.0	2.90	25.95	15.25	3.57	3.49	2.97
500.0	0.39	23.78	16.28	12.62	12.63	13.11
1000.0	0.31	22.19	18.22	14.77	14.73	19.12
1400.0	0.33	20.83	19.15	15.33	15.20	23.63
1500.0	0.34	20.53	19.31	15.59	15.40	24.50
1600.0	0.34	20.19	19.37	15.80	15.58	24.91
1700.0	0.35	19.88	19.43	15.99	15.73	24.97
1800.0	0.36	19.59	19.45	16.11	15.80	24.50
1900.0	0.37	19.32	19.42	16.18	15.81	23.70
2000.0	0.38	19.02	19.44	16.22	15.76	22.78
2025.0	0.38	18.95	19.45	16.34	15.94	22.51
2037.5	0.38	18.92	19.43	16.37	15.98	22.40
2050.0	0.38	18.89	19.42	16.36	15.97	22.28
2062.5	0.38	18.85	19.43	16.30	15.90	22.17
2075.0	0.39	18.82	19.43	16.26	15.84	22.06
2087.5	0.39	18.79	19.45	16.23	15.79	21.95
2100.0	0.39	18.76	19.44	16.23	15.77	21.82
2112.5	0.39	18.73	19.42	16.25	15.81	21.68
2125.0	0.39	18.70	19.42	16.31	15.86	21.54
2150.0	0.39	18.64	19.34	16.37	15.94	21.25
2162.5	0.39	18.60	19.31	16.35	15.91	21.12
2175.0	0.40	18.57	19.32	16.31	15.84	21.00
2200.0	0.40	18.50	19.26	16.22	15.70	20.79
2212.5	0.40	18.46	19.24	16.21	15.68	20.67
2225.0	0.40	18.43	19.20	16.25	15.73	20.55
2237.5	0.40	18.40	19.16	16.31	15.82	20.42
2250.0	0.41	18.37	19.18	16.35	15.89	20.28
2262.5	0.41	18.34	19.17	16.37	15.91	20.13
2275.0	0.41	18.31	19.17	16.37	15.91	19.98
2300.0	0.41	18.24	19.17	16.28	15.76	19.71
2312.5	0.42	18.20	19.18	16.23	15.69	19.59
2325.0	0.42	18.17	19.17	16.21	15.68	19.47
2337.5	0.42	18.13	19.17	16.24	15.70	19.35
2350.0	0.42	18.10	19.20	16.27	15.74	19.24
2362.5	0.43	18.07	19.23	16.31	15.80	19.12
2375.0	0.43	18.04	19.21	16.33	15.83	18.98
2400.0	0.43	17.97	19.18	16.25	15.72	18.70
2412.5	0.43	17.93	19.17	16.16	15.62	18.56
2425.0	0.44	17.90	19.15	16.12	15.56	18.42
2437.5	0.44	17.86	19.14	16.10	15.54	18.30
2450.0	0.44	17.83	19.11	16.11	15.56	18.19
2462.5	0.44	17.79	19.10	16.17	15.62	18.08
2475.0	0.44	17.76	19.07	16.22	15.70	17.96
2500.0	0.44	17.69	19.00	16.19	15.69	17.71
2550.0	0.46	17.56	18.82	16.01	15.37	17.21
2600.0	0.46	17.43	18.67	16.16	15.60	16.74
2650.0	0.47	17.30	18.63	15.94	15.29	16.22
2700.0	0.48	17.16	18.49	16.02	15.42	15.73
2750.0	0.49	17.01	18.36	15.93	15.31	15.23
2800.0	0.50	16.87	18.20	15.94	15.27	14.71
3000.0	0.54	16.30	17.56	16.03	15.21	12.78
4000.0	2.21	7.66	6.36	15.71	19.83	10.80

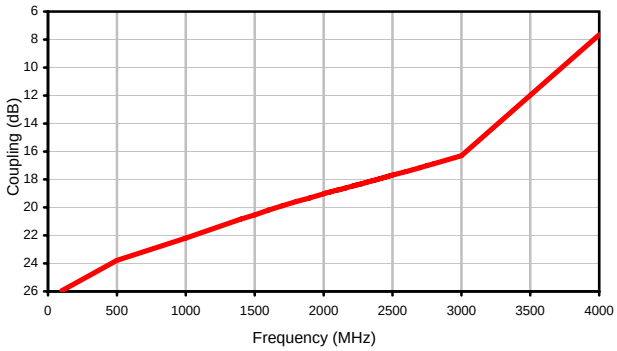
Directional Coupler JDC-ED11944/2

Typical Performance Curves

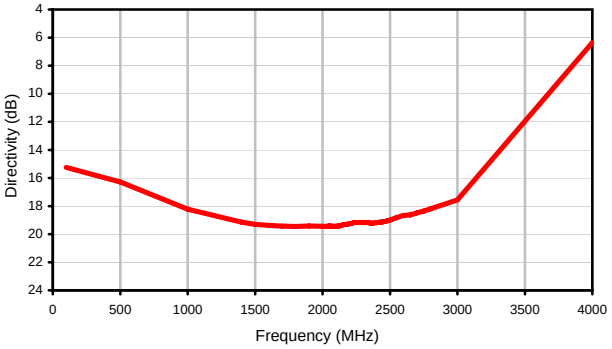
Insertion Loss



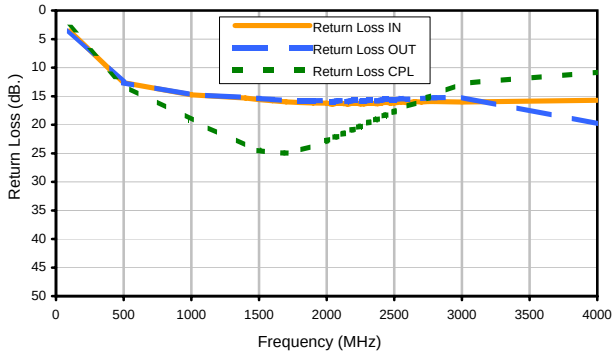
Coupling



Directivity



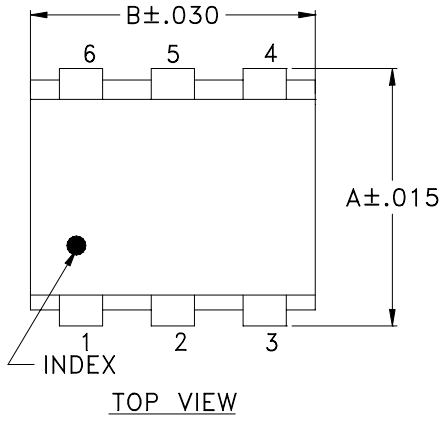
Return Loss



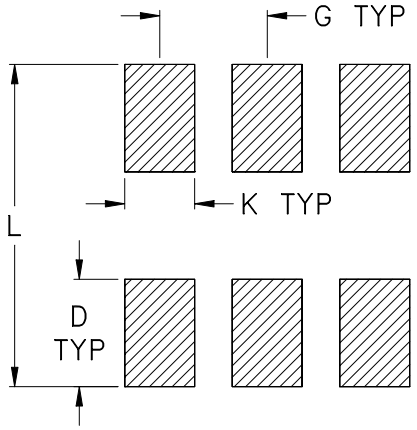
Case Style BH

BH292

Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE #	A	B	C	D	E	F	G	H	J	K	L	WT. GRAM
BH292	.280 (7.11)	.310 (7.87)	- -	.100 (2.54)	.225 (5.72)	.055 (1.40)	.100 (2.54	.047 (1.19)	.065 (1.65)	.065 (1.65)	.300 (7.62)	.45

Dimensions are in inches (mm). Tolerances: 2Pl. ± .01; 3Pl. ± .005

Notes:
1. Case material: Ceramic.
2. Termination finish:
   For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
   For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.

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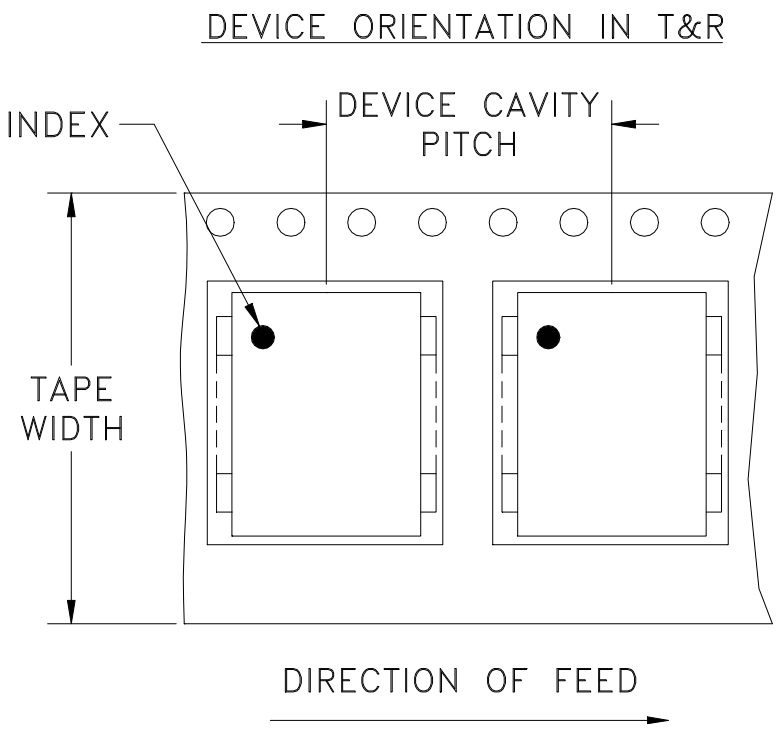

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel
16	12	13	500

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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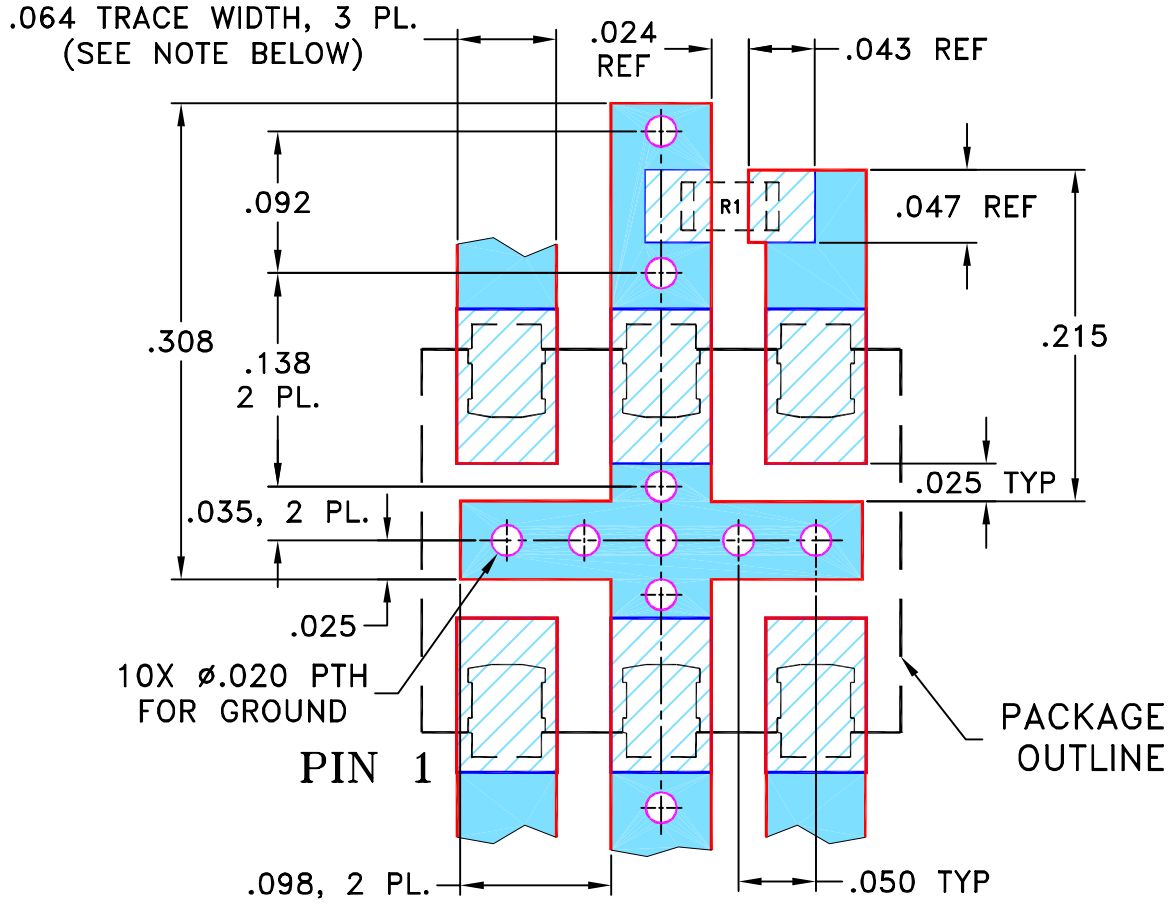
THIRD ANGLE PROJECTION

REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M81002	NEW RELEASE	08/05/02	MMG	LC
A	M102713	ADDED NOTE 2 & "...WITH SMOBC"	01/17/06	MMG	IL

SUGGESTED MOUNTING CONFIGURATION
FOR BH292/CD542 CASE STYLE, "lt" PIN CONNECTION



RESISTOR R1: 49.9 Ohm, 0603 SIZE.

NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .030" ± .002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	MMG	08/05/02
TOLERANCES ON:	CHECKED	AV	08/05/02
2 PL DECIMALS ±	APPROVED	LC	08/05/02
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

ASHEETA1.DWG REV:A DATE:01/12/95

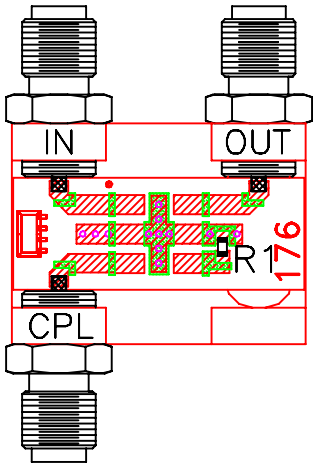
Mini-Circuits®
13 Neptune Avenue
Brooklyn NY 11235

PL, lt, BH292/CD542, JDC/ADC, TB-176

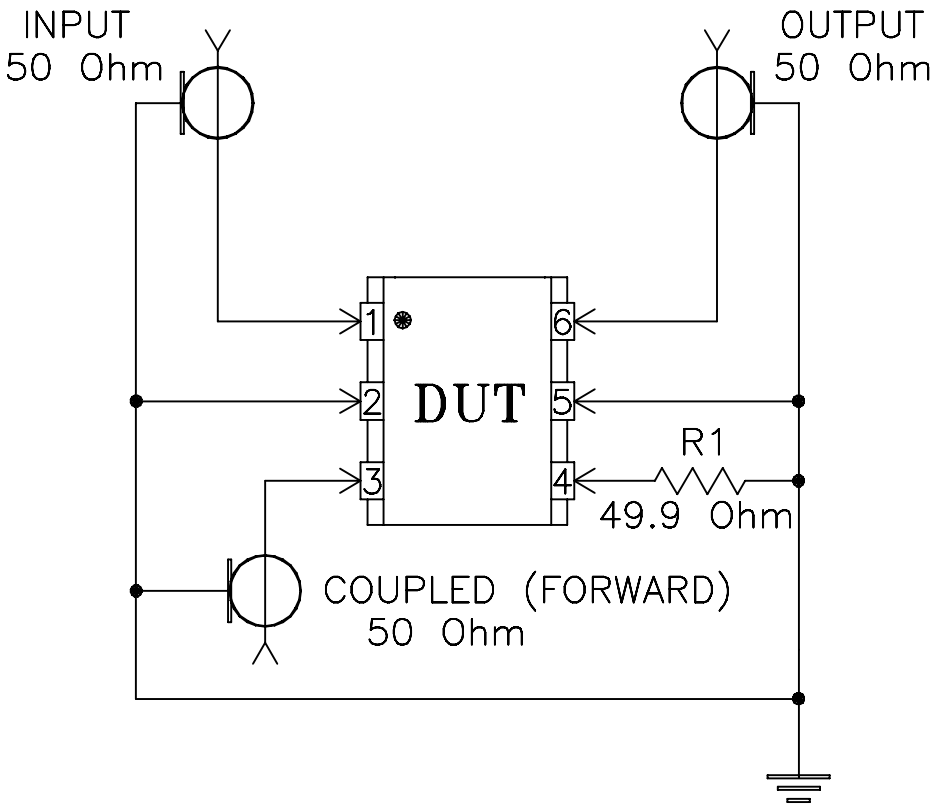
SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-041	A

FILE:	SCALE:	SHEET:
98PL041	8:1	1 OF 1

Evaluation Board and Circuit



TB-176



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.

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Environmental Specifications ENV02T1

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215