

Engineering Development Model

Voltage Controlled Oscillator

JTOS-ED6530/2

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.

At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : BK377

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	1714		1864	MHz
Tuning Voltage	0.5		5	V
Power Output		+5		dBm
Phase Noise				
at 1 kHz offset		-65		dBc/Hz
at 10 KHz offset		-95		dBc/Hz
at 100 KHz offset		-117		dBc/Hz
at 1000 kHz offset		-134		dBc/Hz
Pulling at 12 dBm PK-PK all phases		13		MHz
Pushing at Vcc=5V±0.25V		8		MHz/V
Tuning Sensitivity		30 - 36		MHz/V
Harmonic Suppression		-20		dBc
3 dB Modulation Bandwidth		2000		kHz
Supply Voltage		8		V
Supply Current			25	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+10V
Absolute Tuning Voltage (Vtune)	+7V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	5
GROUND	1,3,4,6,7,8,9,10,11,12,14

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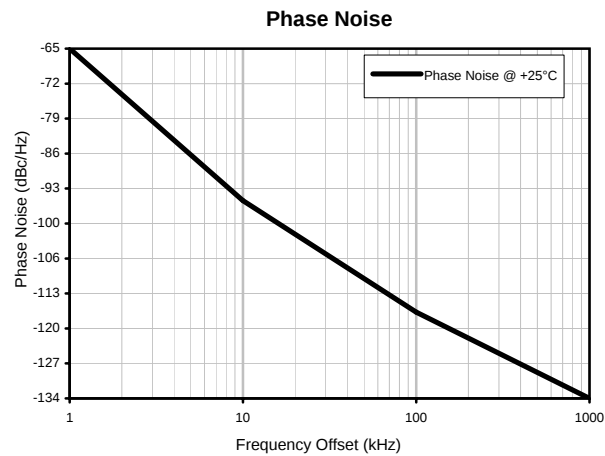
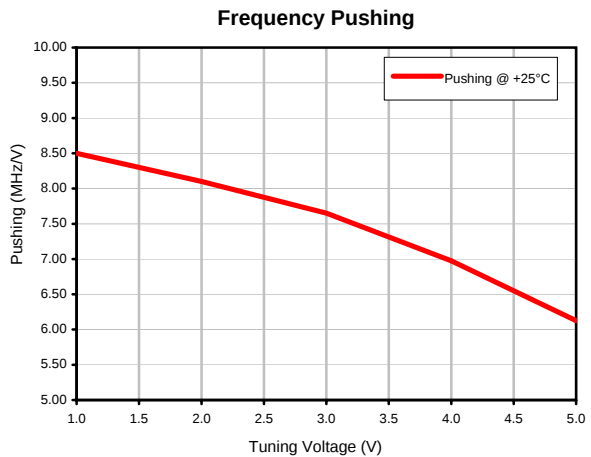
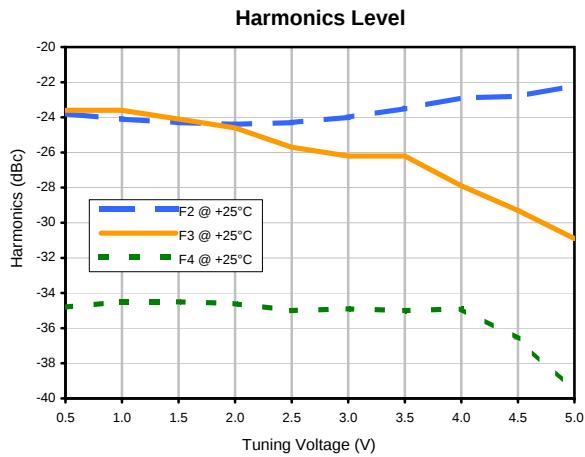
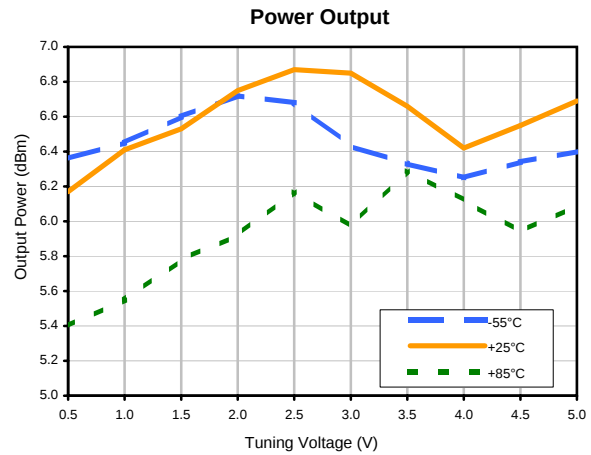
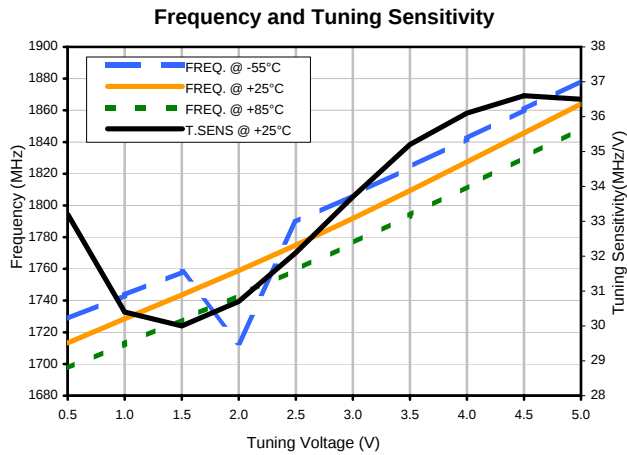
Typical Performance Data

V TUNE	TUNE SENS (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)			HARMONICS (dBc)			FREQ. PUSH (MHz/V)	FREQ OFFSET (KHz)	PHASE NOISE (dBc/Hz)
		-55°C	+25°C	+85°C	-55°C	+25°C	+85°C	F2	F3	F4			
0.5	33.20	1728.5	1713.4	1697.4	6.36	6.17	5.40	-23.8	-23.6	-34.8	8.50	1	-65
1.0	30.40	1743.3	1728.6	1712.7	6.45	6.41	5.55	-24.1	-23.6	-34.5		10	-95
1.5	30.00	1758.1	1743.6	1727.9	6.60	6.53	5.78	-24.3	-24.1	-34.5		100	-117
2.0	30.70	1713.5	1758.9	1743.3	6.72	6.75	5.92	-24.4	-24.6	-34.6	8.10	1000	-134
2.5	32.10	1789.5	1775.0	1759.4	6.68	6.87	6.17	-24.3	-25.7	-35.0			
3.0	33.70	1806.4	1791.8	1776.2	6.43	6.85	5.97	-24.0	-26.2	-34.9			
3.5	35.20	1824.1	1809.4	1793.7	6.33	6.66	6.29	-23.5	-26.2	-35.0	7.65		
4.0	36.10	1842.1	1827.4	1811.9	6.25	6.42	6.12	-22.9	-27.9	-34.9			
4.5	36.60	1860.5	1845.7	1830.2	6.34	6.55	5.94	-22.8	-29.3	-36.6			
5.0	36.50	1878.7	1864.0	1848.6	6.40	6.69	6.09	-22.2	-30.9	-39.6	6.13		

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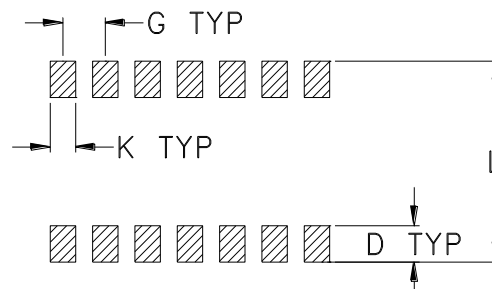
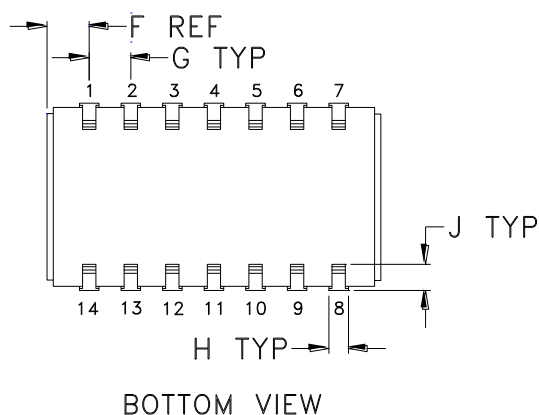
Typical Performance Data



Outline Dimensions



PCB Land Pattern



CASE #	A	B	C	D	E	F	G	H	J	K	L	WT. GRAM
BK377	.505 (12.83)	.800 (20.32)	-- --	.100 (2.54)	.250 (6.35)	.100 (2.54)	.100 (2.54)	.047 (1.19)	.065 (1.65)	.065 (1.65)	.525 (13.34)	2.0 MAX.

Dimensions are in inches (mm). Tolerances: 2Pl. $\pm .01$; 3Pl. $\pm .005$

Notes:

- Case material: Copper Nickel alloy.
- Base material: Printed wiring laminate.
- Termination finish:
 - For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
 - For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



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Tape & Reel Packaging TR-F107

DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
32	20	7	Small quantity standards (see note)	10
				20
				50
				100
		13	Standard	200

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



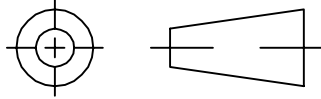
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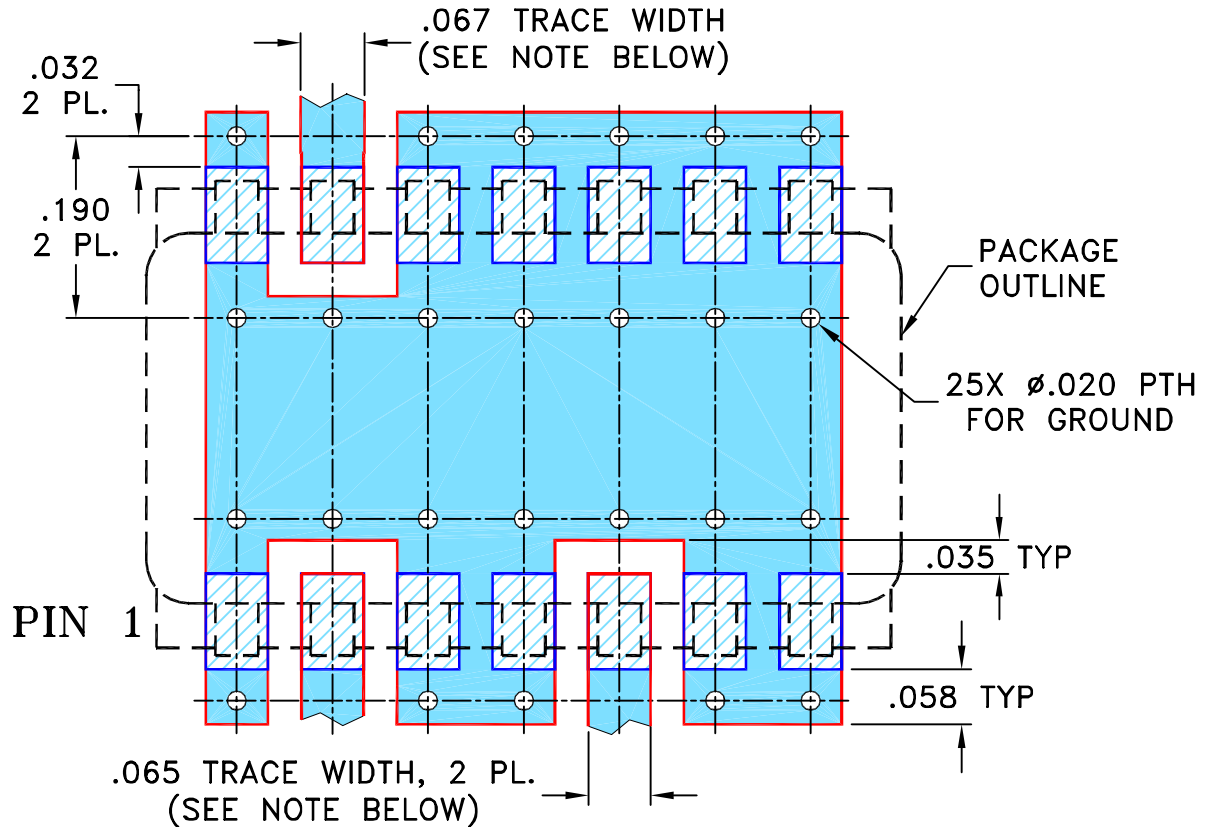
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
B	M76077	UPDATED DRAWING	04/01	GF	MM
C	M82575	UPDATED DRAWING	08/08/02	IL	MM
D	M102713	UPDATED DIMENSIONS & NOTES	01/17/06	MMG	IL
E	M115059	CORRECTED NOTE 2	12/18/07	MMG	IL

SUGGESTED MOUNTING CONFIGURATION FOR BK377 CASE STYLE, "jc" PIN CONNECTION



- NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .030" $\pm$.002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC
(SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005ANGLES $\pm$ FRACTIONS $\pm$

DRAWN

FB

05/20/00

CHECKED

MM

05/24/00

APPROVED

DB

05/24/00



Mini-Circuits® 13 Neptune Avenue
Brooklyn NY 11235

PL, jc, BK377, JTOS, TB-04

SIZE
A

CODE IDENT
15542

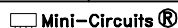
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REV:
E

FILE: 98PL005

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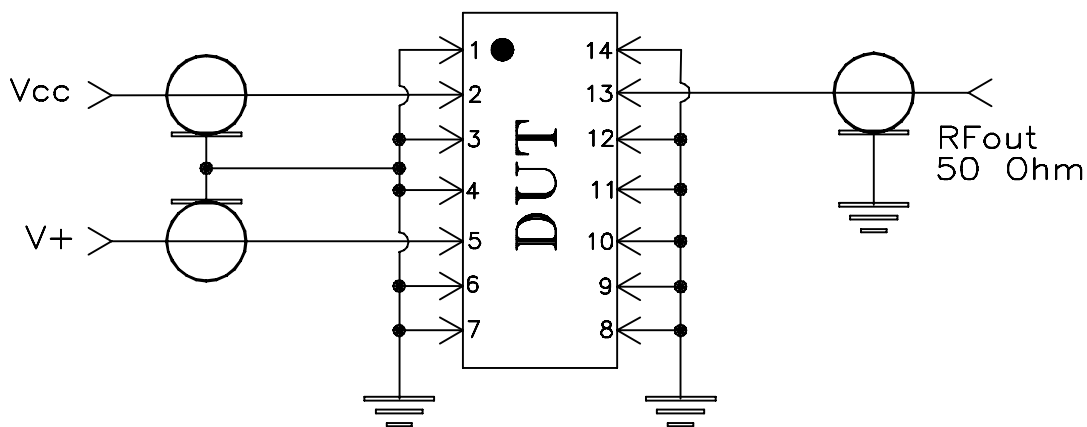
SHEET: 1 OF 1



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ASHEETA1.DWG REV:A DATE:01/12/95

Evaluation Board and Circuit



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.

 Mini-Circuits®



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
HAST	130°C, 85% RH, 96 hours	JESD22-A110
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215