

Engineering Development Model

Frequency Synthesizer

KSN-EDR10139

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE : DK1042

ELECTRICAL SPECIFICATIONS 50Ω, over -45°C to +85°C				
Parameter	Min.	Typ.	Max.	Units
Frequency	1090		1090	MHz
Step size		10000		kHz
Settling Time Within ±1kHz		25		msec
Output Power	+4	+7	+11	dBm
Phase Noise				
at 100 Hz offset		-82		dBc/Hz
at 1 kHz offset		-99	-94	dBc/Hz
at 10 KHz offset		-104	-100	dBc/Hz
at 100 KHz offset		-99	-120	dBc/Hz
at 1000 kHz offset		-156	-151	dBc/Hz
Integrated SSB Phase Noise		-57		dBc
Reference Spurious Suppression		-98		dBc
Comparison Spurious Suppression		-98		dBc
Non-Harm. Spurious Suppression		-90		dBc
Harmonic Suppression		-29	-23	dBc
Supply voltage VCO		5		V
Supply voltage PLL		5		V
Supply current VCO		56	64	V
Supply current PLL		17	25	V
Reference In (Internal)		20		MHz
Frequency		1		Vp-p
Amplitude		100		kΩ

