Engineering Development Model

Frequency Synthesizer KSN-EDR10549

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : DK1171

Not recommended for new designs. Contact applications and/or your account manager for an alternative.

ELECTRICAL SPECIFICATIONS 50Ω, over -5°C to +75°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		2304		2304	MHz
Step size			10240		kHz
Settling Time	**Within ±1kHz**		35		msec
Output Power		-4	0	+3	dBm
Phase Noise					
	at 100 Hz offset		-88		dBc/Hz
	at 1 kHz offset		-89	-83	dBc/Hz
	at 10 KHz offset		-113	-107	dBc/Hz
	at 100 KHz offset		-135	-130	dBc/Hz
	at 1000 kHz offset		-156	-150	dBc/Hz
Integrated SSB Phase Noise			-55		dBc
Ref & Comp Spurious Suppression			-100		dBc
Non-Harm. Spurious Suppression			-90		dBc
Harmonic Suppression			-33	-25	dBc
Supply voltage	**VCO**		5		V
	PLL		3.3		V
Supply current	**VCO**		43	52	V
	PLL		11	19	V
Reference in (External)	**Frequency**		10.24		MHz
	Amplitude		1		Vp-p
	Impedance		100		kΩ
	Ph. N @ 1kHz		-145		dBc/Hz
Digital Lock Detect	**Locked**	2.9		3.3	V
	Unlocked			0.4	
Frequency Synthesizer PLL			Self-programmed (internal microcontroller)		

ABSOLUTE MAXIMUM RATINGS	
Operating Temperature	-45°C to 85°C
Storage Temperature	-55°C to 100°C
VCO Supply Voltage	6V
PLL Supply Voltage	4.3V
Reference Frequency voltage	3.6Vp-p
Data, Clock & LE levels	3.6V

Power On sequence: Vcc VCO followed by Vcc PLL
Power Off sequence: Vcc PLL followed by Vcc VCO

PIN CONNECTIONS			
RF OUT	7	**N.C**	10,11,12
VCC VCO	5	**GROUND**	2,4,6,8,13,14
VCC PLL	1		
REF IN	3		
LOCK DETECT	9		

Notes
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B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
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Frequency Synthesizer KSN-EDR10549

Typical Performance Data

FREQ. (MHz)	POWER OUTPUT (dBm)			HARMONICS (dBc)						VCO CURRENT (mA)			PLL CURRENT (mA)		
				F2			F3								
	-5ºC	+25ºC	+75ºC	-5ºC	+25ºC	+75ºC	-5ºC	+25ºC	+75ºC	-5ºC	+25ºC	+75ºC	-5ºC	+25ºC	+75ºC
2304	-0.36	-0.40	-0.30	-30.48	-31.63	-34.57	-35.04	-33.93	-32.35	42.86	43.43	44.30	10.06	10.77	11.88

FREQ. (MHz)	PHASE NOISE (dBc/Hz) @ OFFSETS														
	-5ºC					+25ºC					+75ºC				
	100Hz	1kHz	10kHz	100kHz	1MHz	100Hz	1kHz	10kHz	100kHz	1MHz	100Hz	1kHz	10kHz	100kHz	1MHz
2304	-87.84	-87.79	-113.34	-136.39	-157.23	-87.11	-90.10	-113.15	-135.69	-156.71	-86.07	-86.85	-112.37	-134.40	-154.69

REFERENCE & COMPARISON SPURIOUS ORDER	REFERENCE & COMPARISON SPURIOUS @Fcarrier 2457.6MHz± (n*Fcomparison) (dBc) NOTE 1		
n	-5ºC	+25ºC	+75ºC
-5	-102.69	-94.89	-102.13
-4	-98.01	-95.4	-102.25
-3	-108.14	-94.79	-97.7
-2	-99.83	-94.75	-99.07
-1	-104.49	-91.09	-93.97
0 note 2	-	-	-
+1	-109.12	-100.19	-102.43
+2	-100.98	-100.6	-104.35
+3	-98.82	-96.68	-100.14
+4	-99.00	-98.39	-99.01
+5	-98.76	-97.38	-96.12

Note 1: Comparison frequency & Reference frequency 10.24 MHz
Note 2: All spurs are referenced to carrier signal (n=0).

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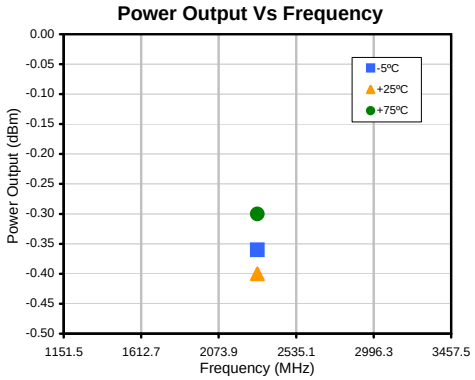
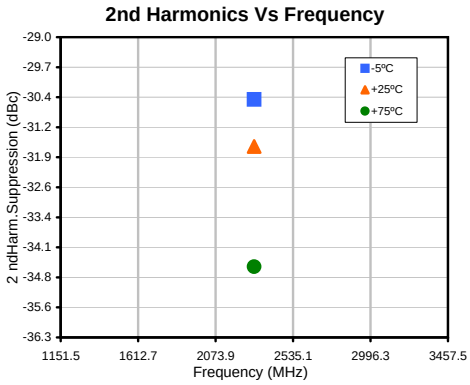
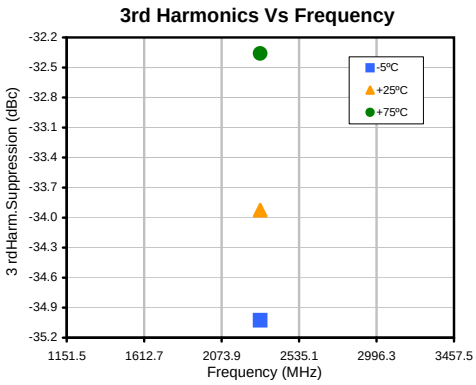
Frequency Synthesizer KSN-EDR10549

Typical Performance Data







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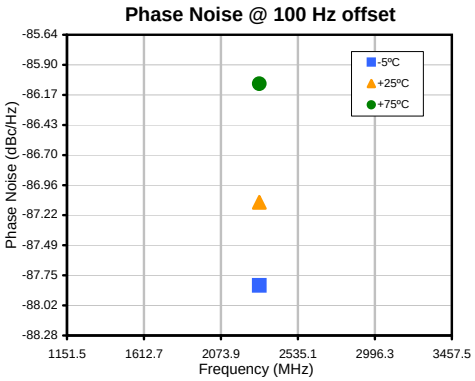

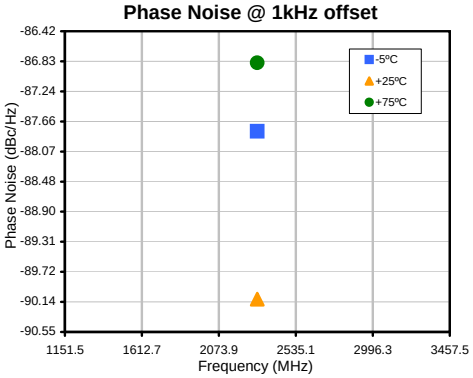

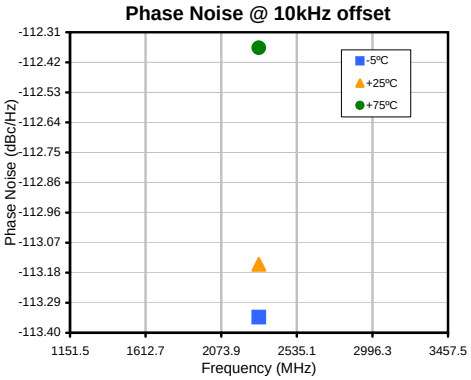

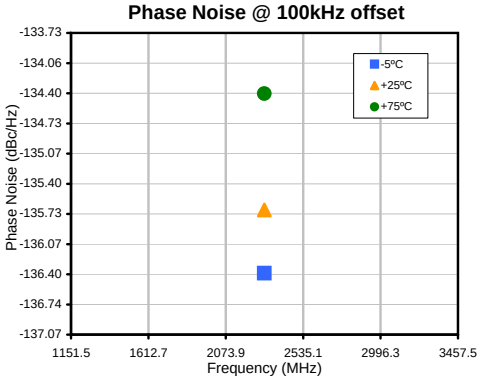

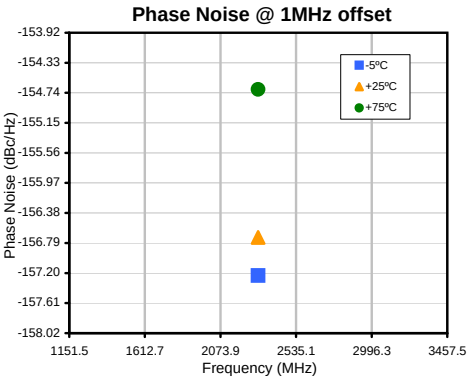

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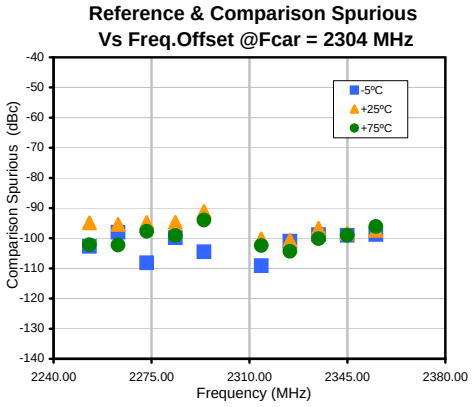

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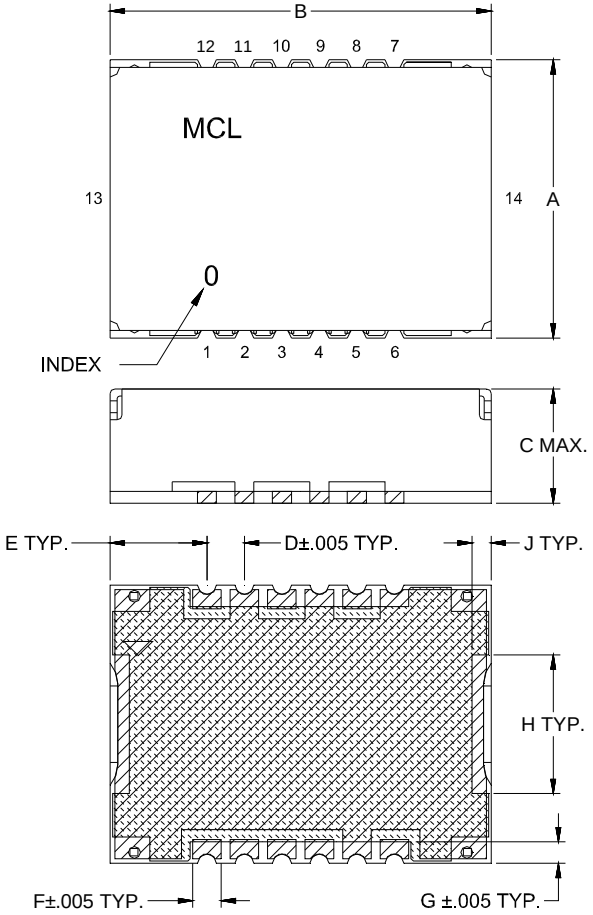
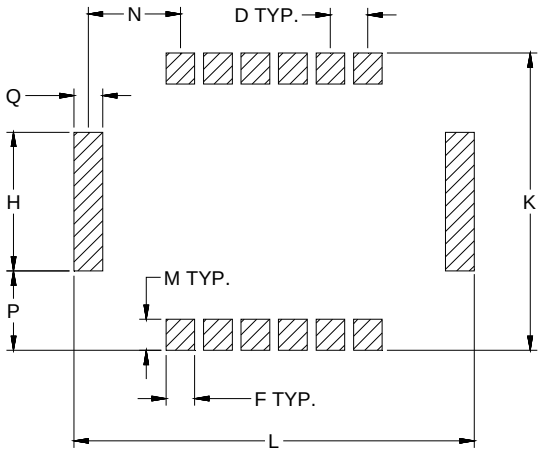
Case Style DK

Outline Dimensions

DK1171



Suggested PCB Land Pattern



METALLIZATION

SOLDER RESIST

CASE#	A	B	C	D	E	F	G	H	J	K	L	M	N	P	Q	WT. GRAM
DK1171	.584 (14.83)	.800 (20.32)	.240 (6.10)	.079 (2.01)	.203 (5.16)	.060 (1.52)	.045 (1.14)	.291 (7.39)	.039 (1.0)	.624 (15.85)	.840 (21.34)	.065 (1.65)	.193 (4.90)	.170 (4.32)	.060 (1.52)	2.5

Dimensions are in inches (mm). Tolerances: 2PL. +/- .03; 3PL. +/- .015

Notes:

1. Case material: Nickel-Silver alloy.
2. Base: Printed wiring laminate.
3. Termination finish:

   For RoHS Case Styles: 2-5 µ inch (.05-.13 microns) Gold over 120-240 µ inch (3.05-6.10 microns) Nickel plate. All models, (+) suffix.

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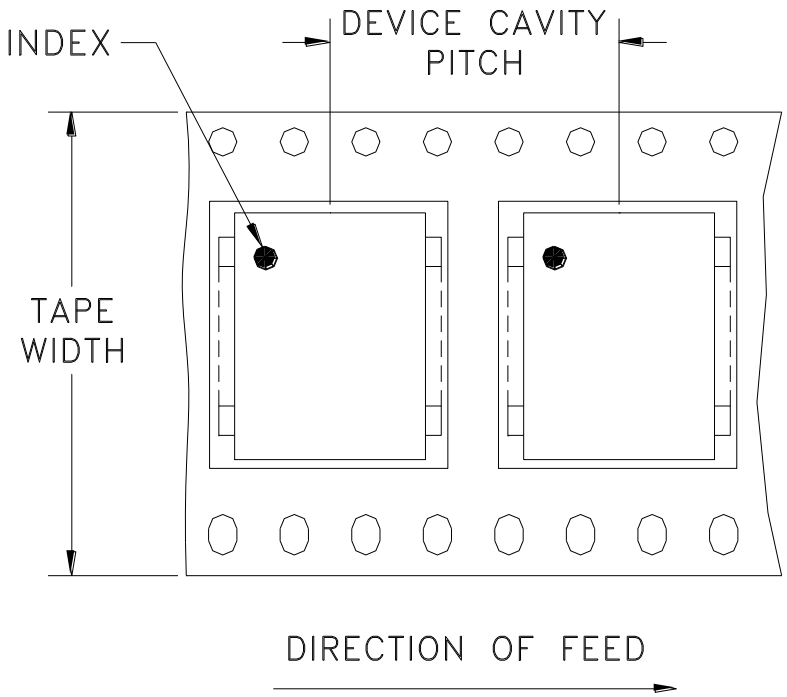
Tape & Reel Packaging TR-F28

DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
32	20	13	Small quantity standards (see note)	20
				50
				100
				200
			Standard	500

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

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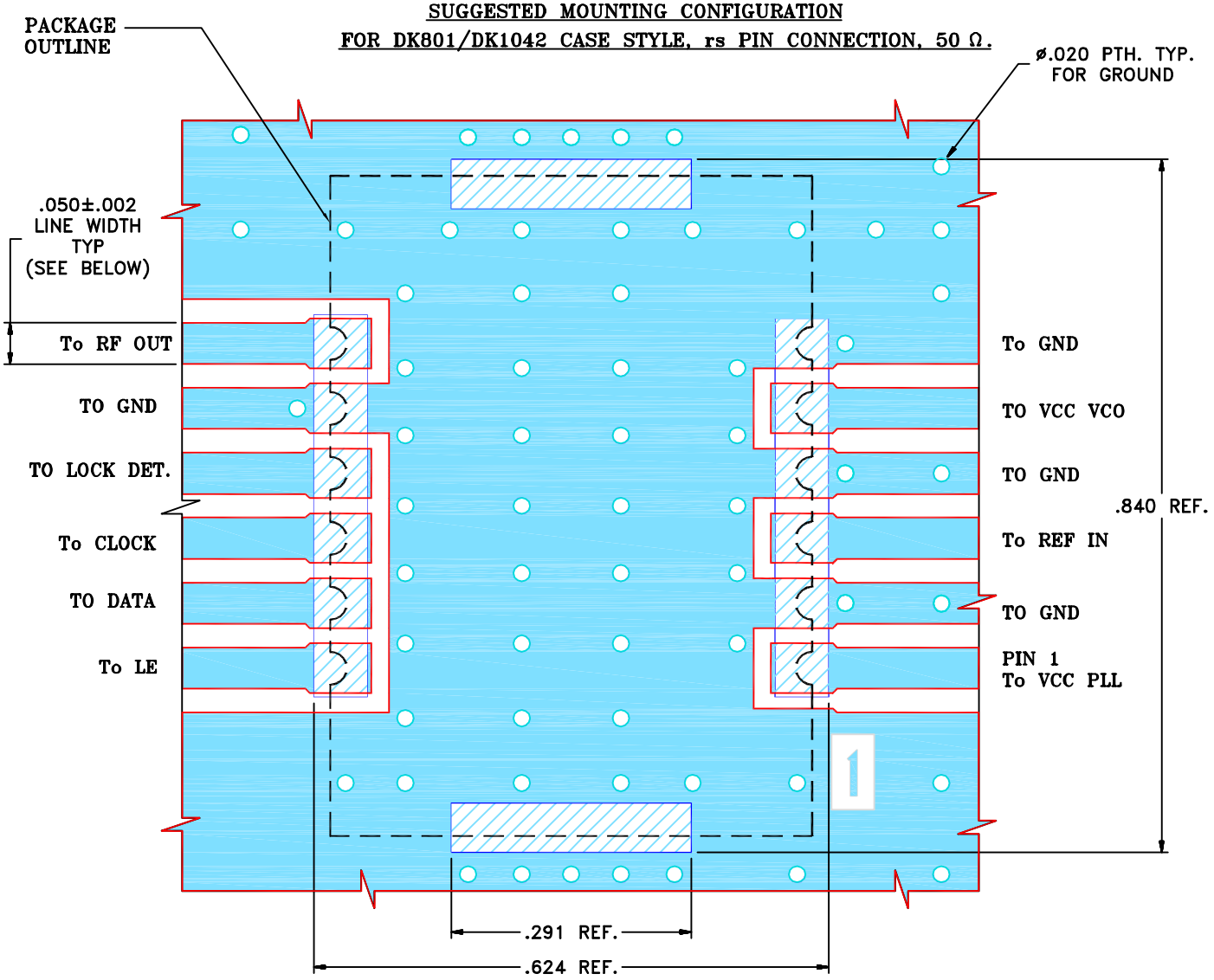
THIRD ANGLE PROJECTION

REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M108069	NEW RELEASE	11/06	DK	HH
A	M110535	MODIFY GROUND PADS	03/07	DK	HH
B	M126537	DELETE CHIP COMP. & TABLE OF VALUES	03/10	DK	HH
B	R78979	DELETE CHIP COMP. & TABLE OF VALUES	03/10	DK	HH

SUGGESTED MOUNTING CONFIGURATION
FOR DK801/DK1042 CASE STYLE, rs PIN CONNECTION, 50 Ω.



NOTES:

1. TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS. .025"±.002". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDERMASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	DK (RAVON)	27 MAR 07
TOLERANCES ON:	CHECKED	RZ (RAVON)	27 MAR 07
2 PL DECIMALS ±	APPROVED	HH (RAVON)	27 MAR 07
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

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ASHEETA1.DWG REV:A DATE:01/12/95

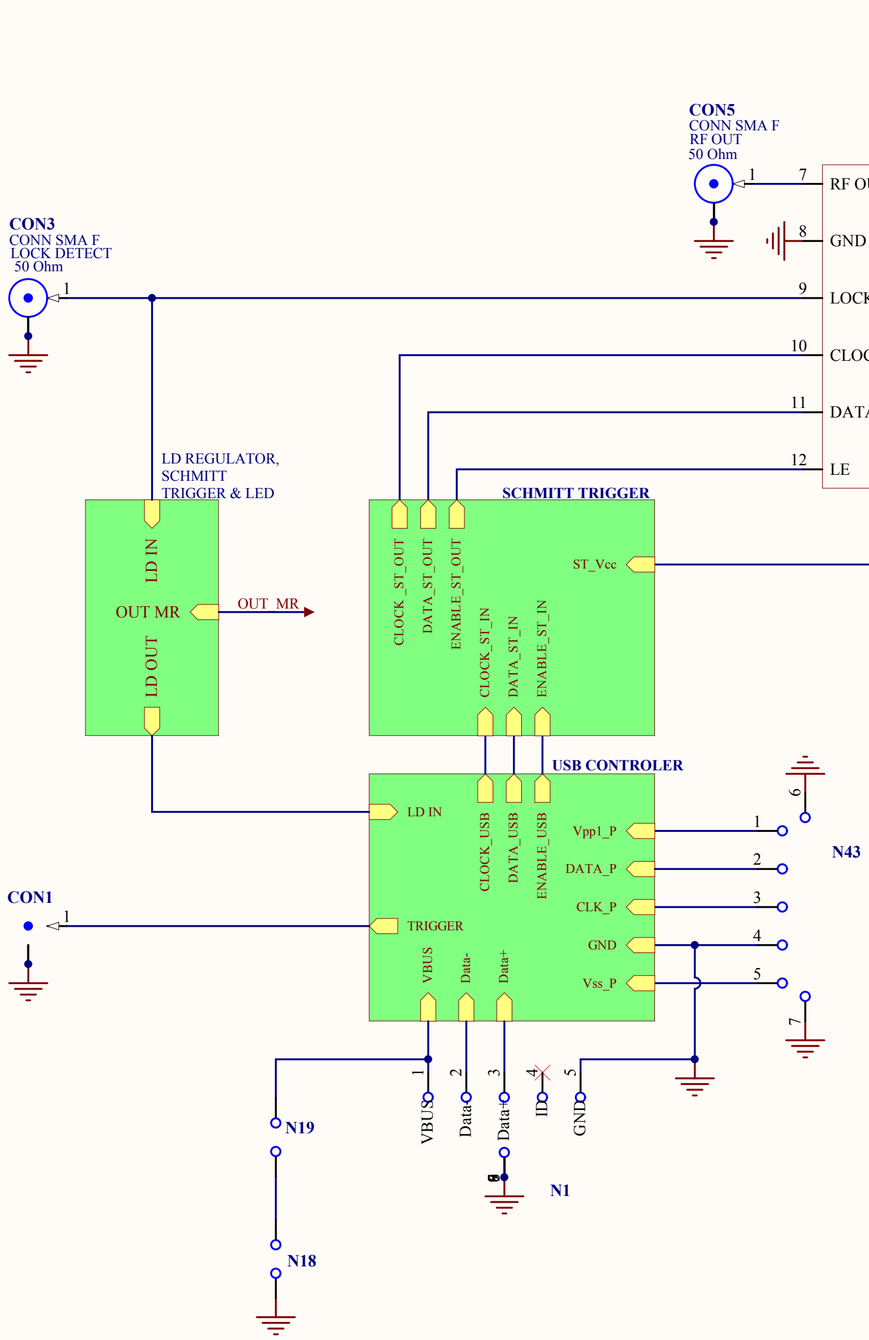
Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

PL, rs ,DK801/DK1042, KSN
TB-567+ (50 Ω)

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-249	B

FILE:	SCALE:	SHEET:
98PL249	5:1	1 OF 1

ELECTRICAL SCHEME-GENERAL
(TB for KSN)



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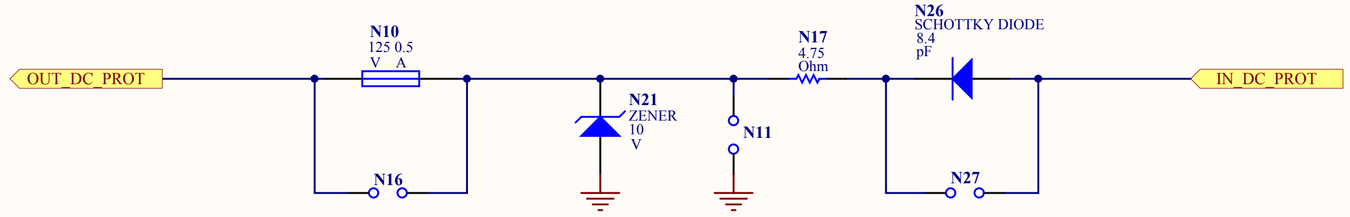
DC PROTECTIONS



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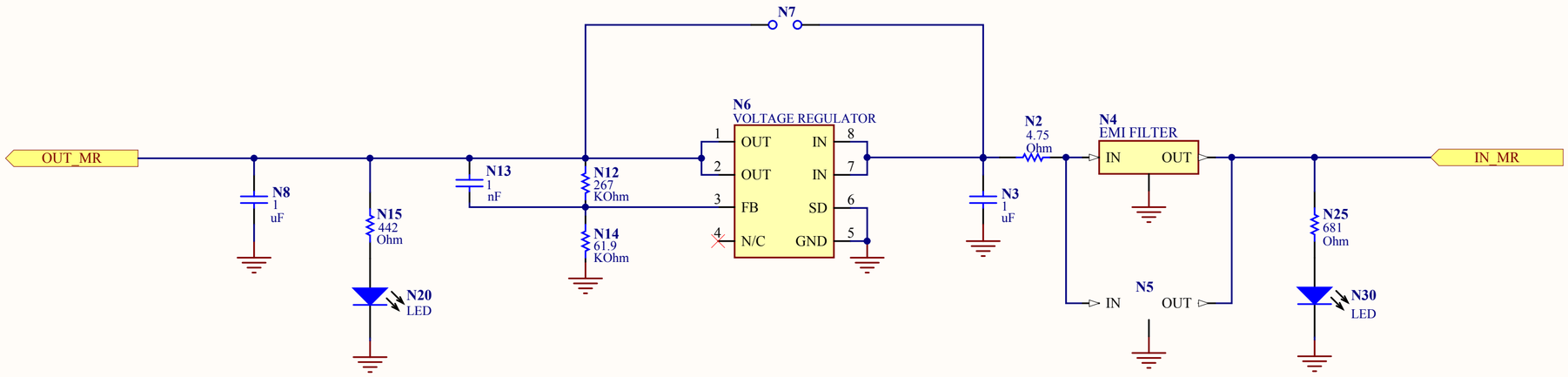
MAIN REGULATOR



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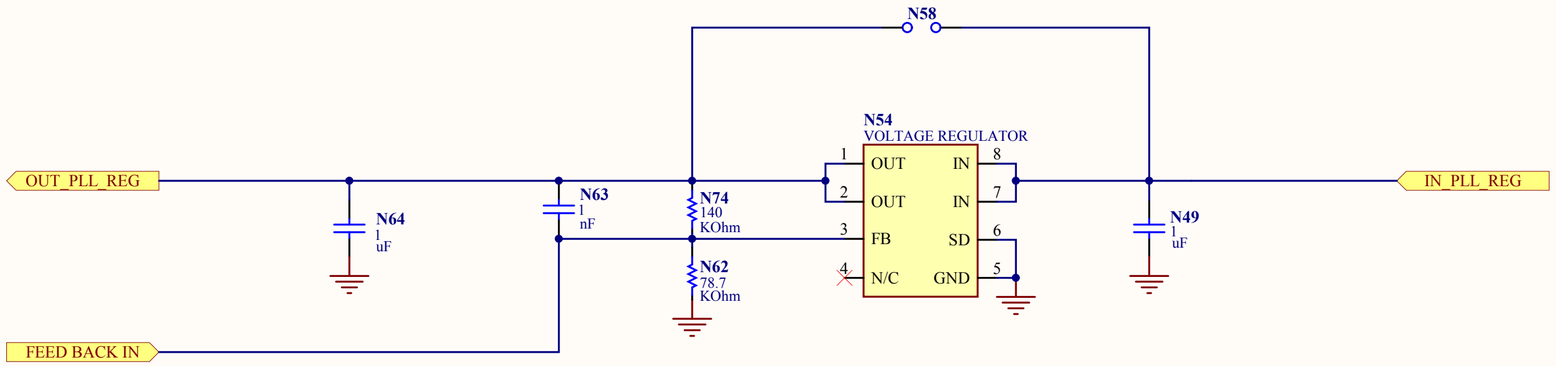
VCC PLL REGULATOR



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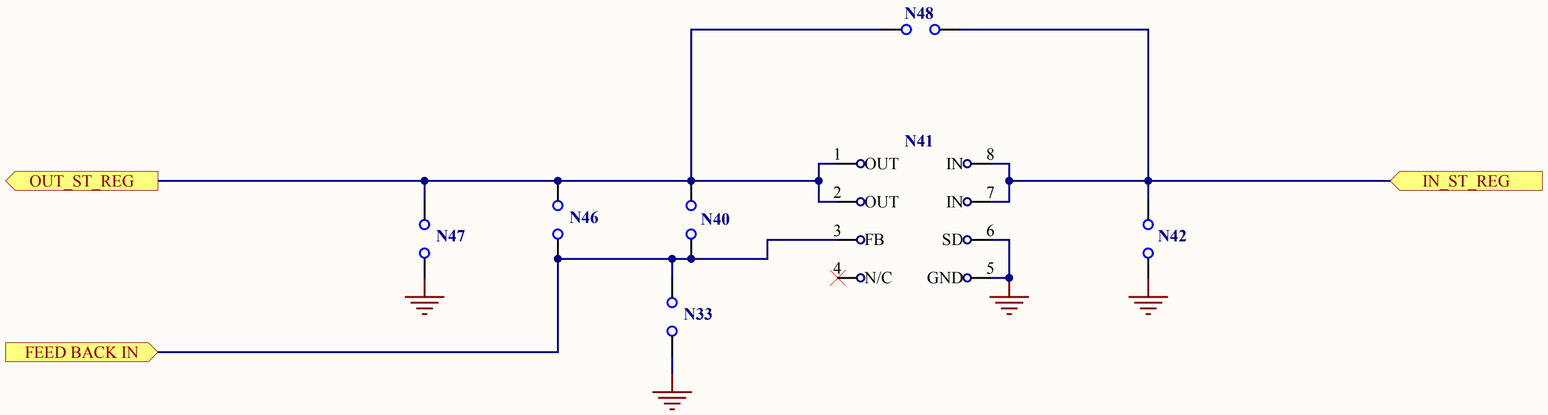
SCHMITT TRIGGER REGULATOR



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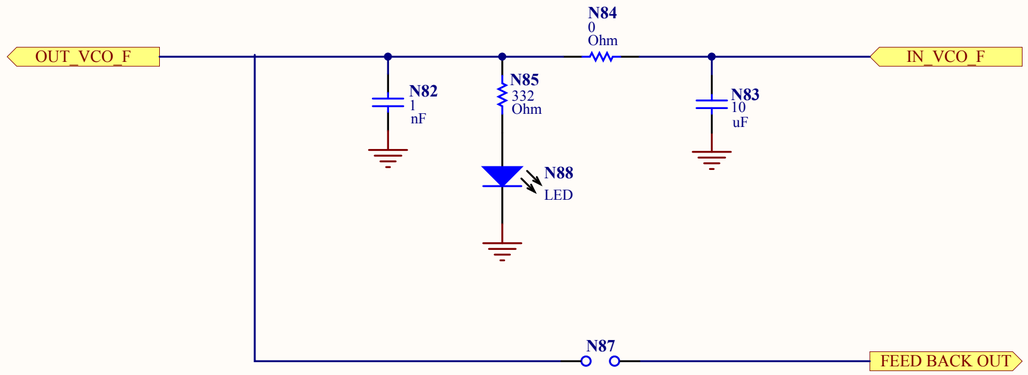
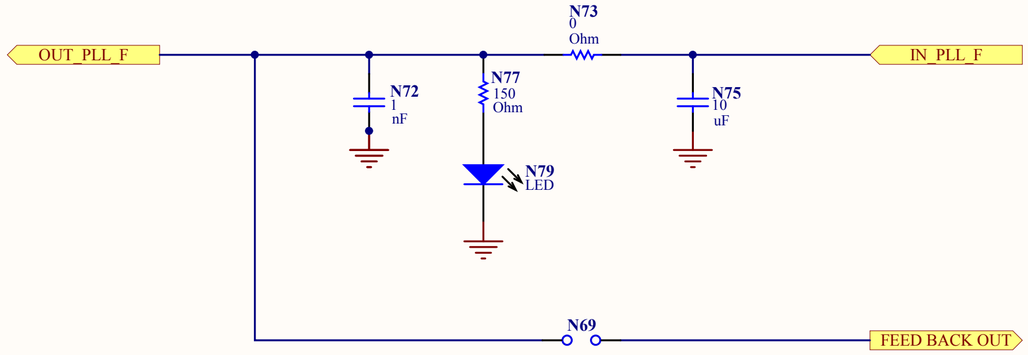
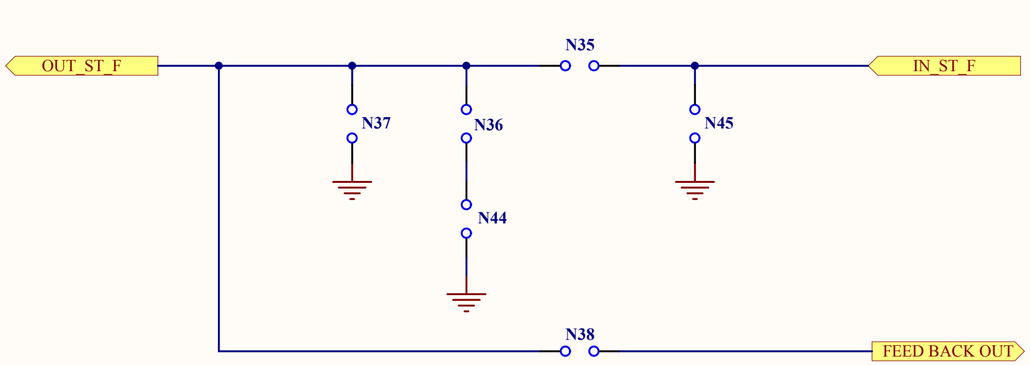
VCC VCO FILTERING



VCC PLL FILTERING



SCHMITT TRIGGER FILTERING



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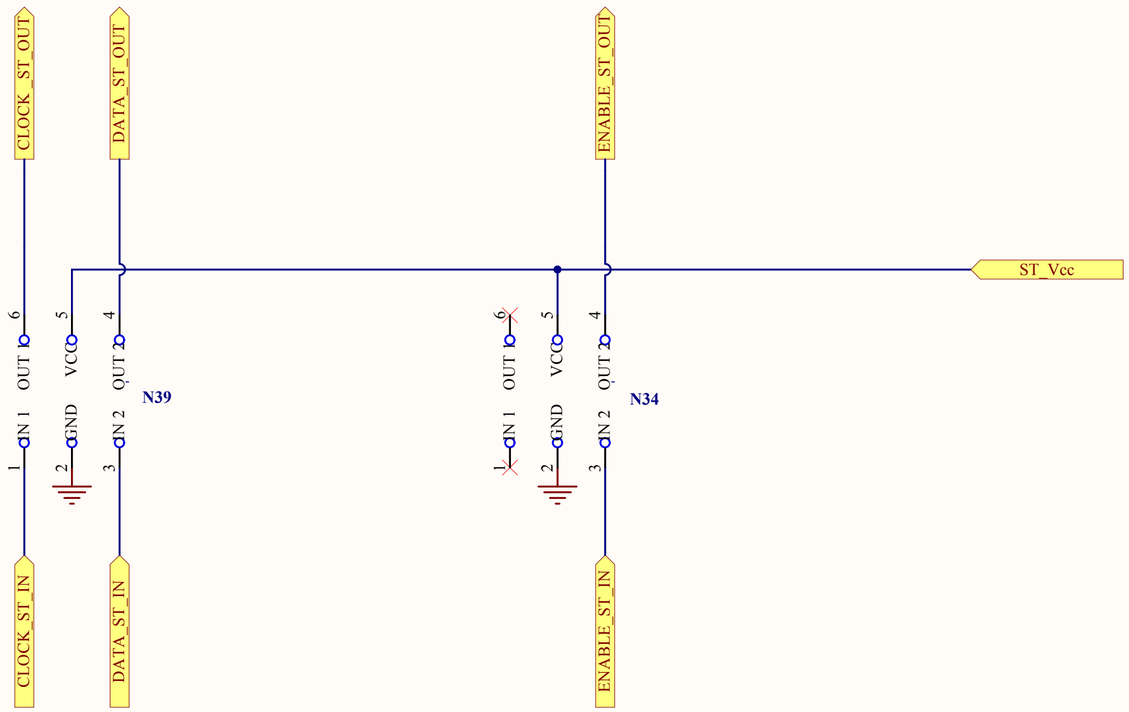
SCHMITT TRIGGER



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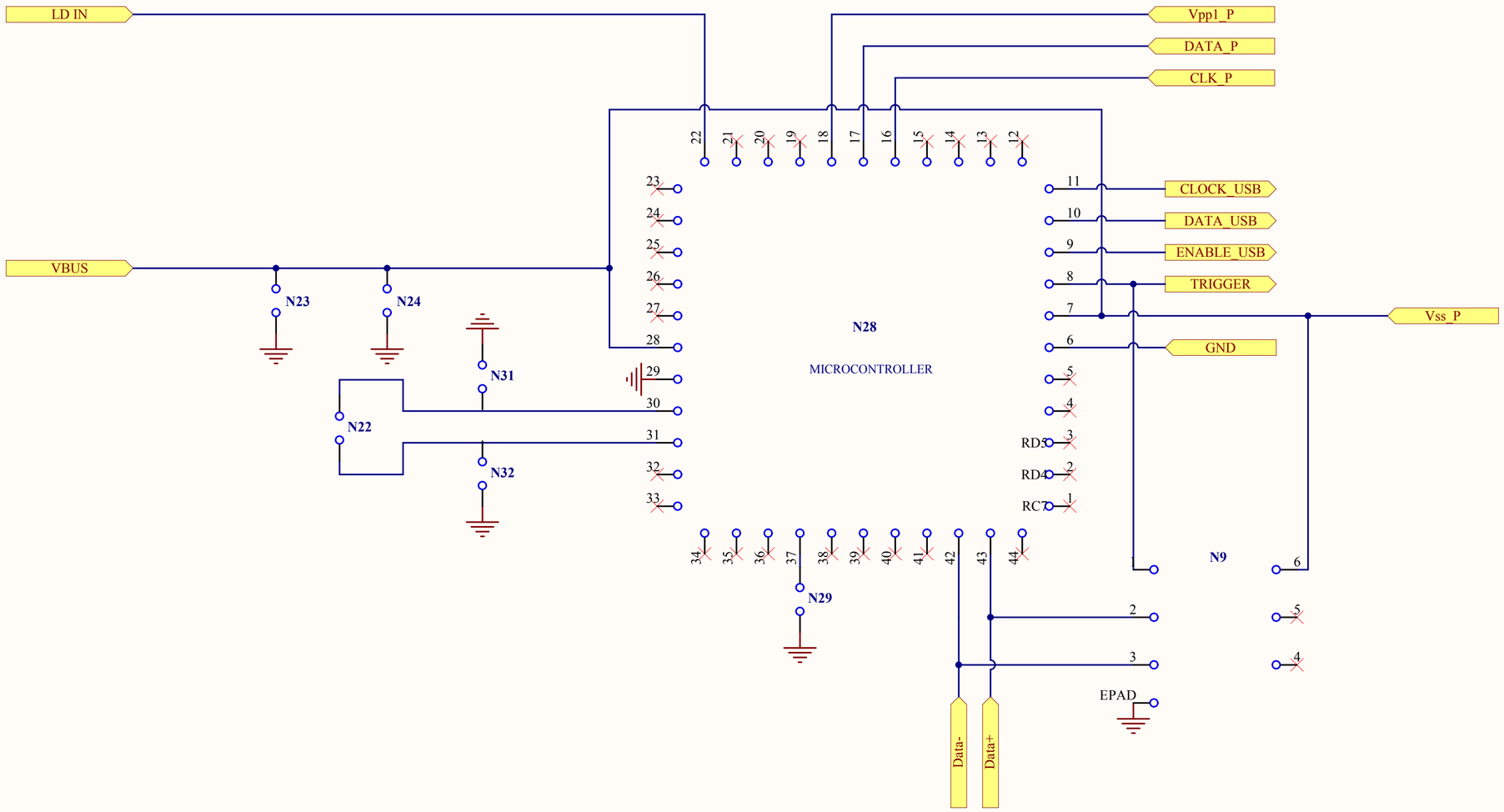
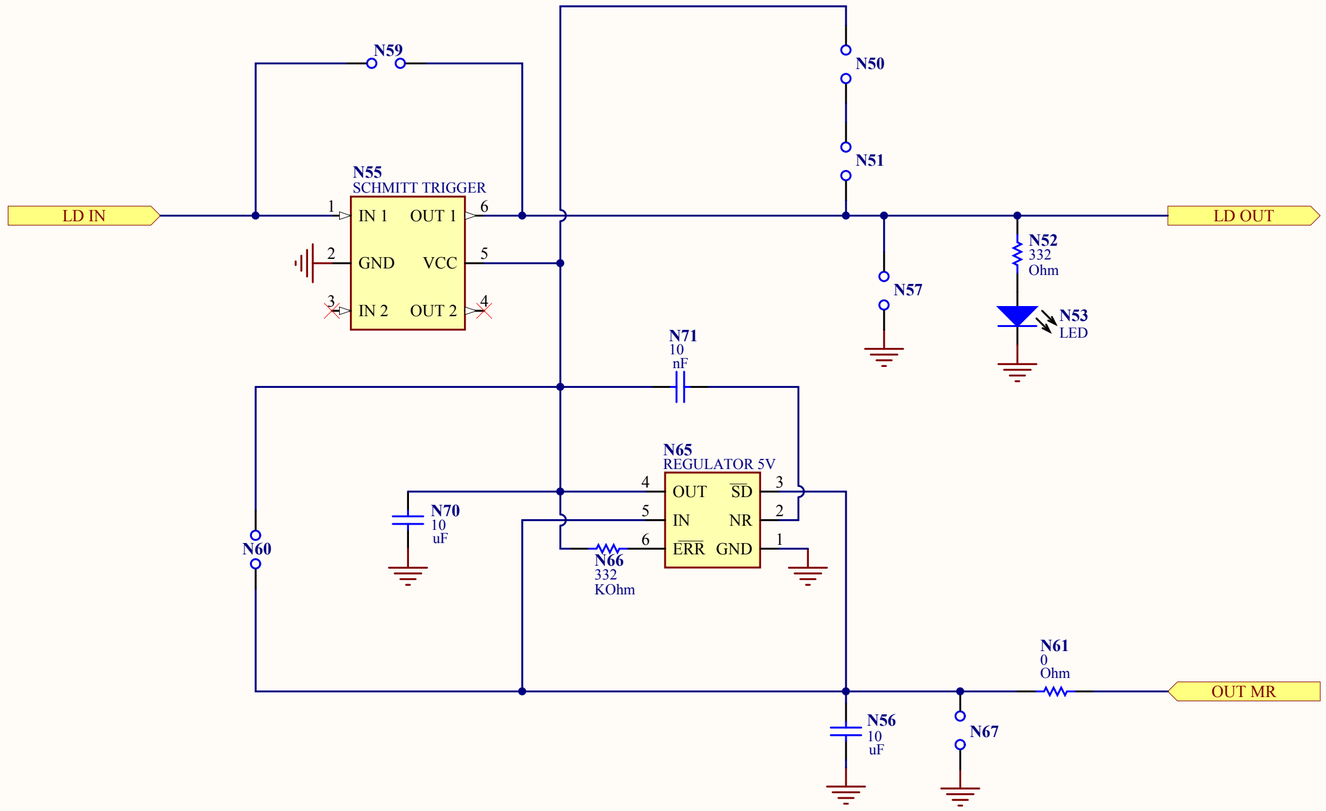
USB CONTROLER



LD REGULATOR SCHMITT TRIGGER AND LED



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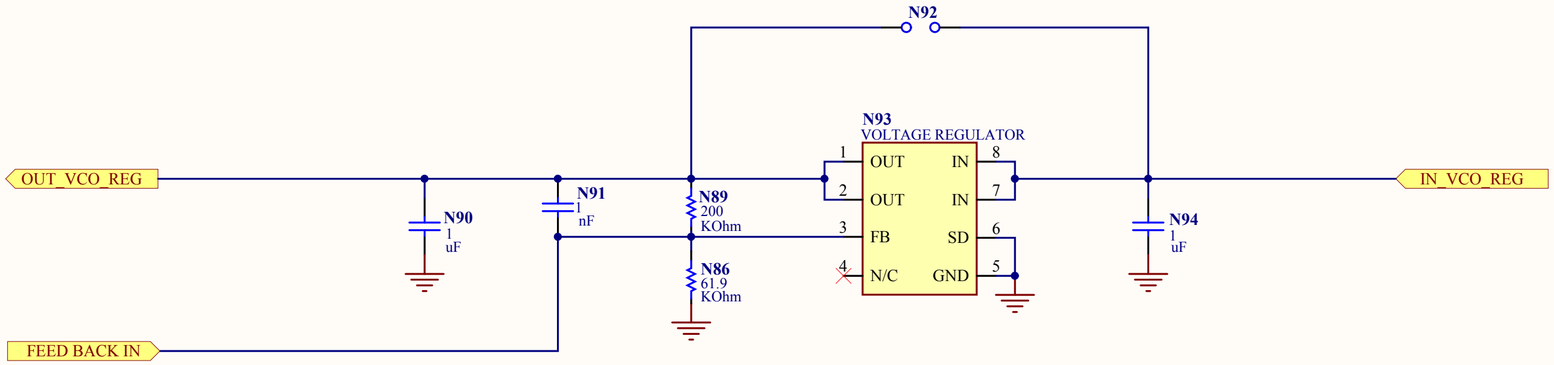
VCC VCO REGULATOR



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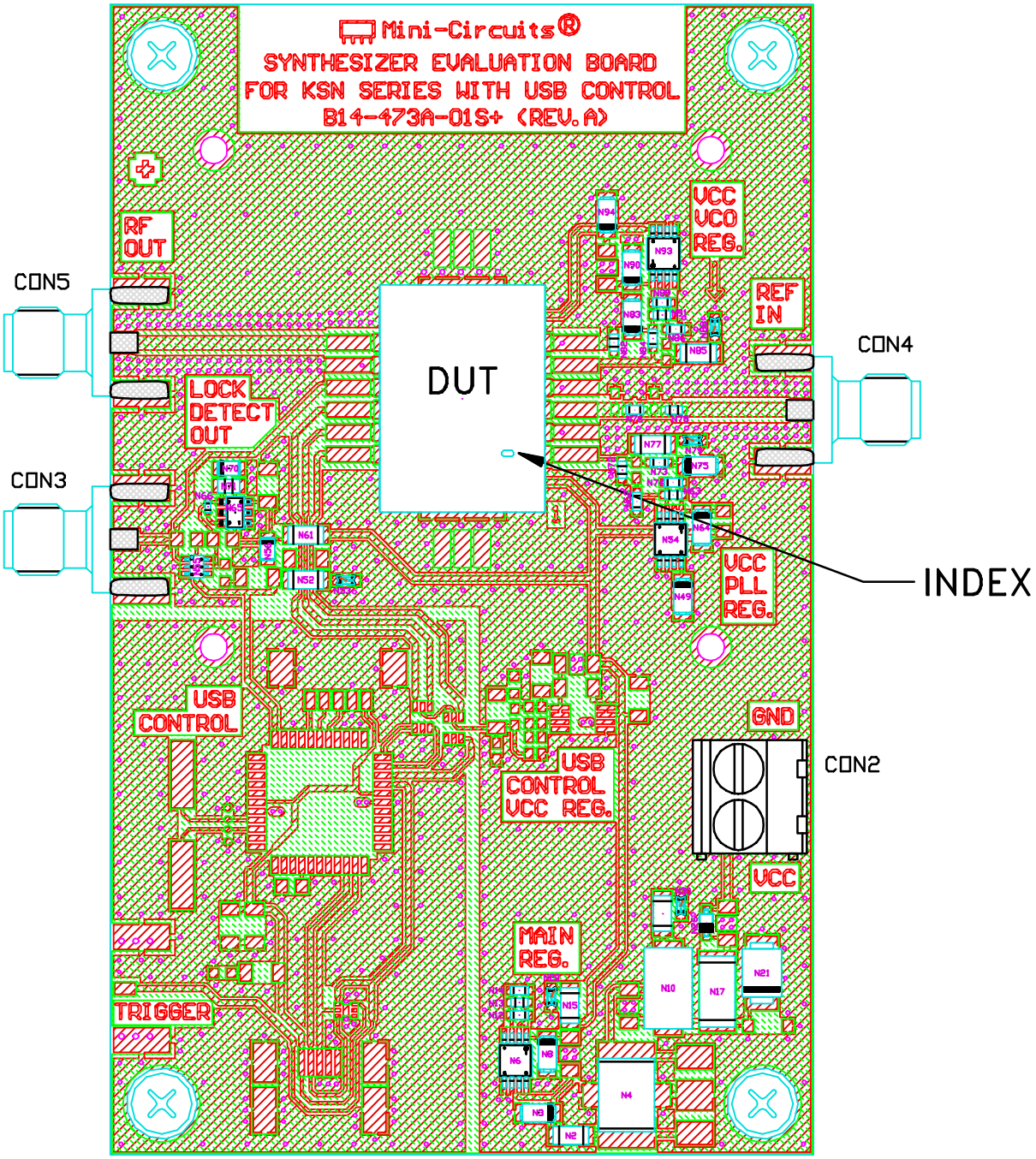
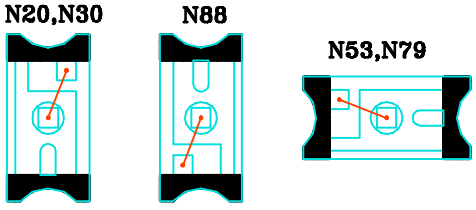
Evaluation Board and Circuit



TB-567-1+F



NOTES:

1. SMA F JACK CONNECTORS.
2. PCB MATERIAL: FR4 OR EQUIVALENT, DIALECTRIC CONSTANT=4.7, DIALECTRIC THICKNESS=.059 INCH.

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Mini-Circuits

Environmental Specifications ENV65T1

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-45° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215

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