

Engineering Development Model

Frequency Synthesizer

KSN-EDR11011SA

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : DK1042

ELECTRICAL SPECIFICATIONS 50Ω, over -5°C to +75°C				
Parameter	Min.	Typ.	Max.	Units
Frequency	100		100	MHz
Step size		10000		kHz
Settling Time Within ±1kHz		250		μsec
Output Power	+1	+5	+9	dBm
Phase Noise				
at 100 Hz offset		-111		dBc/Hz
at 1 kHz offset		-122	-117	dBc/Hz
at 10 KHz offset		-124		dBc/Hz
at 100 KHz offset		-136	-131	dBc/Hz
at 1000 kHz offset		-159	-153	dBc/Hz
Integrated SSB Phase Noise		-75		dBc
Ref & Comp Spurious Suppression		-88		dBc
Non-Harm. Spurious Suppression		-90		dBc
Harmonic Suppression		-23		dBc
Supply voltage		5		V
VCO		3		V
Supply current		36	44	V
VCO		4	12	V
Reference In (External)		10		MHz
Amplitude		1		Vp-p
Impedance		100		kΩ
Ph. N @ 1kHz		-145		dBc/Hz
Input Logic Levels	2.4		3	V
Logic High			0.6	
Digital Lock Detect	2.6		3	V
Locked			0.4	
Unlocked				
Frequency Synthesizer PLL		ADF4001		

ABSOLUTE MAXIMUM RATINGS	
Operating Temperature	-45°C to 85°C
Storage Temperature	-55°C to 100°C
VCO Supply Voltage	6V
PLL Supply Voltage	4V
Reference Frequency voltage	5.8Vp-p
Data, Clock & LE levels	3.3V

Power On sequence: Vcc VCO followed by Vcc PLL
Power Off sequence: Vcc PLL followed by Vcc VCO

PIN CONNECTIONS			
RF OUT	7	CLOCK	10
VCC VCO	5	DATA	11
VCC PLL	1	LATCH ENABLE	12
REF IN	3	GROUND	2,4,6,8,13,14
LOCK DETECT	9		

Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
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Typical Performance Data

FREQ. (MHz)	POWER OUTPUT (dBm)			HARMONICS (dBc)						VCO CURRENT (mA)			PLL CURRENT (mA)		
				F2			F3								
	-5°C	+25°C	+75°C	-5°C	+25°C	+75°C	-5°C	+25°C	+75°C	-5°C	+25°C	+75°C	-5°C	+25°C	+75°C
100	5.11	5.09	4.78	-21.27	-23.51	-43.53	-25.83	-26.64	-29.44	35.47	35.79	36.70	3.69	3.91	4.30

FREQ. (MHz)	PHASE NOISE (dBc/Hz)														
	@ OFFSETS														
	-5°C					+25°C					+75°C				
	100Hz	1kHz	10kHz	100kHz	1MHz	100Hz	1kHz	10kHz	100kHz	1MHz	100Hz	1kHz	10kHz	100kHz	1MHz
100	-111.97	-119.47	-121.24	-135.13	-158.30	-115.02	-121.58	-121.96	-136.15	-158.28	-109.80	-121.00	-122.73	-134.31	-158.41

REFERENCE & COMPARISON SPURIOUS ORDER	REFERENCE & COMPARISON SPURIOUS @Fcarrier 2457.6MHz± (n*Fcomparison) (dBc) NOTE 1		
	-5°C	+25°C	+75°C
n			
-5	-91.75	-102.27	-92.44
-4	-90.85	-94.86	-91.2
-3	-94.09	-85.74	-93.43
-2	-92.97	-85.4	-92.01
-1	-90.28	-84.9	-89.06
0 note 2	-	-	-
+1	-86.56	-81.76	-87.39
+2	-85.36	-80.84	-89.36
+3	-85.85	-80.89	-88.48
+4	-88.49	-82.82	-92.68
+5	-95.12	-86.98	-94.86

Note 1: Comparison frequency & Reference frequency 10 MHz

Note 2: All spurs are referenced to carrier signal (n=0).

Notes

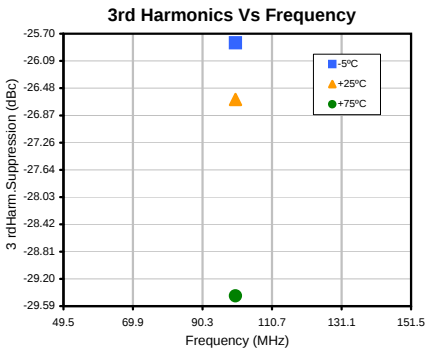
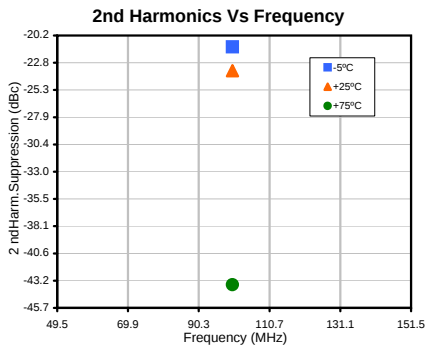
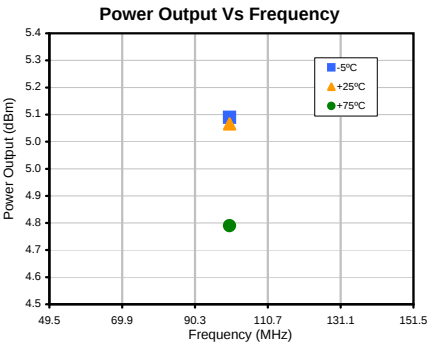
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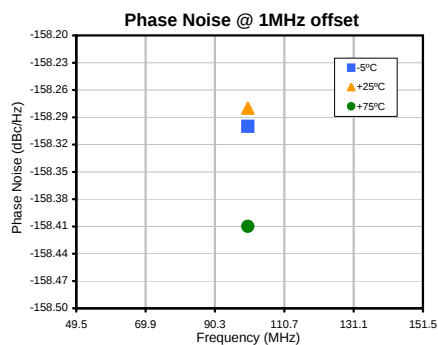
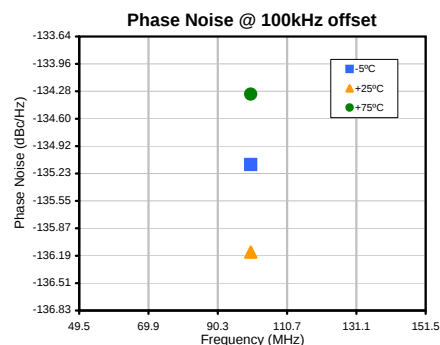
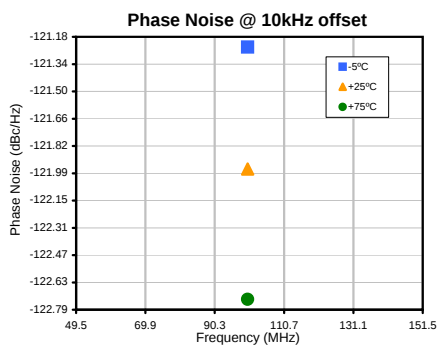
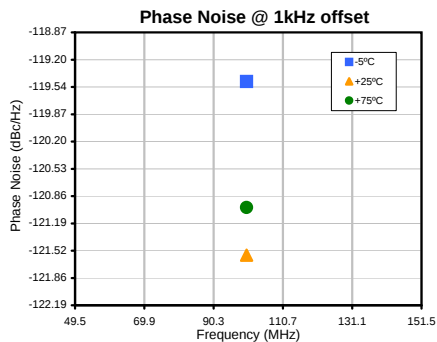
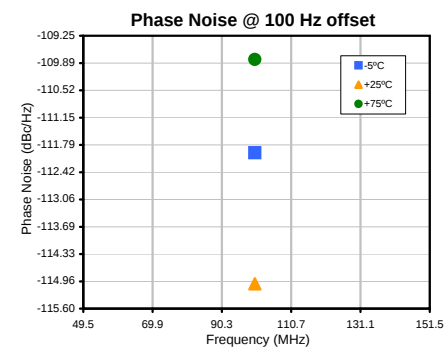
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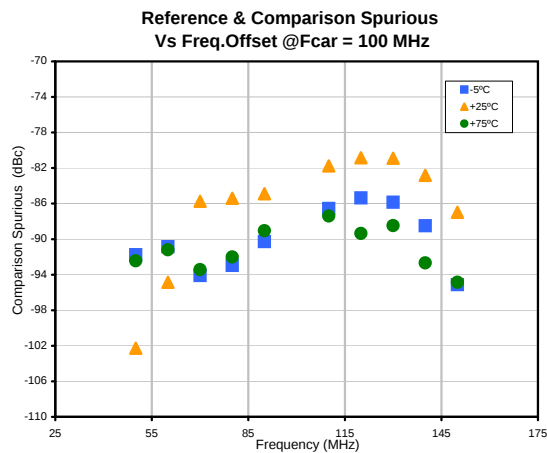
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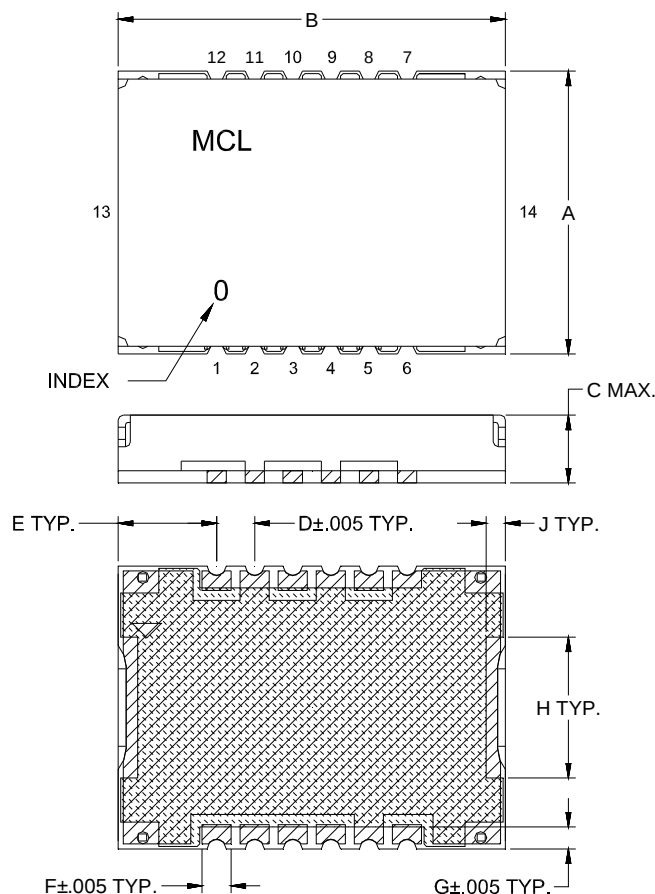


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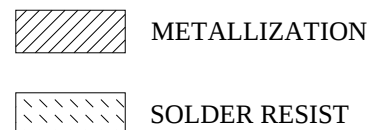
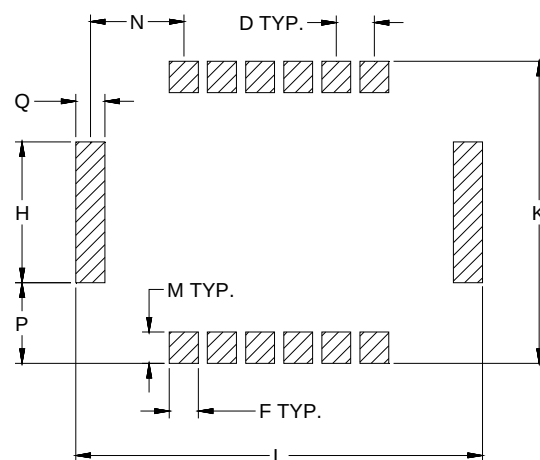
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Outline Dimensions



Suggested PCB Land Pattern



CASE#	A	B	C	D	E	F	G	H	J	K	L	M	N	P	Q	WT. GRAM
DK1042	.584 (14.83)	.800 (20.32)	.154 (3.91)	.079 (2.01)	.203 (5.16)	.060 (1.52)	.045 (1.14)	.291 (7.39)	.039 (1.0)	.624 (15.85)	.840 (21.34)	.065 (1.65)	.193 (4.90)	.170 (4.32)	.060 (1.52)	1.6

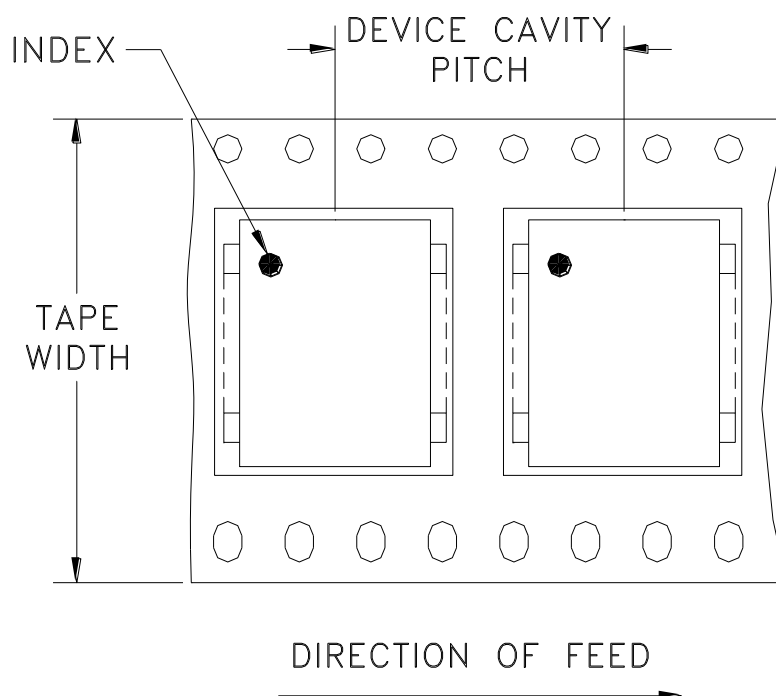
Dimensions are in inches (mm). Tolerances: 2PL. +/- .03; 3PL. +/- .015

Notes:

- Case material: Nickel-Silver alloy.
- Base: Printed wiring laminate.
- Termination finish:
For RoHS Case Styles: 2-5 μ inch (.05-.13 microns) Gold over 120-240 μ inch (3.05-6.10 microns) Nickel plate.
All models, (+) suffix.

Tape & Reel Packaging TR-F28

DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
32	20	13	Small quantity standards (see note)	20
				50
				100
				200
			Standard	500

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



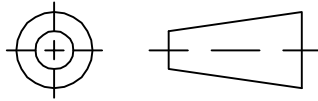
INTERNET <http://www.minicircuits.com>

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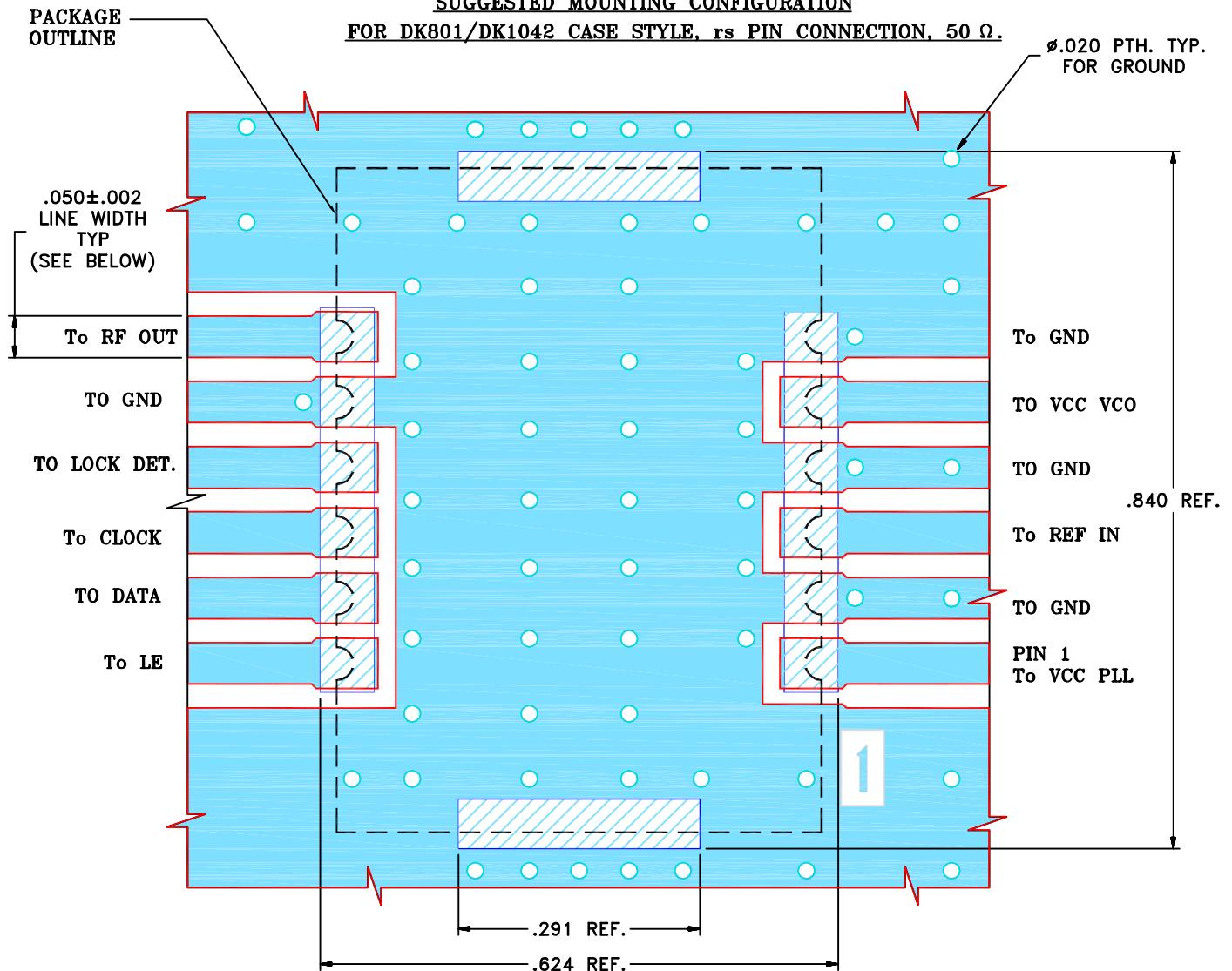
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M108069	NEW RELEASE	11/06	DK	HH
A	M110535	MODIFY GROUND PADS	03/07	DK	HH
B	M126537	DELETE CHIP COMP. & TABLE OF VALUES	03/10	DK	HH
B	R78979	DELETE CHIP COMP. & TABLE OF VALUES	03/10	DK	HH

SUGGESTED MOUNTING CONFIGURATION

FOR DK801/DK1042 CASE STYLE, rs PIN CONNECTION, 50 Ω . ϕ .020 PTH. TYP.
FOR GROUND

NOTES:

- TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS. .025"±.002". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
- BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

DENOTES COPPER LAND PATTERN FREE OF SOLDERMASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS ±

3 PL DECIMALS ± .005

ANGLES ±

FRACTIONS ±

DRAWN

DK (RAVON)

27 MAR 07

CHECKED

RZ (RAVON)

27 MAR 07

APPROVED

HH (RAVON)

27 MAR 07

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ASHEETA1.DWG REV:A DATE:01/12/95


Mini-Circuits® 13 Neptune Avenue
Brooklyn NY 11235

PL, rs ,DK801/DK1042, KSN
TB-567+ (50 Ω)
SIZE
A

CODE IDENT
15542

DRAWING NO:
98-PL-249

REV:
B

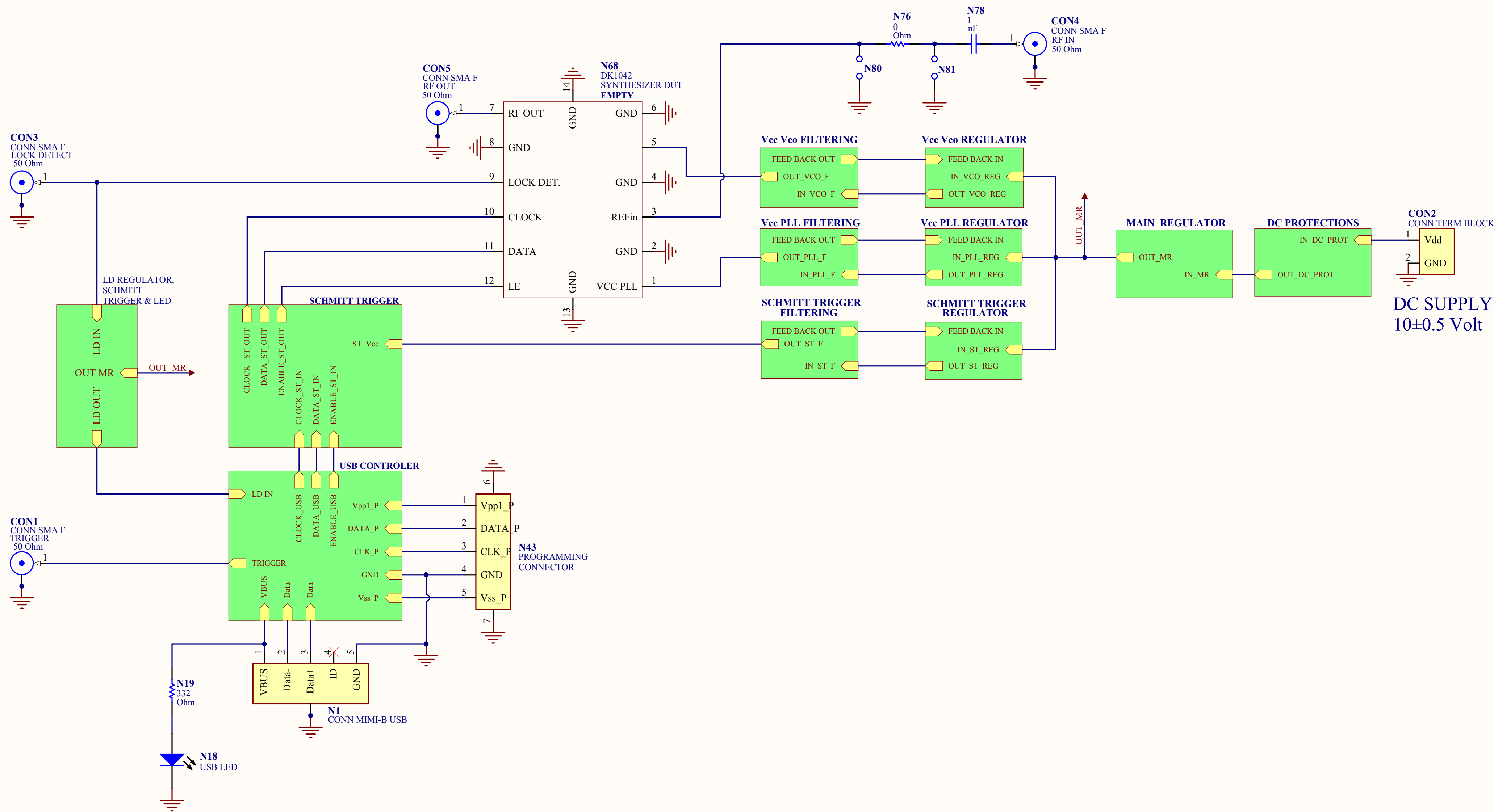
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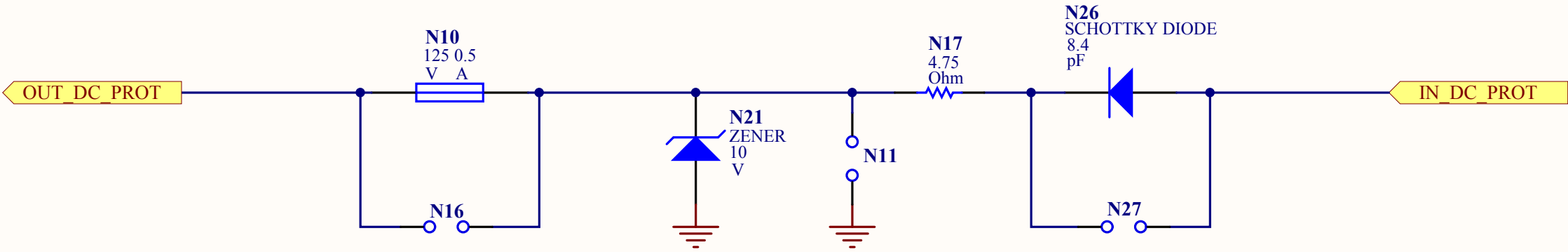
SHEET: 1 OF 1

ELECTRICAL SCHEME-GENERAL

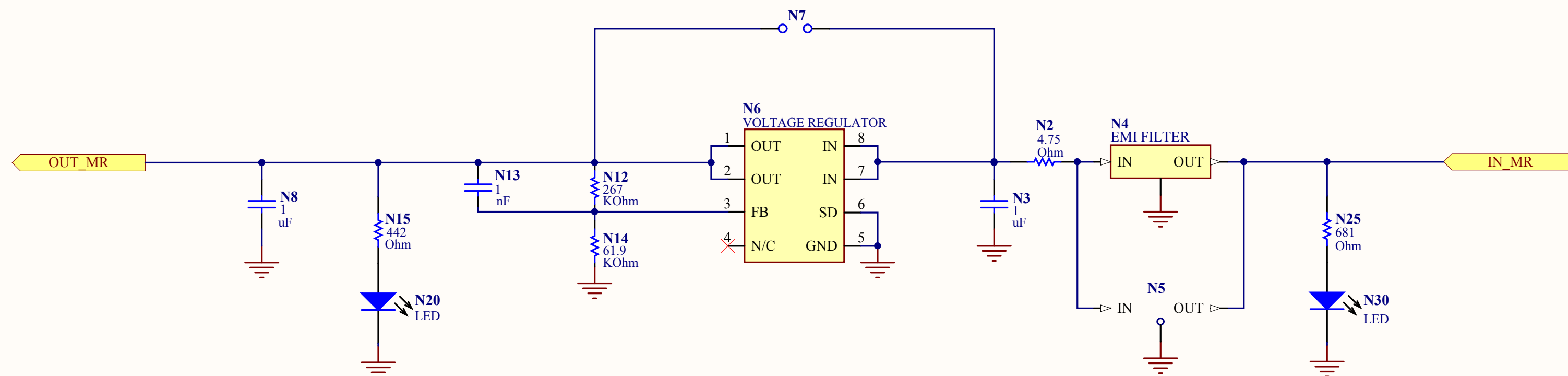
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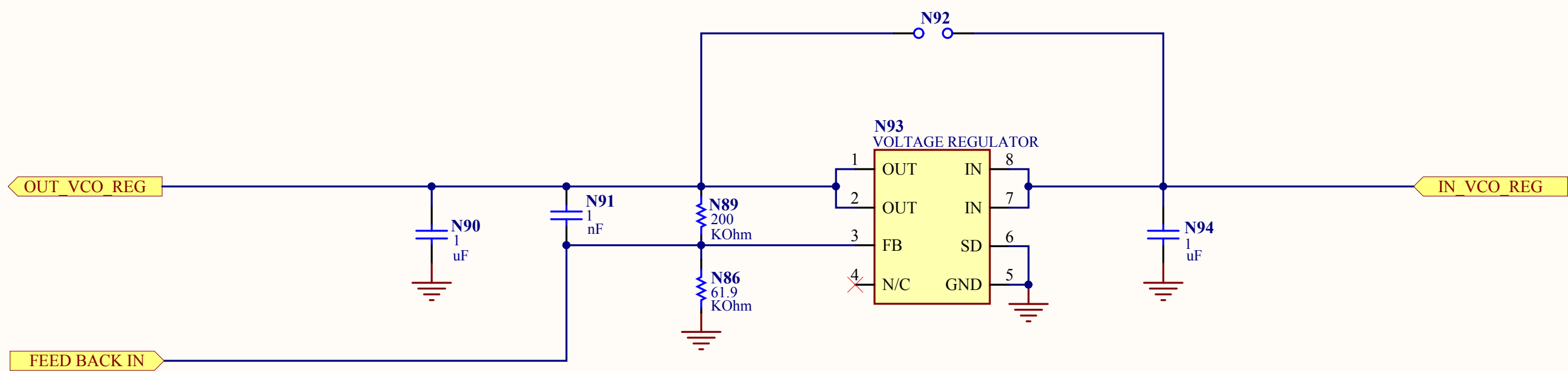
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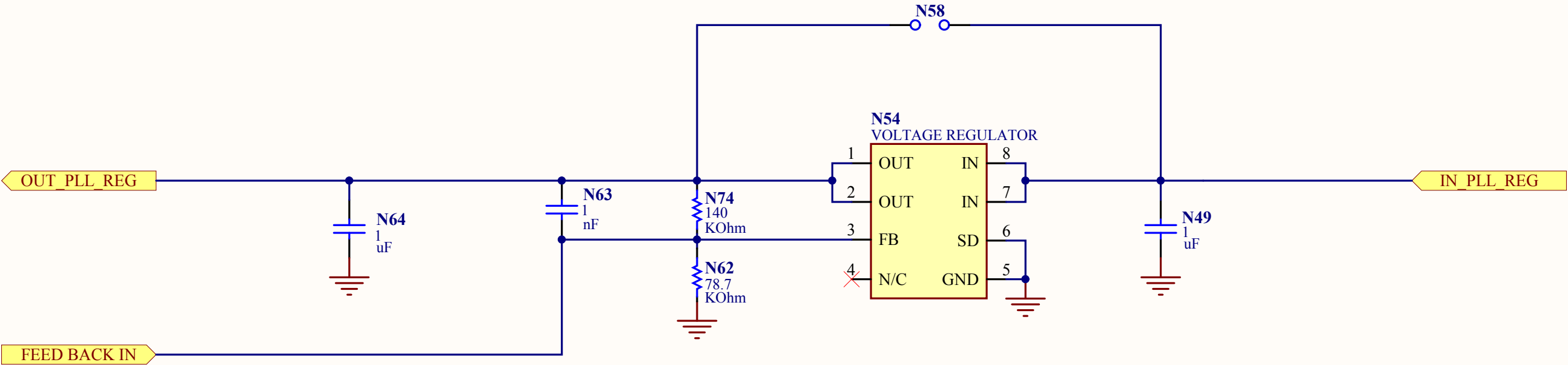
MAIN REGULATOR



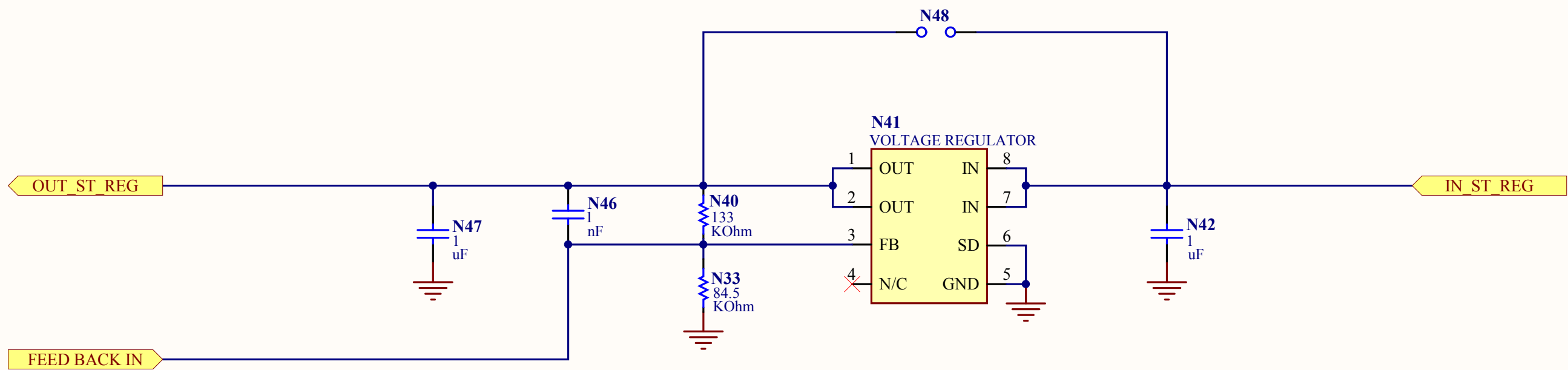
VCC VCO REGULATOR



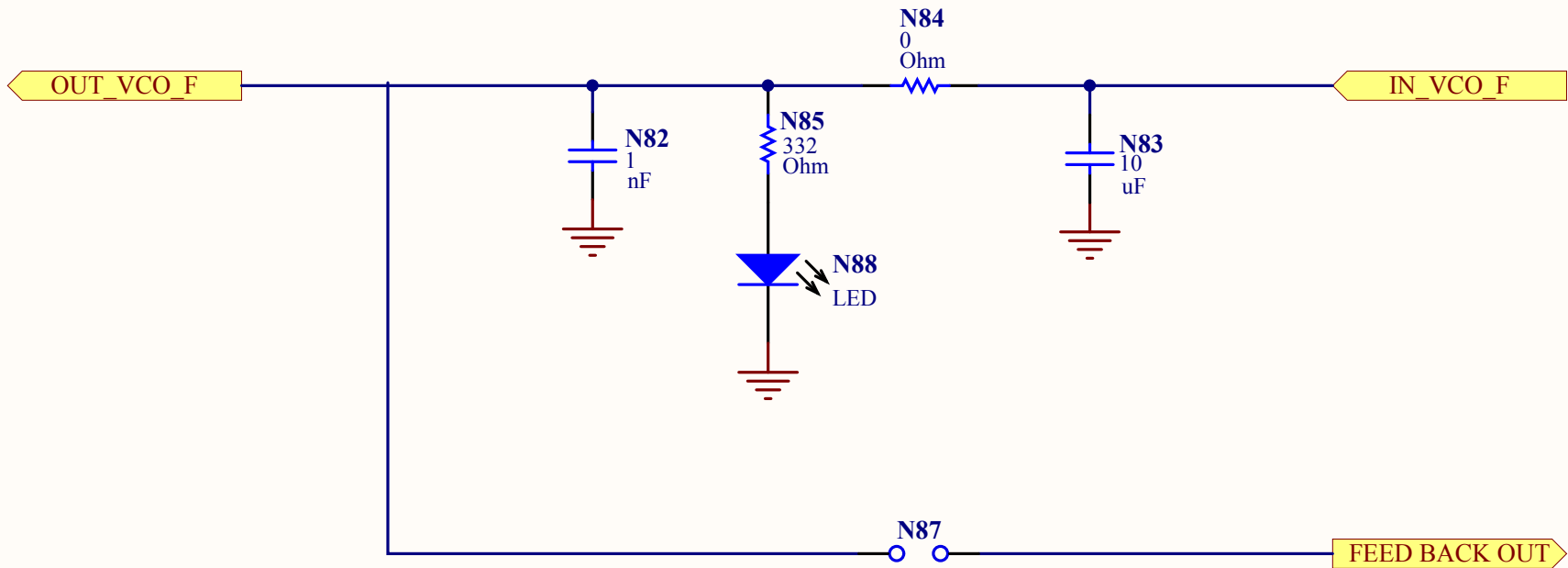
VCC PLL REGULATOR



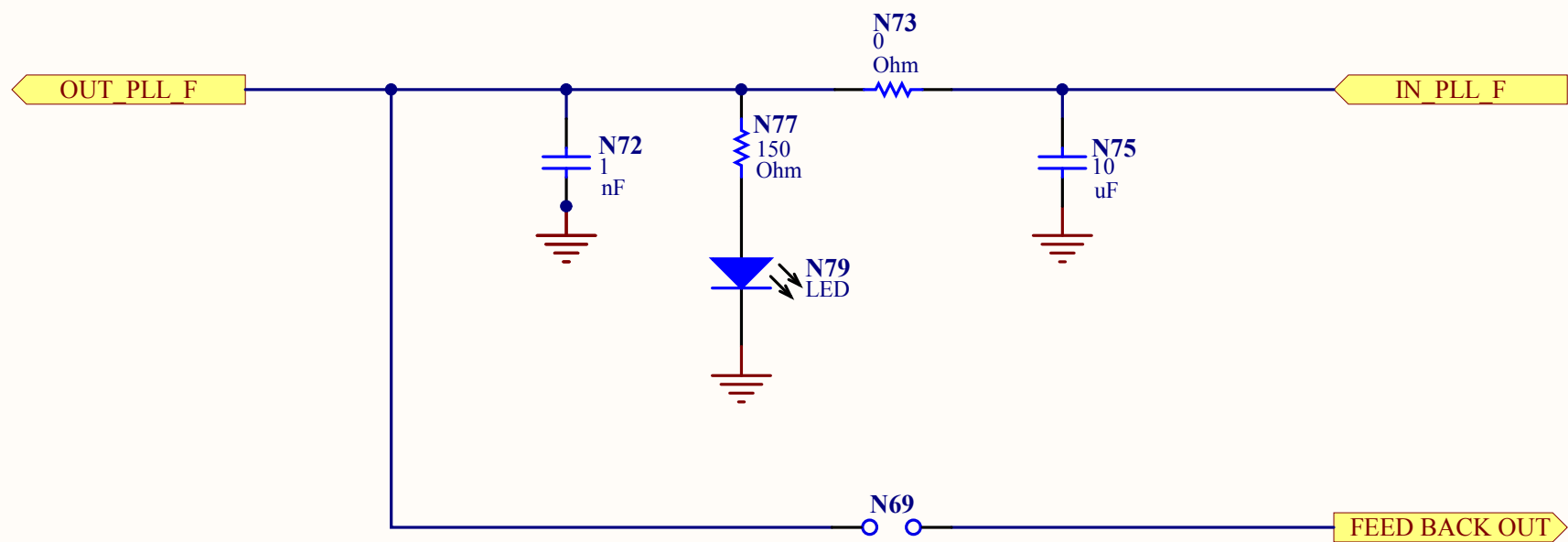
SCHMITT TRIGGER REGULATOR



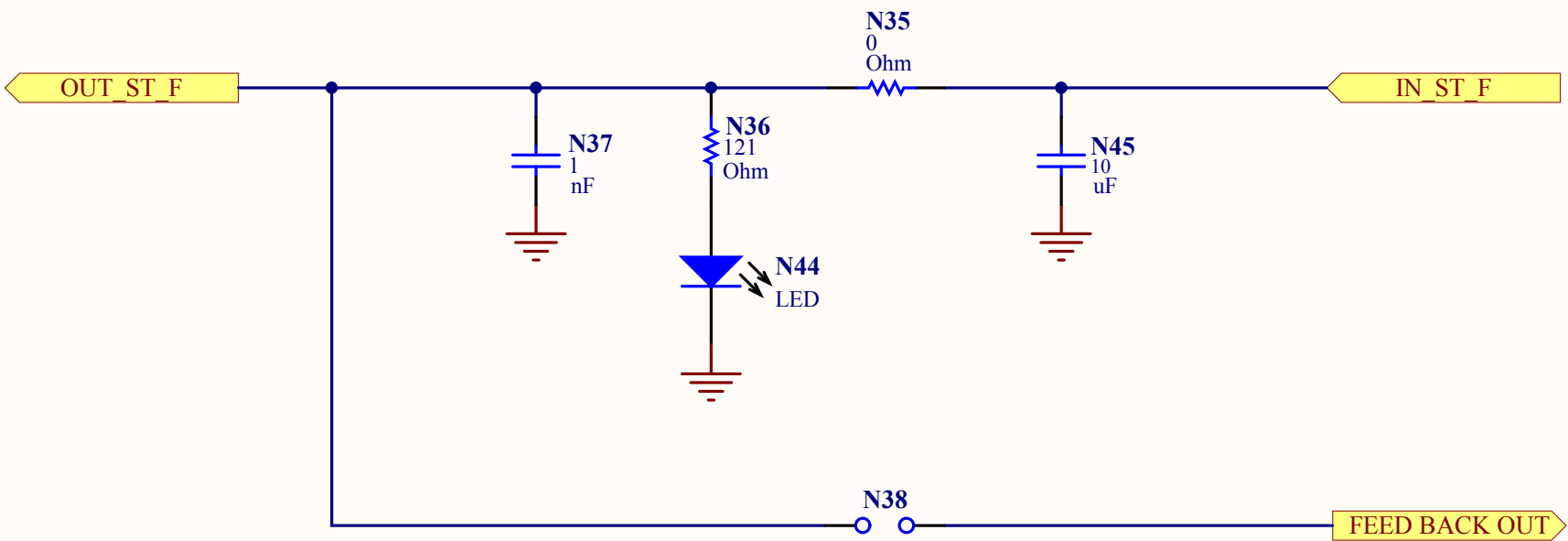
VCC VCO FILTERING



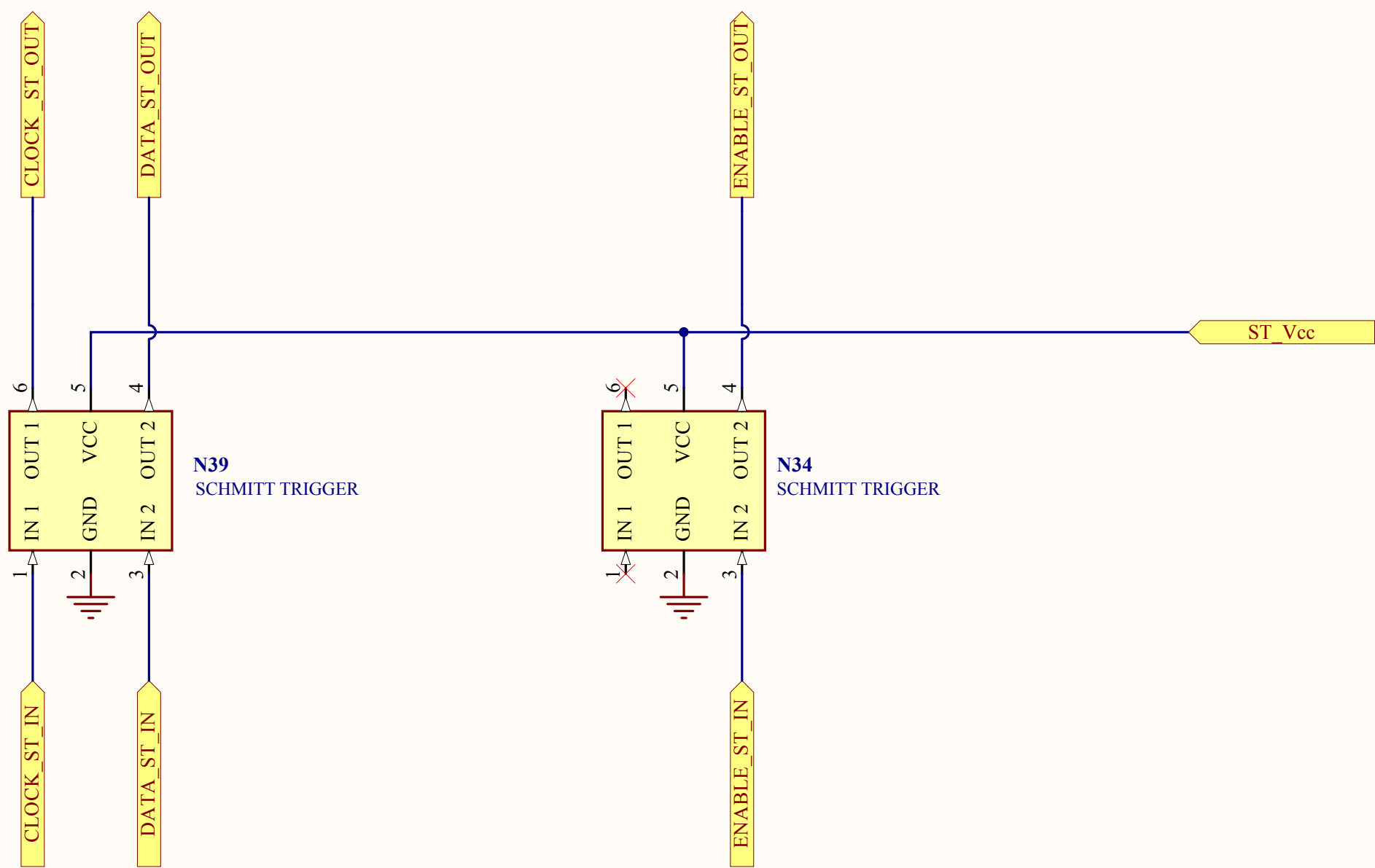
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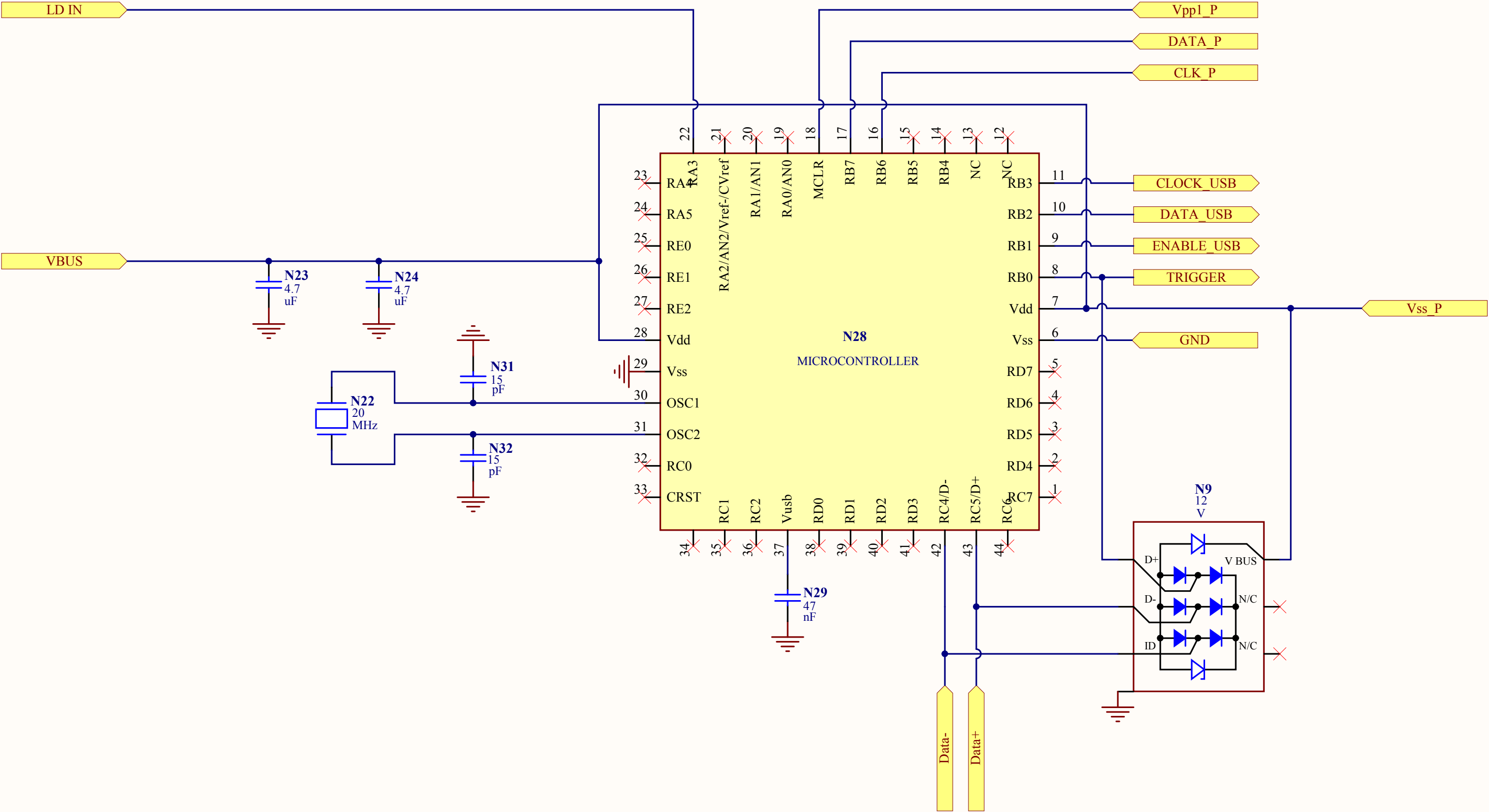
SCHMITT TRIGGER FILTERING



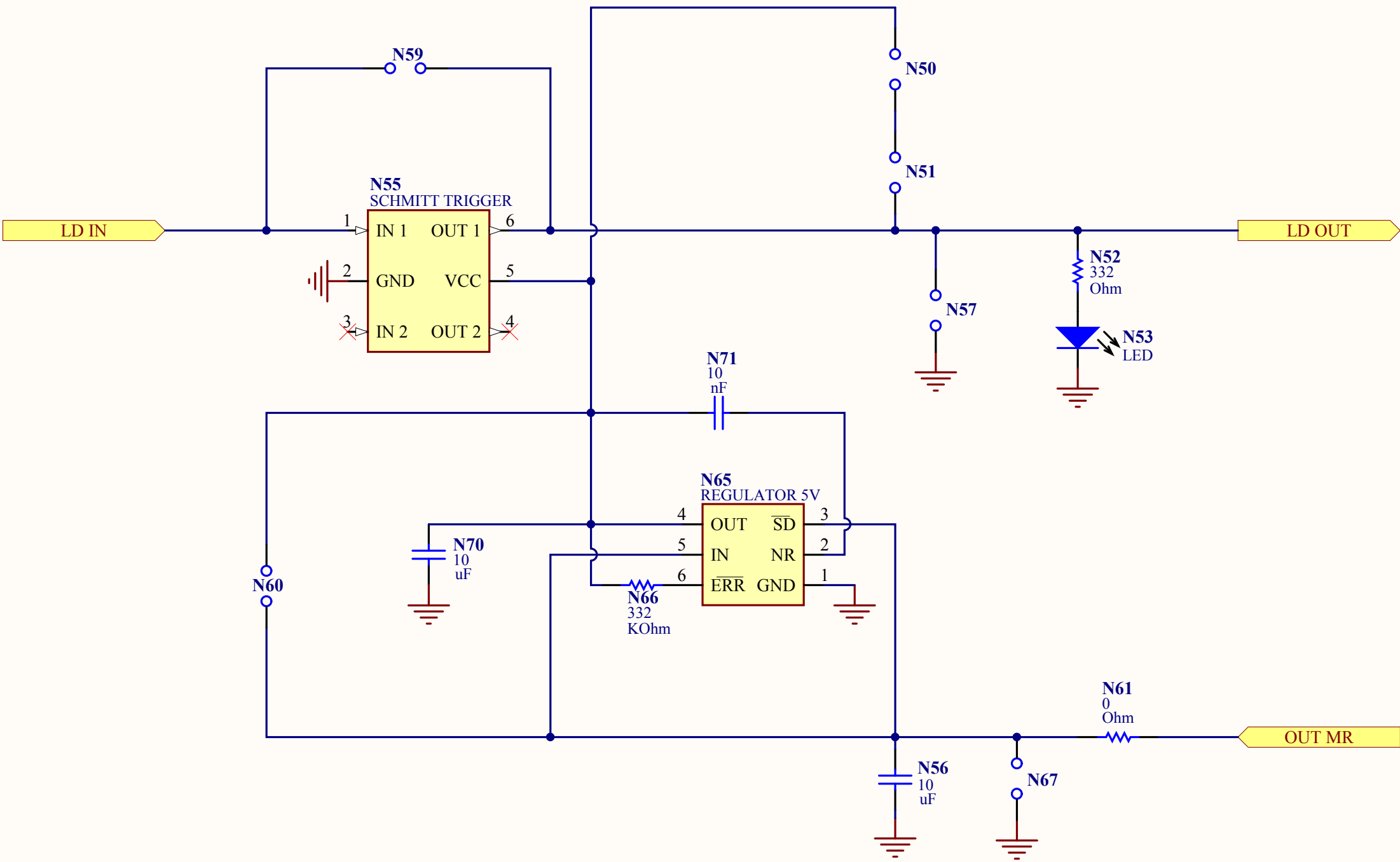
SCHMITT TRIGGER



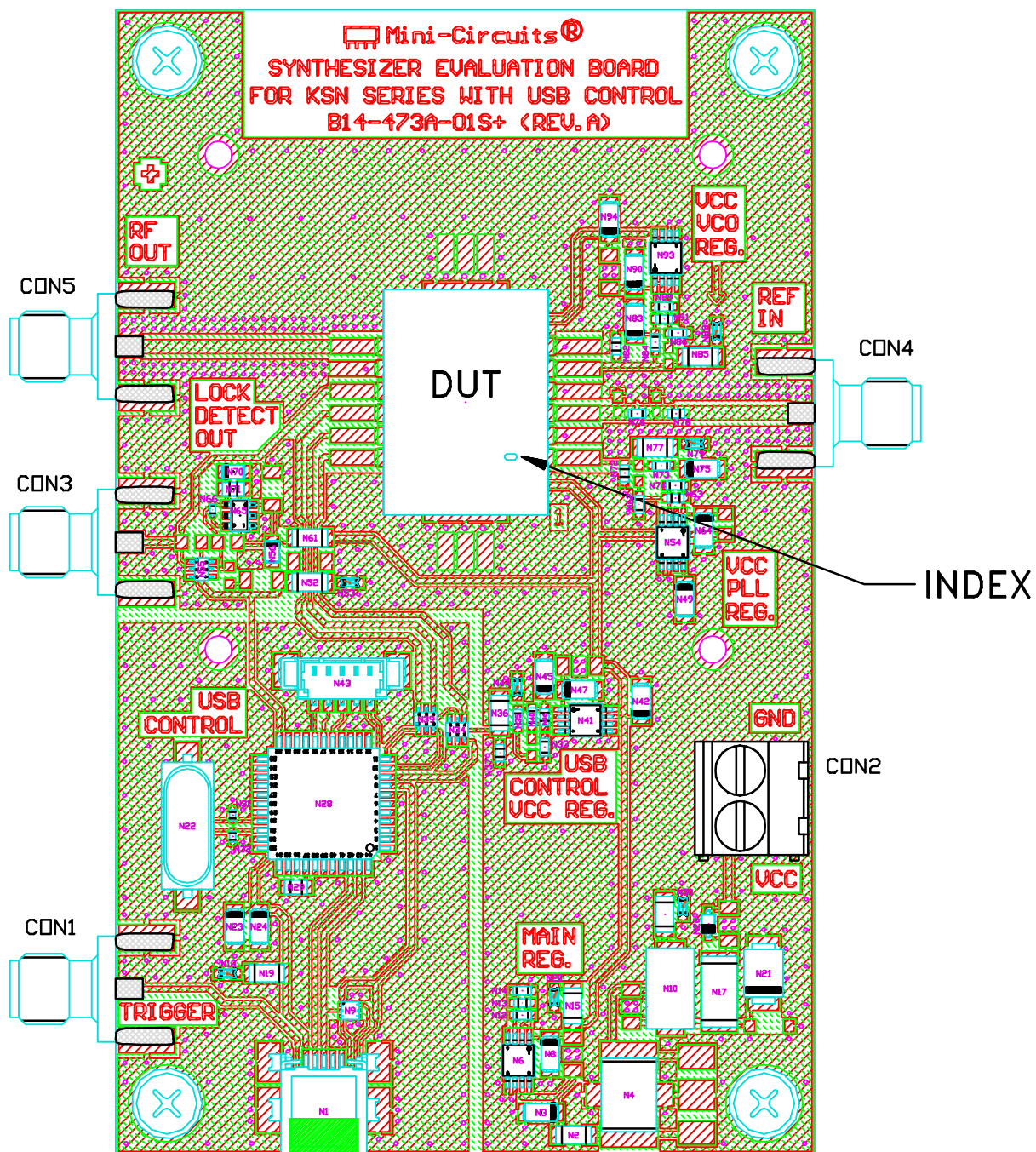
USB CONTROLER



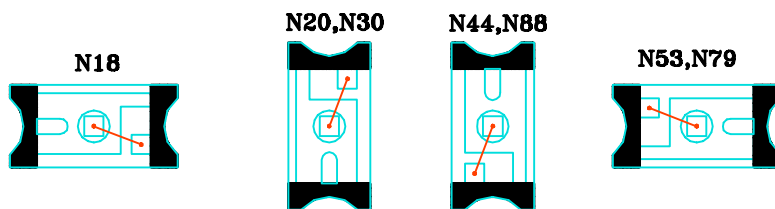
LD REGULATOR SCHMITT TRIGGER AND LED



Evaluation Board and Circuit



TB-567-1+



NOTES:

1. SMA F JACK CONNECTORS.
2. PCB MATERIAL: FR4 OR EQUIVALENT, DIALECTRIC CONSTANT=4.7, DIALECTRIC THICKNESS=.059 INCH.



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-45° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215