Low Pass Filter

50Ω DC¹ to 990 MHz

Features

- Low loss, 0.4 dB typ.
- Small size 0805 (2.0 x 1.25 mm)
- Temperature stable
- LTCC construction

Applications

- Harmonic Rejection
- VHF/UHF transmitters / receivers
- lab use

LFCG-92+



CASE STYLE: GE0805C-2

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications



Electrical Specifications^{1,2} at 25°C

Parameter		F#	Frequency (MHz)	Min.	Тур.	Max.	Unit
	Insertion Loss	DC - F1	DC - 990	_	0.4	0.8	dB
Pass Band	Freq. cut-off	F2	1400	_	3.0	_	dB
	VSWR	DC - F1	DC - 990	_	1.45	_	:1
		F3	1700	_	30	_	dB
Stop Band	Rejection Loss	F4 - F5	1800 - 2700	30	40	_	dB
		F6	5000	_	50	_	dB

¹ In Application where DC voltage is present at either input or output port, coupling capacitors are required.

Maximum Ratings

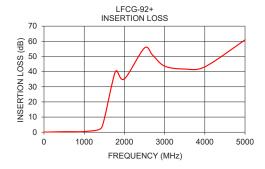
Operating Temperature	-55°C to +100°C
Storage Temperature	-55°C to +100°C
RF Power Input*	2W at 25°C

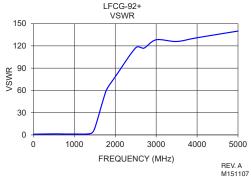
^{*}Passband rating, derate linearly to 1W at 100°C ambient Permanent damage may occur if any of these limits are exceeded.

nanent damage may occur if any of these limits are exceeded.

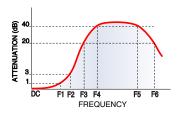
Typical Performance Data at 25°C

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)	
10	0.06	1.03	
20	0.08	1.02	
100	0.11	1.11	
500	0.34	1.44	
700	0.35	1.32	
990	0.44	1.22	
1400	2.18	1.90	
1500	8.83	9.15	
1700	32.78	46.32	
1800	40.59	62.15	
2000	35.16	78.35	
2500	55.61	117.43	
2700	50.94	116.94	
3000	43.62	128.23	
3500	41.69	125.95	
4000	43.14	130.99	
5000	60.95	139.99	



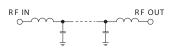


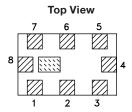
M151107 ED-16419/27 LFCG-92+ MY/CP/AM 150813 Page 1 of 2



Specification Definition

Functional Schematic





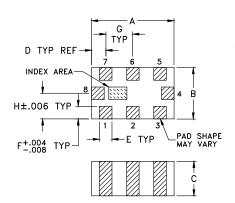
Pad Connections

Input	8
Output	4
Ground	1,3,5,7
Isolate (Do not ground)	2,6

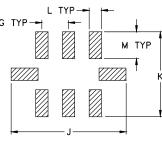
² Measured on Mini-Circuits Characterization Test Board TB-800+

Low Pass Filter LFCG-92+

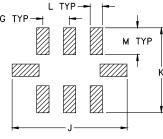
Outline Drawing



PCB Land Pattern

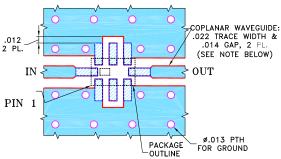


Tolerance to be within .002



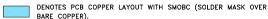
Suggested Layout,

Demo Board MCL P/N: TB-800+ Suggested PCB Layout (PL-427)



NOTES:

- 1. COPLANAR WAVEGIDE IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS. 0.10" ± .00.1". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED. 2. BOTTOM SIDE OF THE PCB IS CONTIN



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

Pad Connections

Input	8
Output	4
Ground	1,3,5,7
Isolate (Do not ground)	2,6

Outline Dimensions (inch)

G	F	Е	D	С	В	Α
.026	.012	.012	.014	.037	.049	.079
0.66	0.30	0.30	0.36	0.94	1.24	2.01
wt		M	L	K	J	Н
wt grams		M .039	L 0.014	.104	J .134	.025

Additional Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp



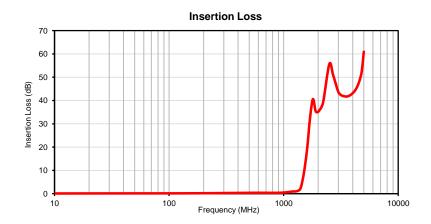
Typical Performance Data

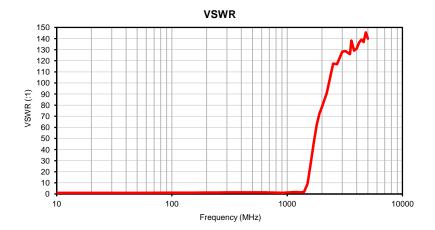
FREQUENCY	INSERTION LOSS	VSWR
(MHz)	(dB)	(:1)
10.0	0.06	1.03
20.0	0.08	1.02
50.0	0.10	1.04
100.0	0.11	1.11
150.0	0.14	1.17
200.0	0.17	1.23
250.0	0.20	1.28
300.0	0.24	1.33
350.0	0.27	1.37
400.0	0.30	1.41
450.0	0.32	1.43
500.0	0.34	1.44
550.0	0.36	1.44
600.0	0.36	1.41
650.0	0.36	1.37
700.0	0.35	1.32
750.0	0.34	1.25
800.0	0.33	1.18
850.0	0.33	1.09
900.0	0.35	1.04
950.0	0.39	1.13
990.0	0.44	1.22
1000.0	0.46	1.25
1100.0	0.68	1.52
1200.0	0.91	1.70
1300.0	1.04	1.43
1400.0	2.18	1.90
1500.0	8.83	9.15
1600.0	19.12	27.82
1700.0	32.78	46.32
1800.0	40.59	62.15
1900.0	35.35	72.27
2000.0	35.16	78.35
2200.0	38.91	90.88
2500.0	55.61	117.43
2700.0	50.94	116.94
3000.0	43.62	128.23
3200.0	42.17	128.69
3500.0	41.69	125.95
3600.0	41.76	138.23
3800.0	42.25	129.20
4000.0	43.14	130.99
4200.0	44.34	136.29
4400.0	46.14	139.09
4600.0	48.71	136.85
4800.0	52.51	145.61
5000.0	60.95	139.99





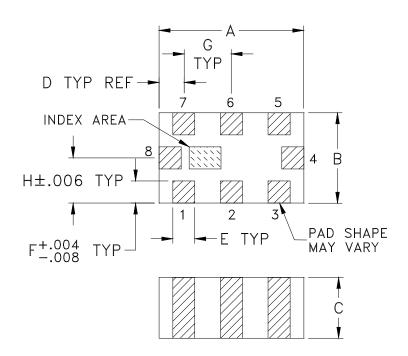
Typical Performance Curves



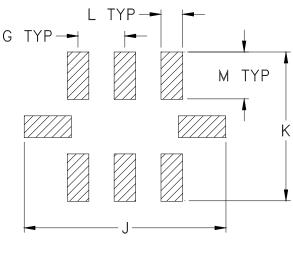


GE0805C-2

Outline Dimensions



PCB Land Pattern



Suggested Layout, Tolerance to be within $\pm .002$

CASE #	A	В	С	D	Е	F	G	Н	J	K	L
GE0805C-2	.079	.049	.037	.014	.012	.012	.026	.025	.134	.110	.014
	(2.00)	(1.25)	(0.95)	(0.35)	(0.30)	(0.30)	(0.65)	(0.63)	(3.40)	(2.80)	(0.35)

CASE #	M	WT. GRAM
GE0805C-2	.039 (1.00)	.008

Dimensions are in inches (mm). Tolerances: 2Pl. ± .01; 3 Pl. ± .005

Notes:

- 1. Open style, ceramic base.
- 2. Termination finish: For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix. For RoHS-5 Case Styles: Tin-Lead plate over Nickel plate. All models, no (+) suffix.
- 3. Pad tolerance to be non-cumulative. Minimum spacing between each pad is .004 (0.1).





P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site

The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F114

DEVICE ORIENTATION IN T&R

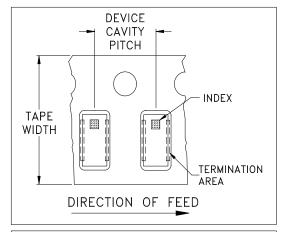


ILLUSTRATION 1

Applicable Case Styles					
GE0805C	JC0603C				
GE0805C-1	JC0603C-4				
GE0805C-1AP	JC0603C-6				
GE0805C-7					
GE0805C-9					
GE0805C-10					
GE0805C-11					
GE0805C-12					

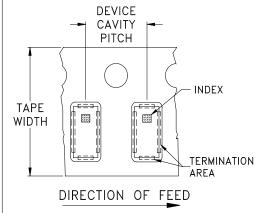


ILLUSTRATION 2

Applicable Case	Styles
GE0805C-2	JC0603C-1
GE0805C-3	JC0603C-2
GE0805C-4	JC0603C-3
GE0805C-5	JC0603C-5
GE0805C-6	JC0603C-7
GE0805C-8	JV1210C-1
GE0805C-15	

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
				20
			Small	50
			quantity	100
8	4	7	standards	200
			(see note)	500
				1000
			Standard	4000

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



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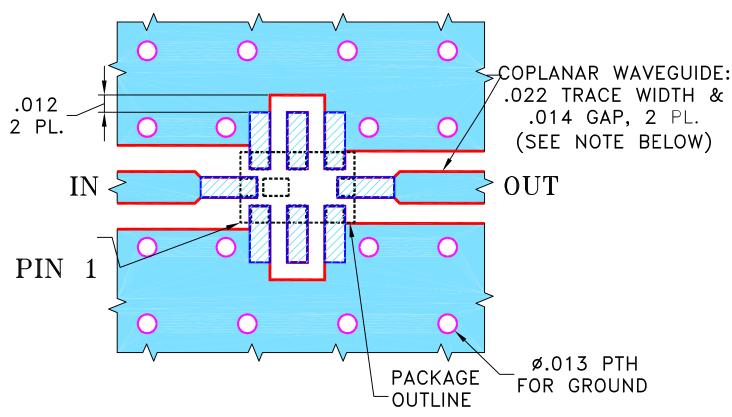
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THIRD ANGLE PROJECTION

REVISIONS						
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH	
OR	M148395	NEW RELEASE	10/03/14	GF	MY	

SUGGESTED MOUNTING CONFIGURATION FOR GE0805C-2 CASE STYLE, "08FL06" PIN CODE



NOTES:

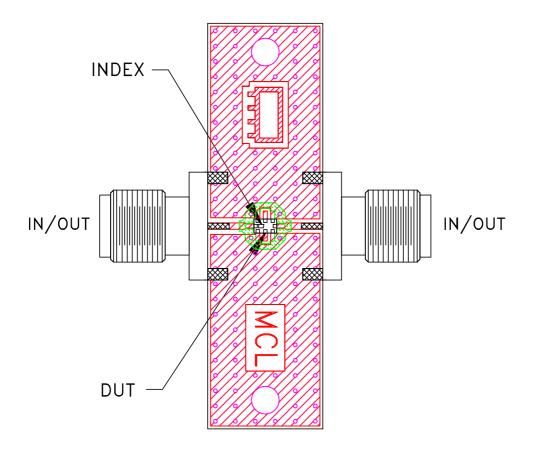
- 1. COPLANAR WAVEGIDE IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .010" \pm .001". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
- 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).

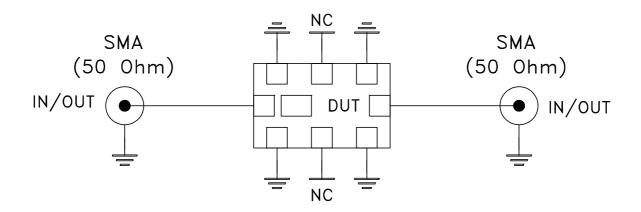
DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE								
DIMENSIONS ARE IN INCHES	DRAWN	GF	10/01/14	- The state of the Brooklyn NY 11235					nue		
TOLERANCES ON: 2 PL DECIMALS ±	CHECKED	IL	10/03/14						NI III	235	
3 PL DECIMALS ± .005	APPROVED	MY	10/03/14]							
FRACTIONS ±				\square PL, 08FL06, GE0805C-2, TB-800			0+				
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			FILE:	98PL427	SCALE:	15:1	SHEET:	1	OF	1	

Evaluation Board and Circuit



TB-800+



Schematic Diagram

Notes:

- 1. 50 Ohm SMA Female connectors.
- 2. PCB Material: R04350 or equivalent, Dielectric Constant=3.5, Thickness=.010 inch.

III Mini-Circuits®



Environmental Specifications

ENV06

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec		
Operating Temperature	-55° to 100°C Ambient Environment	Individual Model Data Sheet		
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet		
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours		
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1		
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage		
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D		
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A		

ENV06 Rev: A

02/25/11

M130240 File: ENV06.pdf