



Mini-Circuits

CERAMIC

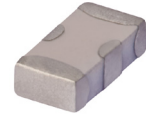
Low Pass Filter

LFCN-1200AT+

50Ω DC¹ to 1200 MHz

FEATURES

- Excellent power handling, 10 W
- Small size
- 7 sections
- Temperature stable
- LTCC construction
- Protected by U.S Patent 6,943,646
- AEC-Q200 qualified component family



Generic photo used for illustration purposes only

CASE STYLE: FV1206

APPLICATIONS

- Automotive

+RoHS Compliant

The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

ELECTRICAL SPECIFICATIONS^{1,2} AT 25°C

Parameter		F#	Frequency (MHz)	Min.	Typ.	Max.	Units
Passband	Insertion Loss	DC-F1	DC-1200	—	—	1.0	dB
	Freq. Cut-Off	F2	1530	—	3.0	—	dB
	VSWR	DC-F1	DC-1200	—	1.2	—	:1
Stop Band	Rejection Loss	F3	1865	20	—	—	dB
		F4-F5	2000-5000	—	30	—	
	VSWR	F6	6200	—	20	—	:1
		F3-F6	1865-6200	—	20	—	

1. In Applications where DC isolation to ground is required, coupling capacitors are recommended to avoid DC leakage. Alternatively, if DC pass IN-OUT is required, Mini-Circuits' "D" suffix version of this model will support DC IN-OUT, and provide >100 MOhm isolation to ground.

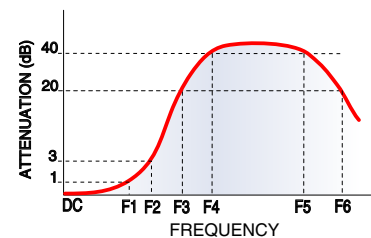
2. Electrical performance measured on TB-270 using LFCN-1200+

ABSOLUTE MAXIMUM RATINGS

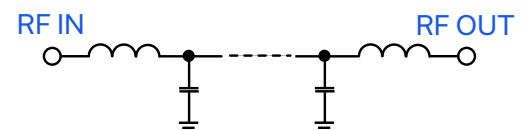
Parameter	Ratings
Operating temperature	-40 to +105 °C
Storage temperature	-40 to +105 °C
RF Power Input ³	10 W max. at +25°C

3. Passband rating, derate linearly to 3.5 W at +105°C ambient.
Permanent damage may occur if any of these limits are exceeded.

TYPICAL FREQUENCY RESPONSE



FUNCTIONAL SCHEMATIC



REV. OR
NPO-005311
LFCN-1200AT+
MCL NY
250603





Mini-Circuits®

CERAMIC

Low Pass Filter

LFCN-1200AT+

PIN CONNECTIONS

RF IN	1
RF OUT	3
GROUND	2,4

PRODUCT MARKING: N/A

DEMO BOARD MCL P/N: TB-270
SUGGESTED PCB LAYOUT (PL-137)



NOTES: 1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH THICKNESS .020" $\pm$.0015".
COPPER: 1/2 OZ. EACH SIDE.
FOR OTHER MATERIALS TRACE WIDTH & GAP MAY NEED TO BE MODIFIED.

2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
 - DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

OUTLINE DRAWING



PCB Land Pattern



OUTLINE DIMENSIONS (Inches mm)

A	B	C	D	E	F	G	
.126	.063	.037	.020	.032	.009	.169	
3.20	1.60	0.94	0.51	0.81	0.23	4.29	
H	J	K	L	M	N	P	wt
.087	.024	.122	.024	.087	.012	.071	grams
2.21	0.61	3.10	0.61	2.21	0.30	1.80	.020

TAPE & REEL INFORMATION: F71



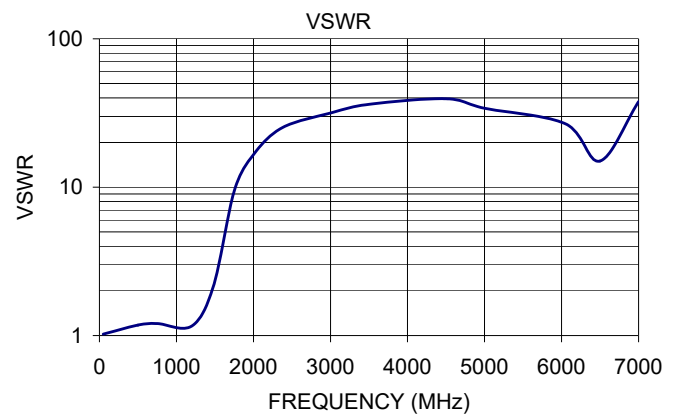
CERAMIC

Low Pass Filter

LFCN-1200AT+

TYPICAL PERFORMANCE DATA AT 25°C

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)
50.00	0.04	1.02
500.00	0.30	1.18
750.00	0.34	1.20
1200.00	0.73	1.16
1480.00	2.79	2.15
1750.00	24.69	9.43
2000.00	31.88	16.56
2375.00	40.40	25.19
3000.00	39.23	31.60
3500.00	37.97	36.20
4500.00	36.37	39.49
5000.00	36.23	34.07
6050.00	22.99	26.74
6500.00	12.01	15.00
7000.00	16.10	37.77



NOTES

- Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard. Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp



Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	L	M	N	P	WT. GRAM
FV1206	.126 (3.20)	.063 (1.60)	.037 (0.94)	.020 (0.51)	.032 (0.81)	.009 (0.23)	.169 (4.29)	.087 (2.21)	.024 (0.61)	.122 (3.10)	.024 (0.61)	.087 (2.21)	.012 (0.30)	.071 (1.80)	.020

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .01$; 3 Pl. $\pm .005$

Notes:

- Open style, ceramic base.
- Termination finish: **as shown below or indicated on Data Sheet.**
For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F71



ILLUSTRATION 1

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	3000

Note: Please Consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf



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The Design Engineers Search Engine Provides ACTUAL Data Instantly From Mini-Circuits At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M88634	NEW RELEASE	08/28/03	GF	ABD
A	M102713	ADDED "...WITH SMOBC"	01/17/06	MMG	IL

**SUGGESTED MOUNTING CONFIGURATION
FOR FV1206 CASE STYLE, "nx" PIN CONNECTION**



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DENOTES PCB COPPER LAYOUT WITH SMOBC
(SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005ANGLES $\pm$ FRACTIONS $\pm$

DRAWN

GF

08/27/03

CHECKED

AV

08/28/03

APPROVED

ABD

08/28/03



Mini-Circuits®

13 Neptune Avenue
Brooklyn NY 11235

PL, nx, FV1206, LFCN/HFCN, TB-270

SIZE

A

CODE IDENT

15542

DRAWING NO:

98-PL-137

REV:

A

FILE:

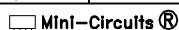
98PL137

SCALE:

10:1

SHEET:

1 OF 1



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ASHEETA1.DWG REV:A DATE:01/12/95