CERAMIC BALUN

RF Transformer

NCS1-23+

50Ω 1300 to 2000 MHz 1:1 Ratio

FEATURES

- Wideband, 1300 to 2000 MHz
- Low phase unbalance, 2 deg. and amplitude unbalance, 0.3 dB typ.
- Miniature size, 0.079"x0.049"x0.033"
- LTCC construction
- Low cost
- Aqueous washable



Generic photo used for illustration purposes only

CASE STYLE: GE0805C-1

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

APPLICATIONS

- WCDMA
- PCS
- GPS

ELECTRICAL SPECIFICATIONS AT 25°C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Units
Impedance Ratio			1		
Frequency Range		1300		2000	MHz
Insertion Loss[1]	1300 - 2000	—	1.0	—	dB
Amplitude Unbalance	1300 - 2000	—	0.3	—	dB
Phase Unbalance[2]	1300 - 2000	—	2	—	Degree

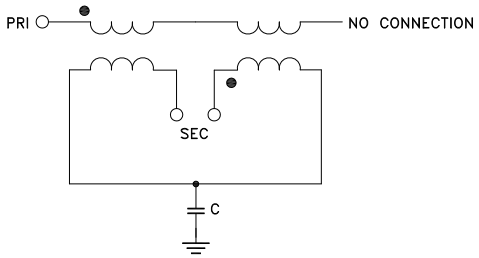
1. Insertion Loss is referenced to mid-band loss, 0.7 dB. Reference Demo Board TB-419+
2. Relative to 180°

MAXIMUM RATINGS

Parameter	Ratings
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
RF Power	3W

Permanent damage may occur if any of these limits are exceeded.

CONFIGURATION R

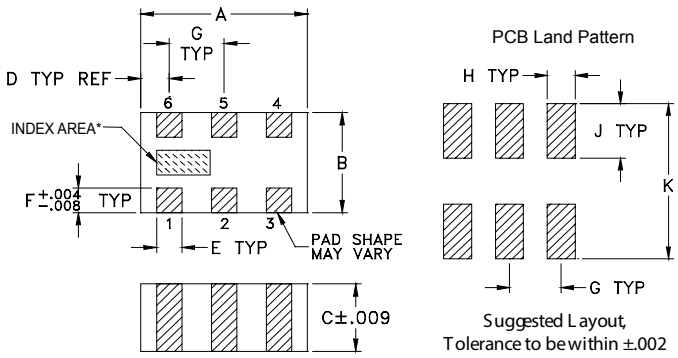


PAD CONNECTIONS

PRIMARY DOT (Unbalanced Port)	1
PRIMARY (GND)	2
SECONDARY DOT (Balanced)	4
SECONDARY (Balanced)	3
NO CONNECTION	6
NOT USED (GND Externally)	5

Pads 2,3,4 are DC-connected internally

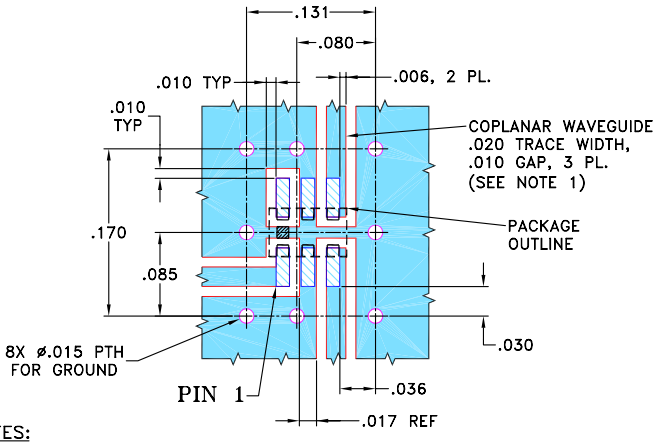
OUTLINE DRAWING



*Shape of index marking may vary

PRODUCT MARKING: N/A

DEMO BOARD MCL P/N: TB-419+
SUGGESTED PCB LAYOUT (PL-264)



NOTES:

1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .010" ± .001". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

OUTLINE DIMENSIONS (Inches / mm)

A	B	C	D	E	F
.079	.049	.033	.014	.012	.012
2.01	1.24	0.84	0.36	0.30	0.30

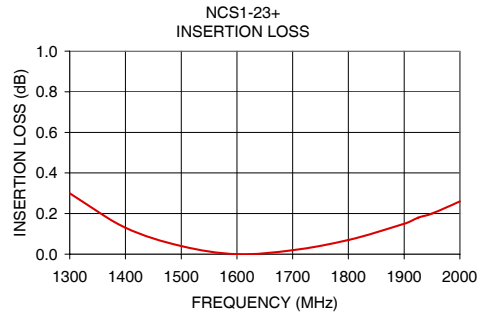
G	H	J	K		wt
.026	.014	.039	.110		grams
0.66	0.36	1.00	2.80		.008

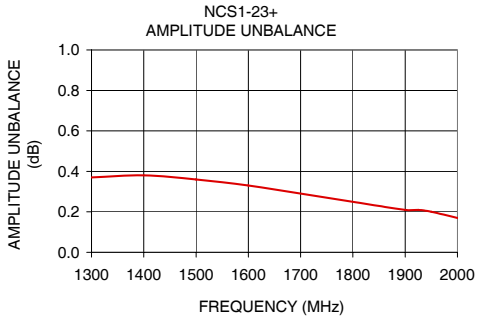
TAPE & REEL INFORMATION: F74

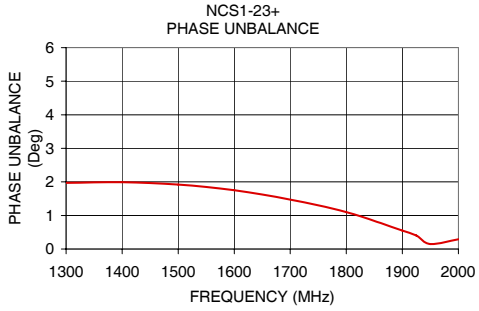
TYPICAL PERFORMANCE DATA[3]

Frequency (MHz)	Insertion Loss (dB)	Input Return Loss (dB)	Amplitude Unbalance (dB)	Phase Unbalance (deg)
1300	0.30	11.79	0.37	1.97
1400	0.13	14.68	0.38	1.99
1500	0.04	17.97	0.36	1.92
1600	0.00	20.16	0.33	1.75
1700	0.02	19.06	0.29	1.47
1800	0.07	16.51	0.25	1.10
1900	0.15	14.30	0.21	0.55
1925	0.18	13.75	0.21	0.40
1950	0.20	13.29	0.20	0.15
2000	0.26	12.51	0.17	0.29

3. Measured with Agilent E5071B network analyzer using impedance conversion and port extension.







NOTES

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the standard. Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp

RF Transformer NCS1-23+

Typical Performance Data

FREQUENCY (MHz)	AVERAGE INSERTION LOSS* (dB)	INPUT RETURN LOSS (dB)	AMPLITUDE UNBALANCE (dB)	PHASE UNBALANCE** (deg)
700	4.35	2.41	0.39	1.58
750	3.70	2.84	0.38	1.62
800	3.12	3.34	0.37	1.75
850	2.63	3.86	0.37	1.73
900	2.19	4.47	0.37	1.94
950	1.81	5.11	0.36	1.88
1000	1.48	5.84	0.35	1.95
1050	1.20	6.62	0.37	1.94
1100	0.95	7.46	0.37	2.08
1150	0.74	8.42	0.39	1.94
1200	0.56	9.44	0.37	1.96
1250	0.42	10.59	0.38	1.95
1300	0.30	11.79	0.37	1.97
1325	0.25	12.47	0.36	1.97
1350	0.20	13.21	0.37	2.02
1375	0.17	13.94	0.38	2.04
1400	0.13	14.68	0.38	1.99
1425	0.10	15.46	0.36	1.95
1450	0.08	16.32	0.35	1.94
1475	0.05	17.19	0.36	1.93
1500	0.04	17.97	0.36	1.92
1525	0.03	18.67	0.34	1.90
1550	0.01	19.34	0.33	1.86
1575	0.00	19.89	0.32	1.81
1600	0.00	20.16	0.33	1.75
1625	0.00	20.17	0.32	1.70
1650	0.00	20.03	0.30	1.63
1675	0.01	19.65	0.29	1.55
1700	0.02	19.06	0.29	1.47
1725	0.03	18.39	0.28	1.41
1750	0.04	17.79	0.26	1.29
1775	0.06	17.19	0.25	1.19
1800	0.07	16.51	0.25	1.10
1825	0.08	15.87	0.25	1.00
1850	0.11	15.33	0.24	0.86
1875	0.13	14.84	0.22	0.68
1900	0.15	14.30	0.21	0.55
1925	0.18	13.75	0.21	0.40
1950	0.20	13.29	0.20	0.15
1975	0.23	12.90	0.19	0.09
2000	0.26	12.51	0.17	0.29
2050	0.31	11.71	0.19	0.60
2100	0.38	11.11	0.17	1.12
2200	0.52	9.95	0.19	2.00
2300	0.66	9.01	0.23	3.06
2400	0.81	8.23	0.29	4.13
2500	0.96	7.62	0.35	5.08
2600	1.12	7.13	0.43	6.11
3000	1.66	6.01	0.77	10.01
3500	2.14	5.83	1.09	18.87

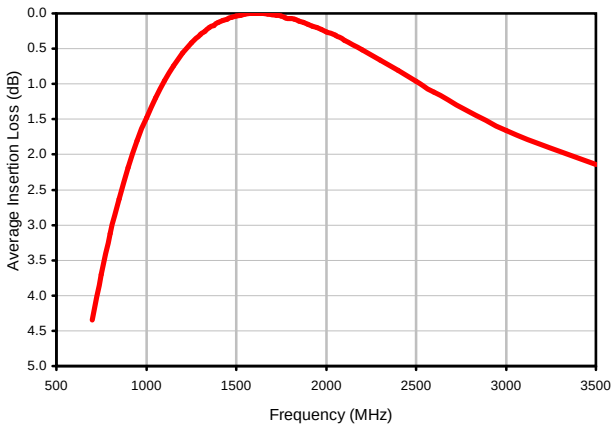
* Insertion Loss is referenced to mid-band loss , 0.7 dB.

**Phase Unbalance is relative to 180˚

RF Transformer NCS1-23+

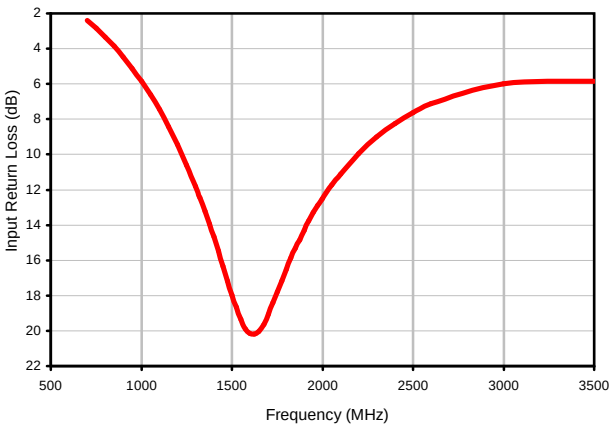
Typical Performance Curves

Average Insertion Loss*

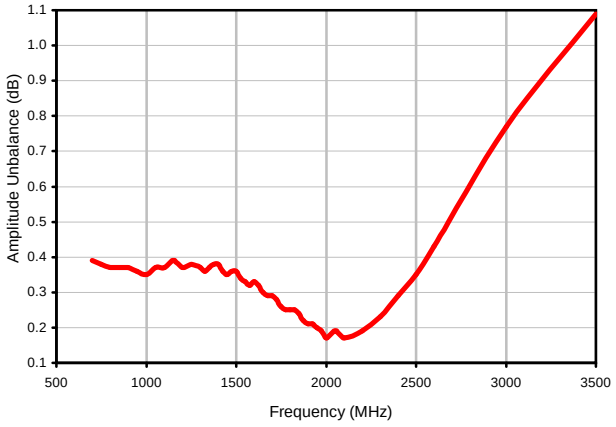


* Insertion Loss is referenced to mid-band loss , 0.7 dB.

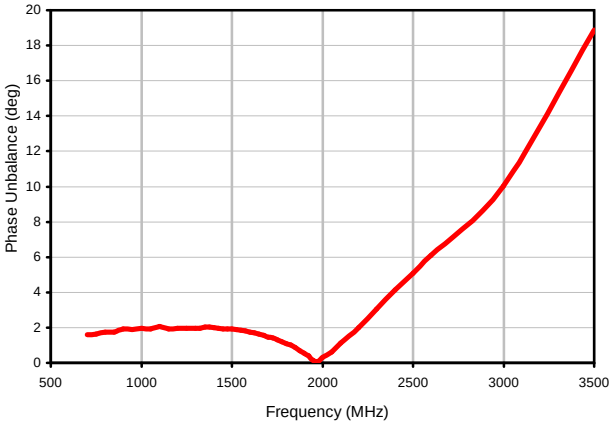
Input Return Loss



Amplitude Unbalance



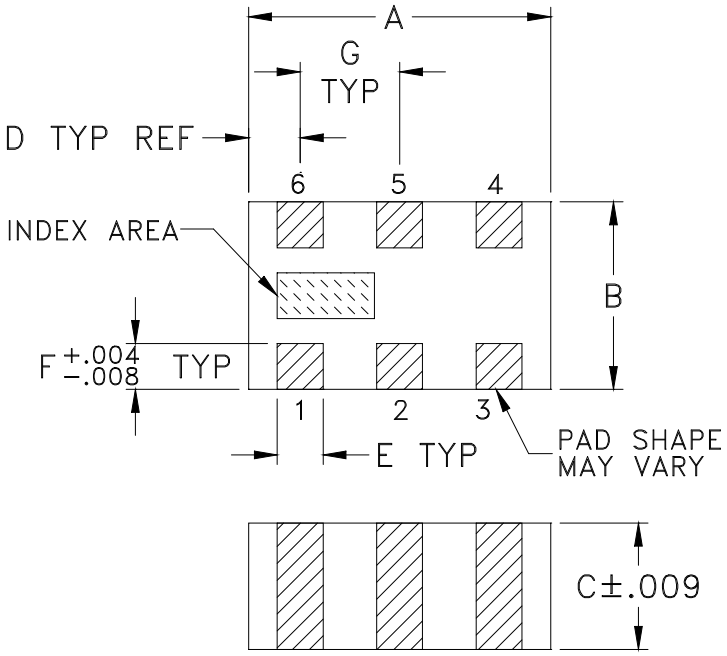
Phase Unbalance**



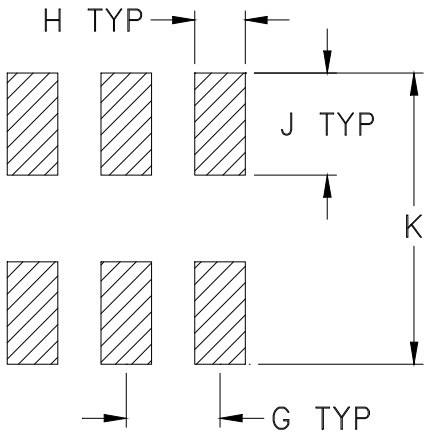
**Phase Unbalance is relative to 180˚

GE0805C-1

Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE #	A	B	C	D	E	F	G	H	J	K	WT. GRAM
GE0805C-1	.079 (2.00)	.049 (1.25)	.033 (0.84)	.014 (0.35)	.012 (0.30)	.012 (0.30)	.026 (0.65)	.014 (0.35)	.039 (1.00)	.110 (2.80)	.008

Dimensions are in inches (mm). Tolerances: 2Pl. ± .01; 3 Pl. ± .005

Notes:

1. Open style, ceramic base.
2. Termination finish: For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
   For RoHS-5 Case Style: Tin-lead plate. All models, no (+) suffix.

This document and its contents are the property of Mini-Circuits.

Tape & Reel Packaging TR-F74

DEVICE ORIENTATION IN T&R

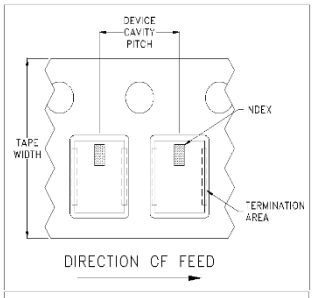


ILLUSTRATION 1

Applicable Case Styles
GE0805C-1
GE0805C-1AP
JV1210C-1
GU2939

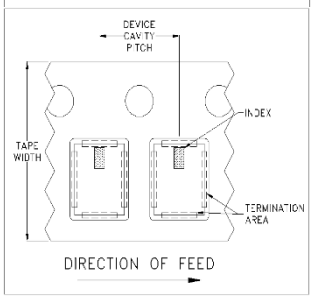


ILLUSTRATION 2

Applicable Case Styles
JV1210C
JV1210C-2
JV1210C-3
JV1210C-4
JV1210C-5
JV1210C-6
JV1210C-11

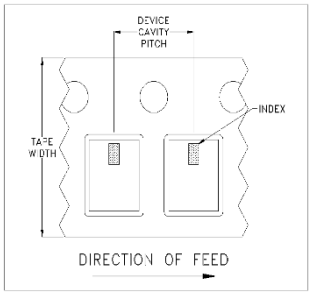


ILLUSTRATION 3

Applicable Case Styles
JC0603C-8
JC0603C-9
JV1210C-7
JV1210C-8
JV1210C-9
JV1210C-10
JV1210C-13
GE0805C-13
GE0805C-19
GE0805C-20

Tape Width, mm	Device Cavity Pitch, mm	Real Size, inches	Devices per Reel	
8	4	7	Small quantity standards **(see note)**	20
				50
				100
				200
				500
				1000
			Standard	2000
				4000

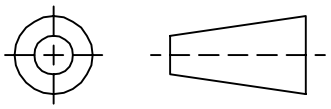
Note: Small reel availability varies by model. Refer to pricing and availability on individual model dashboard.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.
Go to: www.minicircuits.com/pages/pdfs/tape.pdf

Mini-Circuits®
INTERNET http://www.minicircuits.com
P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661
Distribution Centers NORTH AMERICA 800-654-7949 • 417-335-5935 • Fax 417-335-5945 • EUROPE 44-1252-832600 • Fax 44-1252-837010
Mini-Circuits ISO 9001 & ISO 14001 Certified

This document and its contents are the property of Mini-Circuits.

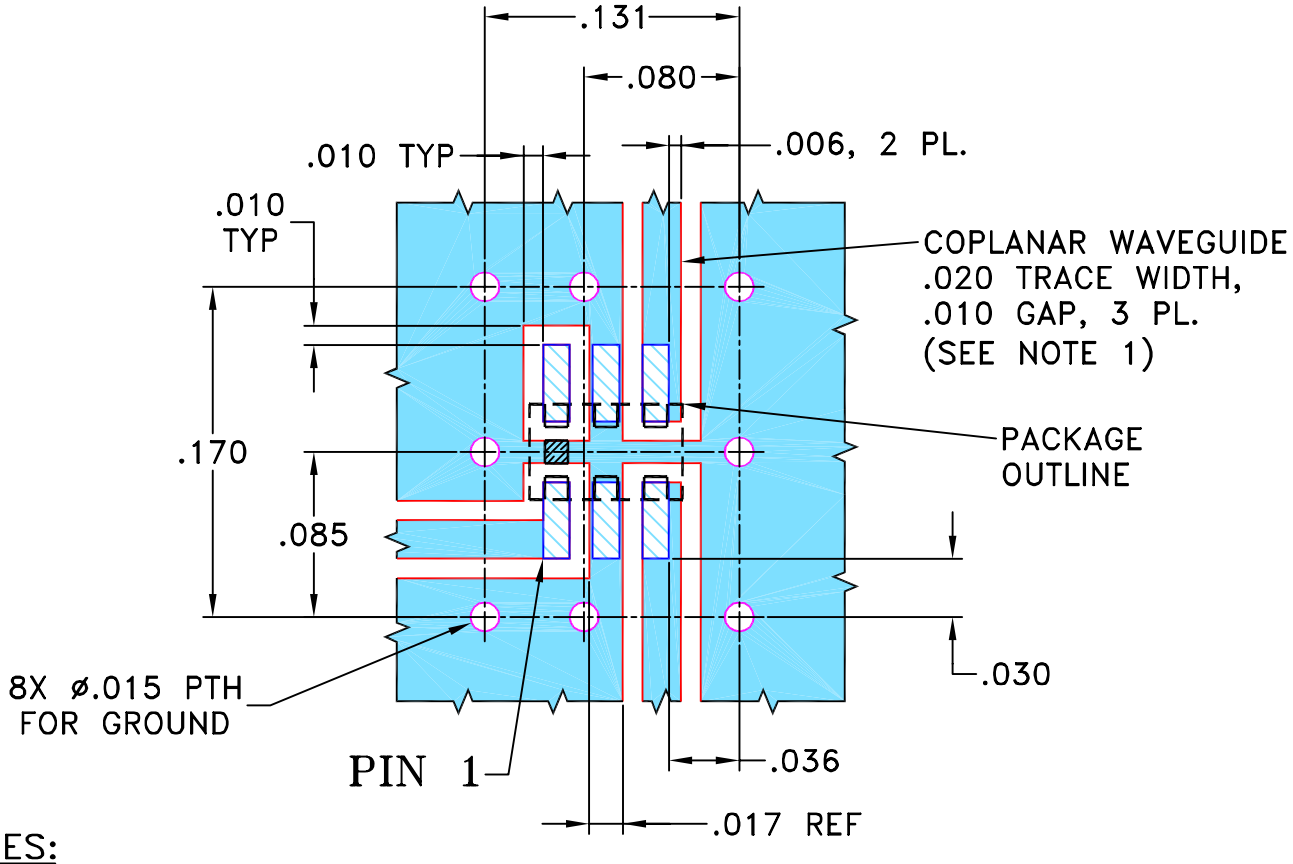
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M109549	NEW RELEASE	01/31/07	PW	DJ

SUGGESTED MOUNTING CONFIGURATION FOR GE0805C-1 CASE STYLE, "ry" PIN CONNECTION.



NOTES:

1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .010" ± .001". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	PW	01/30/07
TOLERANCES ON:	CHECKED	IL	01/31/07
2 PL DECIMALS ±	APPROVED	DJ	01/31/07
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

Mini-Circuits ®
THIS DOCUMENT AND ITS CONTENTS ARE THE PROPERTY OF MINI-CIRCUITS. EXCEPT FOR USE EXPRESSLY GRANTED, IN WRITING, TO ITS VENDORS, VENDEE AND THE UNITED STATES GOVERNMENT, MINI-CIRCUITS RESERVES ALL PROPRIETARY DESIGN, USE , MANUFACTURING AND REPRODUCTION RIGHTS THERETO. THESE CONTENTS SHALL NOT BE USED, DUPLICATED OR DISCLOSED TO ANY OUTSIDE PARTY, IN WHOLE OR IN PART, WITHOUT WRITTEN PERMISSION OF MINI-CIRCUITS.

ASHEETA1.DWG REV:A DATE:01/12/95

Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

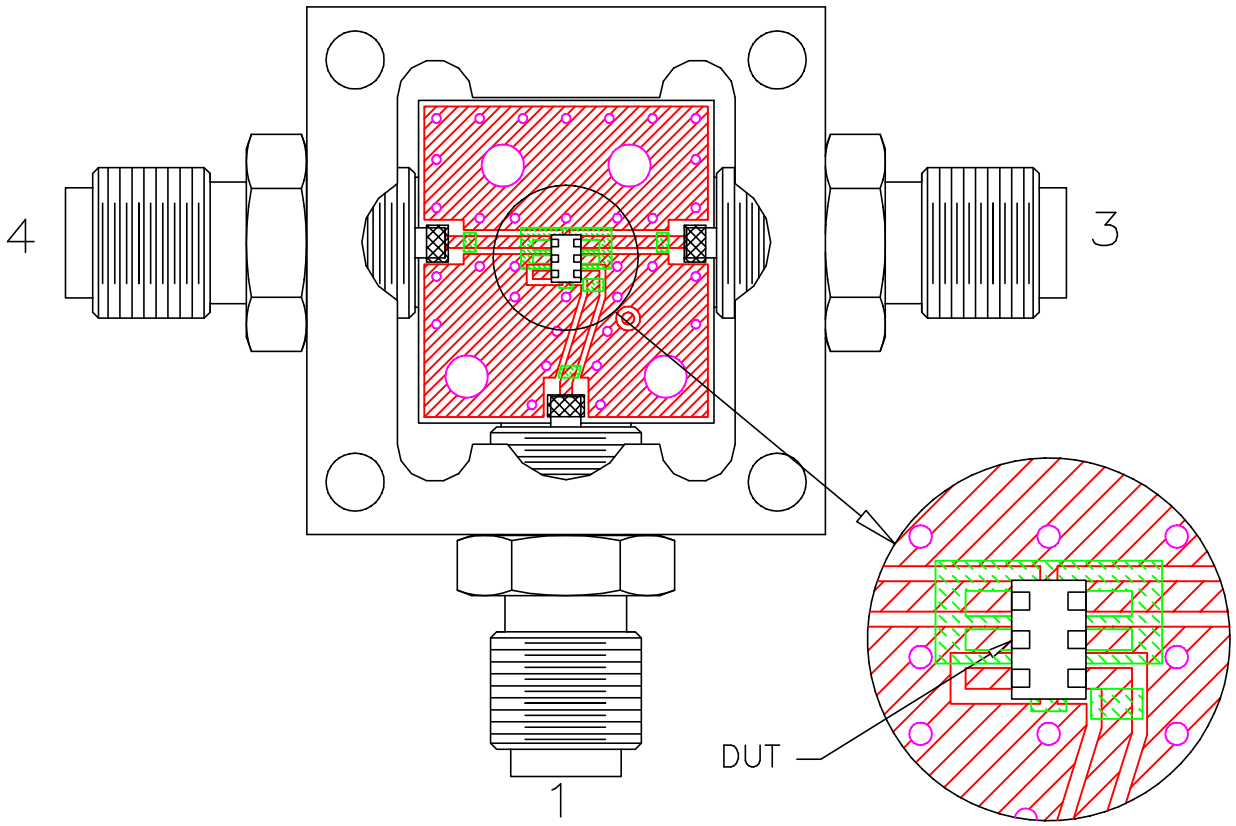
PL, ry, GE0805C-1, NCS, TB-419+

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-264	OR

FILE:	SCALE:	SHEET:
98PL264	10:1	1 OF 1

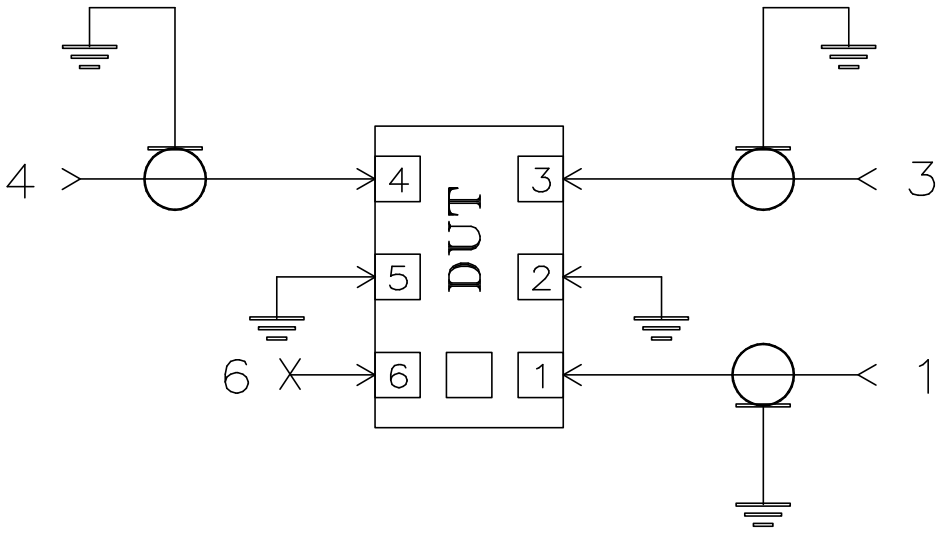
Evaluation Board and Circuit

For Pin Connections refer to Data Sheet of the DUT



TB-419+

DETAIL
(SCALE 5:1)



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.010 inch.

Mini-Circuits®

THIS DOCUMENT AND ITS CONTENTS ARE THE PROPERTY OF MINI-CIRCUITS.

Mini-Circuits

Environmental Specifications ENV06T2

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A

This document and its contents are the property of Mini-Circuits.