

Engineering Development Model

RF Transformer

NCS1-ED12817/34B30

Impedance Ratio : 1

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

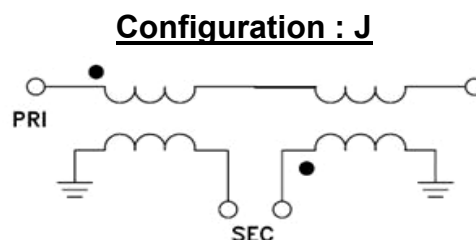
CASE STYLE: GE0805C-1

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter	Min.	Typ.	Max.	Units
Frequency	5050		5875	MHz
Insertion Loss *	1 dB Bandwidth		5050 - 5875	MHz

Notes: * Insertion Loss is referenced to mid-band loss, 1.29dB typ.

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Input RF Power **	3 W

** Derate linearly to 2W at 85°C



PIN CONNECTIONS	
PRIMARY DOT (Unbalanced)	1
PRIMARY (GND)	2
SECONDARY DOT (Balanced)	4
SECONDARY (Balanced)	3
NO CONNECTION	6
NOT USED (GND Externally)	5

Pads 2,3,4 are DC-connected internally.

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Typical Performance Data

FREQUENCY (MHz)	INSERTION LOSS (dB)	RETURN LOSS (dB)
3800	4.86	3.06
3900	4.51	3.33
4000	4.17	3.64
4100	3.84	3.99
4200	3.53	4.38
4300	3.23	4.82
4400	2.95	5.31
4500	2.69	5.87
4600	2.45	6.49
4700	2.23	7.17
4800	2.03	7.95
4900	1.86	8.78
4925	1.82	9.01
4950	1.78	9.21
4975	1.74	9.44
5000	1.71	9.70
5025	1.67	9.94
5050	1.64	10.17
5075	1.61	10.41
5100	1.58	10.67
5125	1.55	10.91
5150	1.52	11.16
5175	1.50	11.42
5200	1.48	11.66
5225	1.46	11.91
5250	1.44	12.14
5300	1.40	12.63
5350	1.37	13.11
5400	1.34	13.58
5450	1.33	13.93
5500	1.31	14.24
5550	1.31	14.60
5600	1.29	14.84
5650	1.30	14.93
5700	1.30	14.97
5750	1.31	15.01
5800	1.33	14.99
5850	1.35	14.85
6000	1.43	14.60
6200	1.60	14.36
6400	1.82	14.82
6600	1.93	16.24
6800	1.79	18.97
7000	1.48	24.01
7200	1.23	29.00
7400	1.18	25.77
7600	1.28	23.52
7800	1.45	23.44
8000	1.59	24.70
8100	1.65	25.56
8200	1.70	26.54
8300	1.72	27.11
8400	1.75	26.48
8500	1.78	25.29



Typical Performance Curves



Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within ± 0.002

CASE #	A	B	C	D	E	F	G	H	J	K	WT. GRAM
GE0805C-1	.079 (2.00)	.049 (1.25)	.033 (0.84)	.014 (0.35)	.012 (0.30)	.012 (0.30)	.026 (0.65)	.014 (0.35)	.039 (1.00)	.110 (2.80)	.008

Dimensions are in inches (mm). Tolerances: 2Pl. $\pm .01$; 3 Pl. $\pm .005$

Notes:

- Open style, ceramic base.
- Termination finish: For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
For RoHS-5 Case Style: Tin-lead plate. All models, no (+) suffix.



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F74

DEVICE ORIENTATION IN T&R

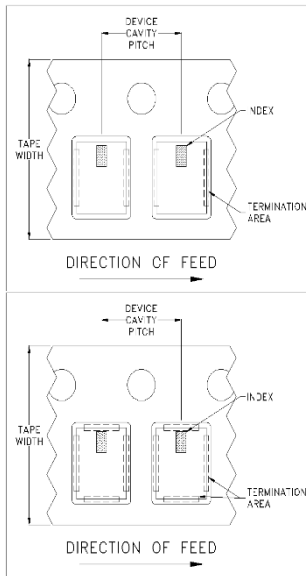


ILLUSTRATION 1

Applicable Case Styles

GE0805C-1
GE0805C-1AP
JV1210C-1
GU2939

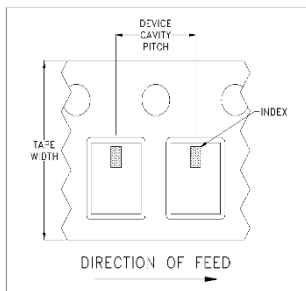


ILLUSTRATION 2

Applicable Case Styles

JV1210C
JV1210C-2
JV1210C-3
JV1210C-4
JV1210C-5
JV1210C-6
JV1210C-11

ILLUSTRATION 3

Applicable Case Styles

JC0603C-8
JC0603C-9
JV1210C-7
JV1210C-8
JV1210C-9
JV1210C-10
JV1210C-13
GE0805C-13
GE0805C-19
GE0805C-20

Tape Width, mm	Device Cavity Pitch, mm	Real Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	2000
				4000

Note: Small reel availability varies by model. Refer to pricing and availability on individual model dashboard.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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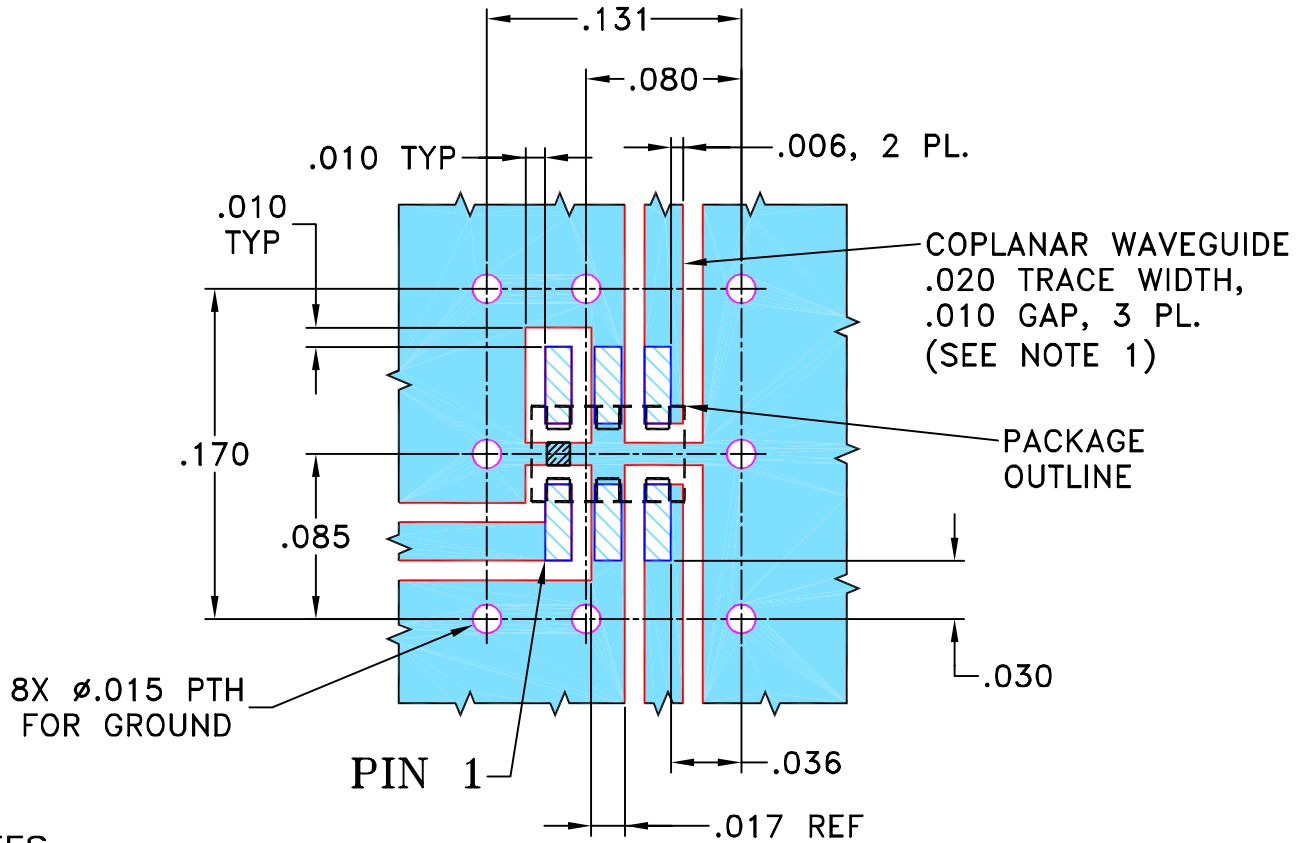
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M109549	NEW RELEASE	01/31/07	PW	DJ

SUGGESTED MOUNTING CONFIGURATION
FOR GE0805C-1 CASE STYLE, "ry" PIN CONNECTION.

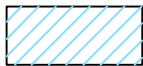


NOTES:

1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS R04350B WITH DIELECTRIC THICKNESS .010" ± .001". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED	INITIALS		DATE
DIMENSIONS ARE IN INCHES TOLERANCES ON: 2 PL DECIMALS ± 3 PL DECIMALS ± .005 ANGLES ± FRACTIONS ±	DRAWN	PW	01/30/07
	CHECKED	IL	01/31/07
	APPROVED	DJ	01/31/07



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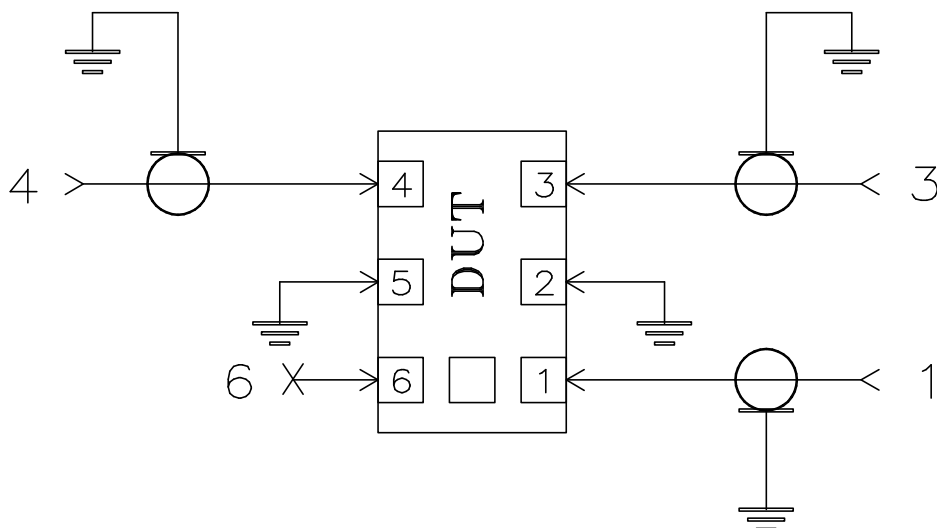
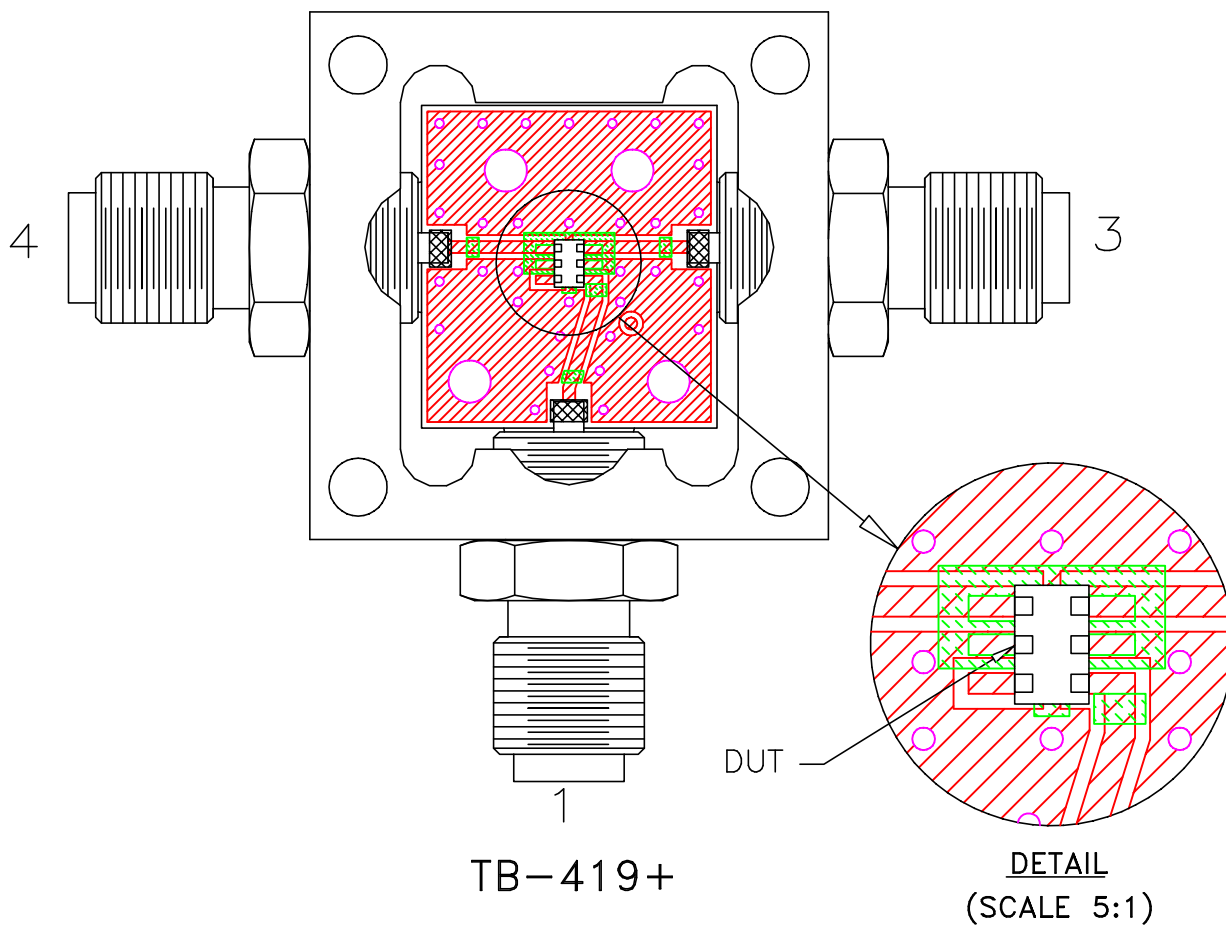
PL, ry, GE0805C-1, NCS, TB-419+

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SIZE A	CODE IDENT 15542	DRAWING NO: 98-PL-264	REV: OR
FILE: 98PL264	SCALE: 10:1	SHEET: 1 OF 1	

Evaluation Board and Circuit


For Pin Connections refer to Data Sheet of the DUT



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent, Dielectric Constant=3.5, Thickness=.010 inch.

 **Mini-Circuits®**

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A