Engineering Development Model

RF Transformer NCS2-ED12817/34B23

Impedance Ratio : 2

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE: GE0805C-1

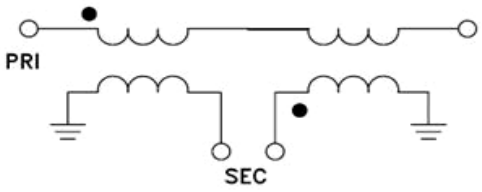
ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		1550		2750	MHz
Insertion Loss *	**1 dB Bandwidth**		1550 - 2750		MHz

Note: * Insertion Loss is referenced to mid-band loss, 0.6dB typ.

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Input RF Power **	3 W

**** Derate linearly to 2W at 85°C

Configuration : J



PIN CONNECTIONS	
PRIMARY DOT (Unbalanced)	1
PRIMARY (GND)	2
SECONDARY DOT (Balanced)	4
SECONDARY (Balanced)	3
NO CONNECTION	6
NOT USED (GND Externally)	5

Pads 2,3,4 are DC-connected internally.

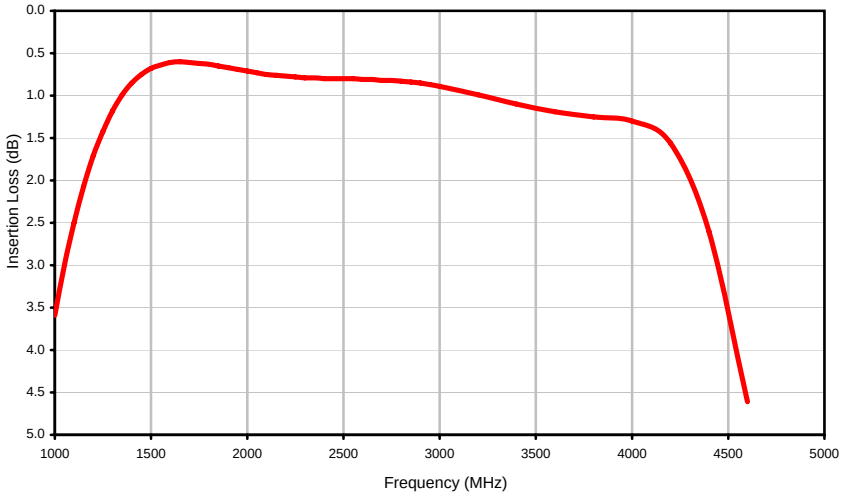
RF Transformer NCS2-ED12817/34B23

Typical Performance Data

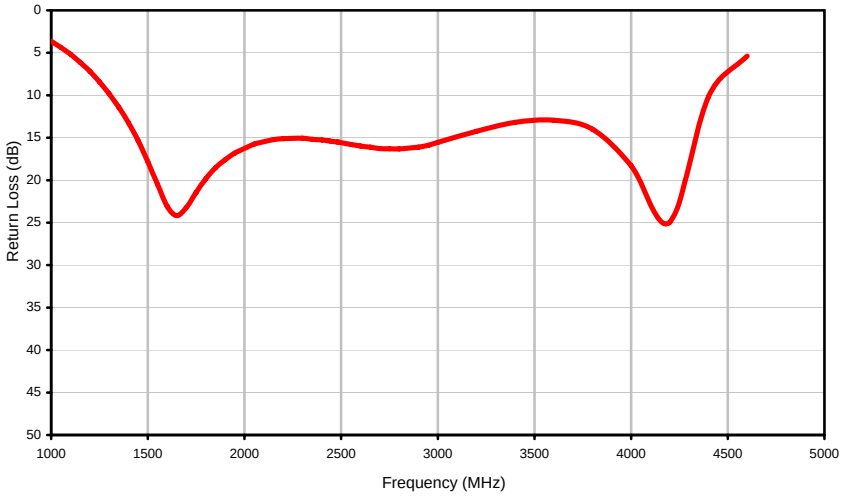
FREQUENCY (MHz)	INSERTION LOSS (dB)	RETURN LOSS (dB)
1000	3.59	3.63
1050	3.00	4.35
1100	2.50	5.17
1150	2.07	6.12
1200	1.71	7.18
1250	1.42	8.43
1300	1.18	9.82
1350	0.99	11.42
1400	0.85	13.23
1450	0.75	15.36
1500	0.68	17.83
1550	0.64	20.50
1600	0.61	23.06
1650	0.60	24.17
1700	0.61	23.19
1750	0.62	21.44
1800	0.63	19.82
1850	0.65	18.54
1900	0.67	17.59
1950	0.69	16.80
2000	0.71	16.25
2050	0.73	15.75
2100	0.75	15.47
2150	0.76	15.22
2200	0.77	15.10
2250	0.78	15.07
2300	0.79	15.06
2350	0.79	15.18
2400	0.80	15.26
2408	0.80	15.28
2420	0.80	15.32
2428	0.80	15.33
2440	0.80	15.37
2460	0.80	15.45
2480	0.80	15.50
2488	0.80	15.53
2500	0.80	15.58
2550	0.80	15.79
2600	0.81	15.97
2650	0.81	16.11
2700	0.82	16.27
2750	0.82	16.30
2800	0.83	16.32
2850	0.84	16.23
2900	0.85	16.10
2950	0.87	15.90
3000	0.89	15.55
3200	0.99	14.25
3400	1.10	13.19
3600	1.19	12.95
3800	1.25	13.99
4000	1.30	18.29
4200	1.56	24.97
4400	2.60	10.17
4600	4.61	5.38

Typical Performance Curves

Insertion Loss

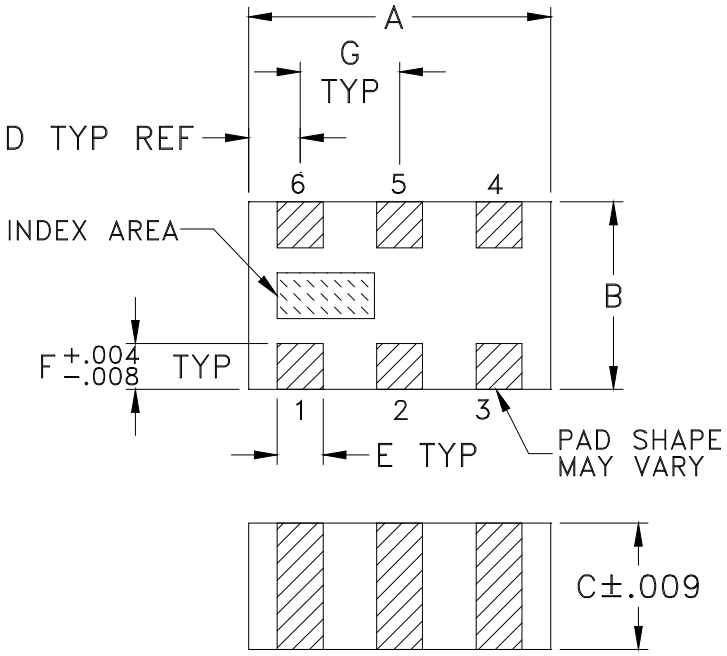


Return Loss

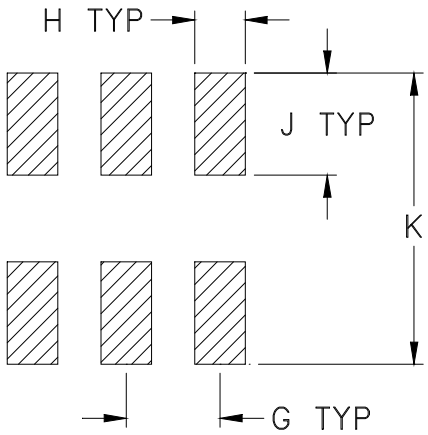


GE0805C-1

Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE #	A	B	C	D	E	F	G	H	J	K	WT. GRAM
GE0805C-1	.079 (2.00)	.049 (1.25)	.033 (0.84)	.014 (0.35)	.012 (0.30)	.012 (0.30)	.026 (0.65)	.014 (0.35)	.039 (1.00)	.110 (2.80)	.008

Dimensions are in inches (mm). Tolerances: 2Pl. ± .01; 3 Pl. ± .005

Notes:

1. Open style, ceramic base.
2. Termination finish: For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
   For RoHS-5 Case Style: Tin-lead plate. All models, no (+) suffix.

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Tape & Reel Packaging TR-F74

DEVICE ORIENTATION IN T&R

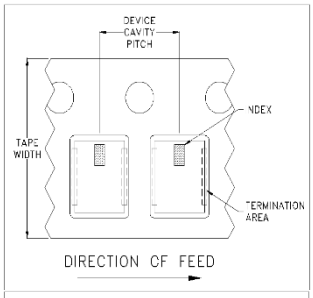

ILLUSTRATION 1

Applicable Case Styles
GE0805C-1
GE0805C-1AP
JV1210C-1
GU2939

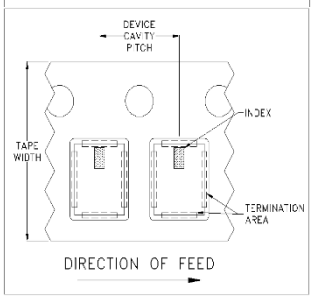

ILLUSTRATION 2

Applicable Case Styles
JV1210C
JV1210C-2
JV1210C-3
JV1210C-4
JV1210C-5
JV1210C-6
JV1210C-11

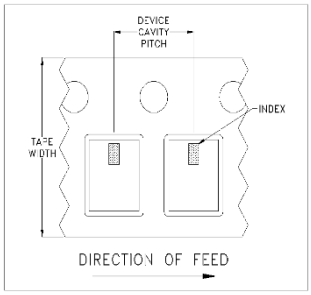

ILLUSTRATION 3

Applicable Case Styles
JC0603C-8
JC0603C-9
JV1210C-7
JV1210C-8
JV1210C-9
JV1210C-10
JV1210C-13
GE0805C-13
GE0805C-19
GE0805C-20

Tape Width, mm	Device Cavity Pitch, mm	Real Size, inches	Devices per Reel	
8	4	7	Small quantity standards **(see note)**	20
				50
				100
				200
				500
				1000
			Standard	2000
				4000

Note: Small reel availability varies by model. Refer to pricing and availability on individual model dashboard.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.
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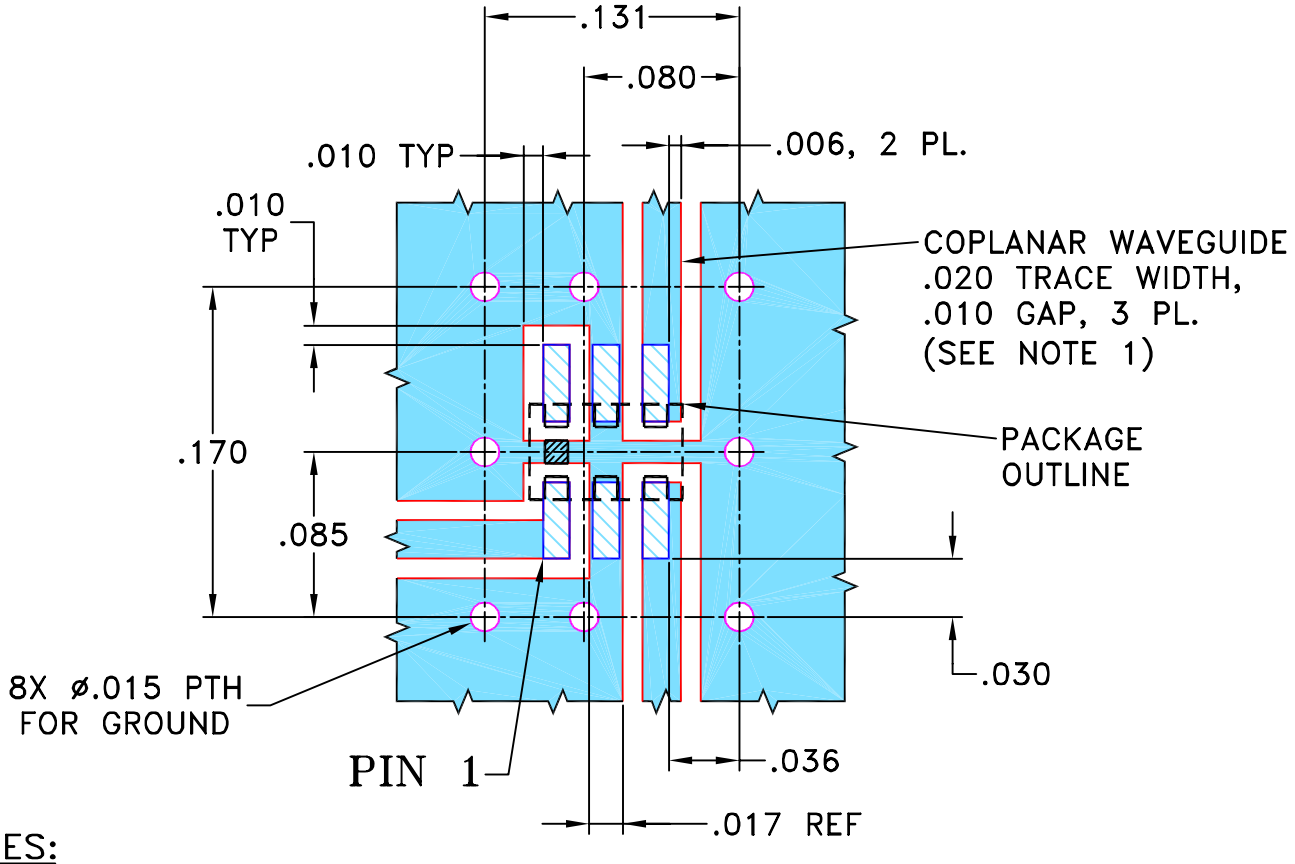
THIRD ANGLE PROJECTION

REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M109549	NEW RELEASE	01/31/07	PW	DJ

SUGGESTED MOUNTING CONFIGURATION FOR GE0805C-1 CASE STYLE, "ry" PIN CONNECTION.



NOTES:

1. COPLANAR WAVEGUIDE PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .010" ± .001". COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER).

DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	PW	01/30/07
TOLERANCES ON:	CHECKED	IL	01/31/07
2 PL DECIMALS ±	APPROVED	DJ	01/31/07
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

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ASHEETA1.DWG REV:A DATE:01/12/95

Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

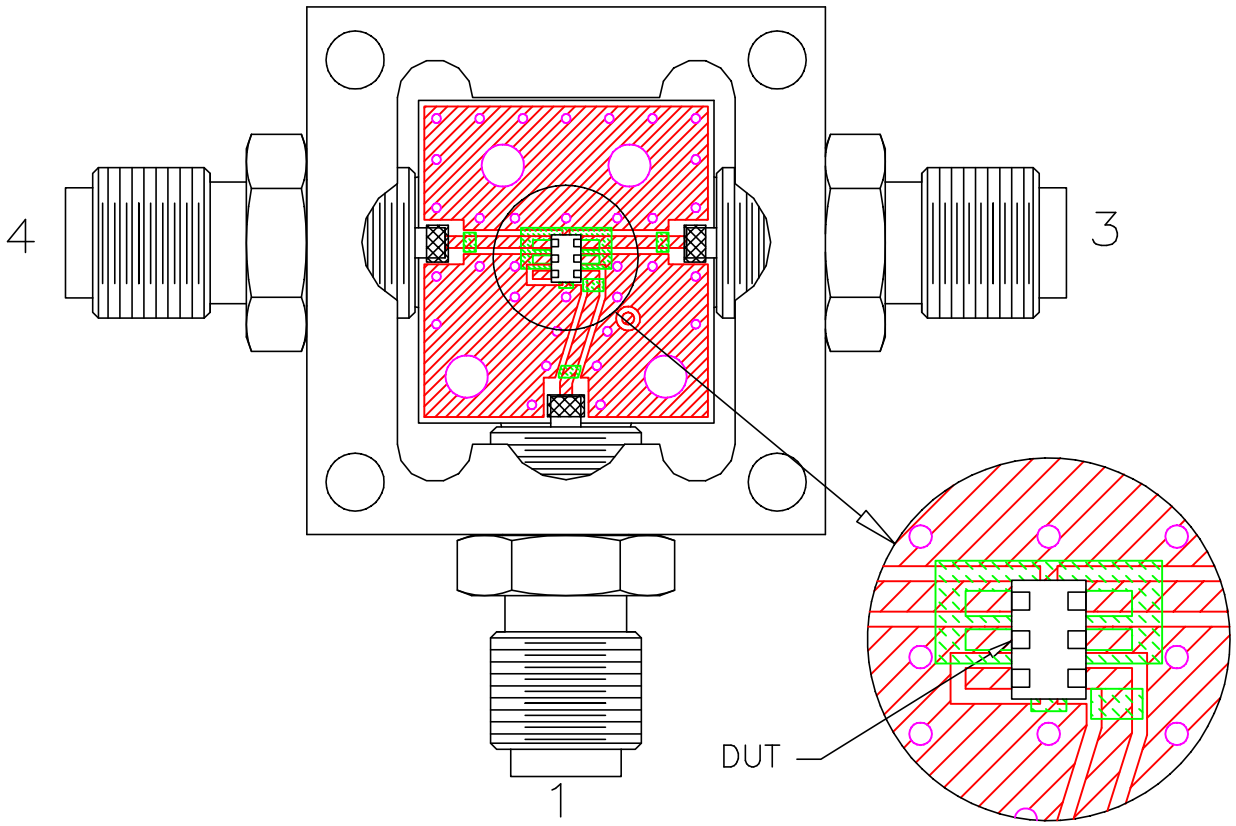
PL, ry, GE0805C-1, NCS, TB-419+

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-264	OR

FILE:	SCALE:	SHEET:
98PL264	10:1	1 OF 1

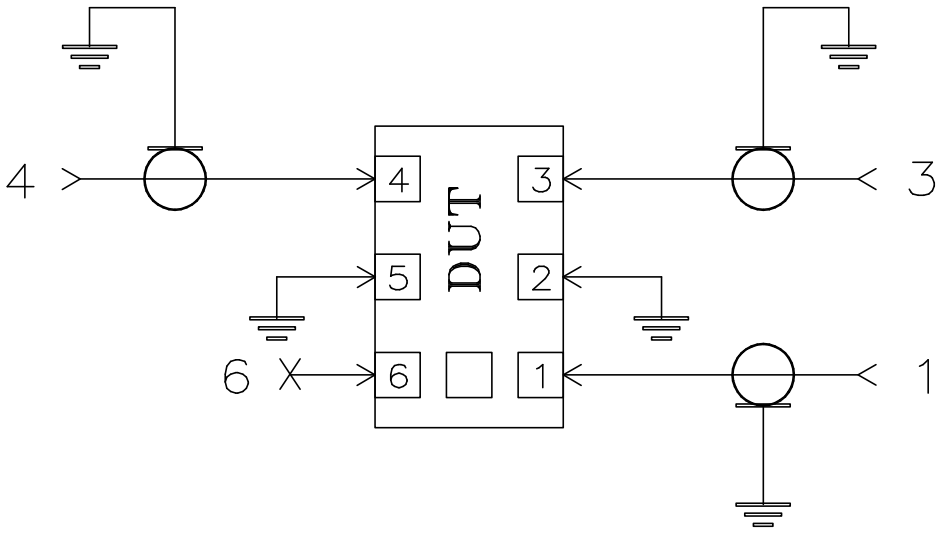
Evaluation Board and Circuit

For Pin Connections refer to Data Sheet of the DUT



TB-419+

DETAIL
(SCALE 5:1)



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.010 inch.

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Environmental Specifications ENV06T2

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A

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