

50Ω 1 W 20 dB DC to 7 GHz

FEATURES

- Wideband, DC to 7 GHz
- Excellent VSWR Through Entire Band
- Miniature Size
- Aqueous Washable

APPLICATIONS

- Power Leveling
- Impedance Match Improvement



PAT-20+

Generic photo used for illustration purposes only CASE STYLE: AF320

+RoHS Compliant The +Suffix identifies RoHS Compliance. our website for methodologies and qualific

ELECTRICAL SPECIFICATIONS AT +25°C

Parameter	Condition (GHz)	Min.	Тур.	Max.	Unit
Frequency Range		DC		7	GHz
Attenuation, Nominal			20±1.3		dB
	DC - 1			0.5	
Attenuation, Flatness ¹	DC - 2.5			0.7	dB
	DC - 7			2.6	
	DC - 1			1.4	
VSWR	DC - 2.5			1.4	:1
	DC - 7			1.5	
Input Power ²				1.0	W

1. Flatness = variation over band divided by 2.

2. RF power at +25°C case temperature: 1 Watt. Derate linearly to 0.1 Watt at +100°C.

ABSOLUTE MAXIMUM RATINGS

Parameter	Ratings
Operating Temperature	-55°C to +100°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded.

REV. J ECO-024219 PAT-20+ MCL NY 250114



MINIATURE CERAMIC

Fixed Attenuator



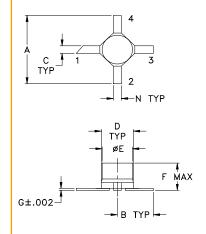
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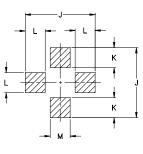
50Ω 1 W 20 dB DC to 7 GHz

PIN CONNECTIONS

INPUT	1
OUTPUT	3
GROUND	2,4

OUTLINE DRAWING





PCB Land Pattern

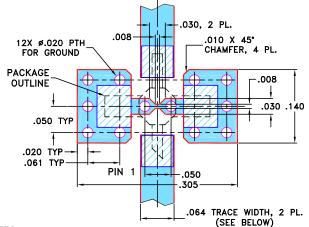
Suggested Layout, Tolerance to be within ±.002

OUTLINE DIMENSIONS (Inch)

A	B	C	D	E	F	G
.200	.100	.020	.070	.068	.057	.005
5.08	2.54	0.51	1.78	1.73	1.45	0.13
H	J	K	L	M	N	wt
	.230	.065	.060	.080	.040	grams
	5.84	1.65	1.52	2.03	1.02	0.04

TAPE & REEL INFORMATION: F26

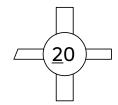
DEMO BOARD MCL P/N: TB-319 SUGGESTED PCB LAYOUT (PL-208)



NOTES:

- TRACE WIDTH IS SHOWN FOR ROGERS R04350B WITH DIELECTRIC THICKNESS .030" ± .002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
 BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
- BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE. DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
 - DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

PRODUCT MARKING



Marking may contain other features or characters for internal lot control

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MINIATURE CERAMIC

Fixed Attenuator

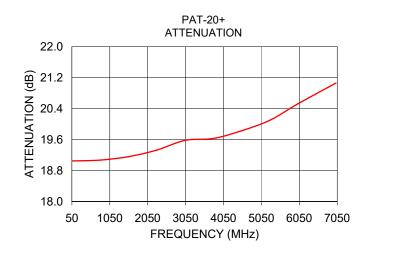


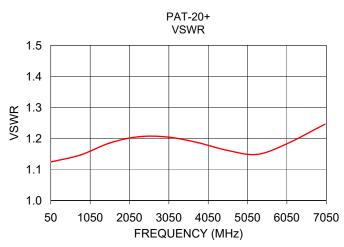
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 50Ω 1 W 20 dB DC to 2.5 GHz

TYPICAL PERFORMANCE DATA AND CHARTS

Frequency (MHz)	Attenuation (dB)	VSWR (:1)
50.00	19.05	1.12
796.25	19.07	1.15
1542.50	19.16	1.19
2288.75	19.33	1.21
3035.00	19.58	1.20
3781.25	19.63	1.19
4527.50	19.83	1.16
5273.75	20.10	1.15
6020.00	20.53	1.18
7015.00	21.06	1.25





NOTES

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.

B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
 C. The parts covered by this specification document are subject to Mini-Circuits' standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and

benefits contained therein. For a full statement of the standard terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/terms/viewterm.html

Fixed Attenuator

Typical Performance Data

FREQUENCY (MHz)	ATTENUATION (dB)	RETURN LOSS (dB)
50.00	19.05	24.65
796.25	19.07	23.32
1542.50	19.16	21.44
2288.75	19.33	20.61
3035.00	19.58	20.66
3781.25	19.63	21.37
4527.50	19.83	22.52
5273.75	20.10	23.21
6020.00	20.53	21.61
7015.00	21.06	19.22



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 IF/RF MICROWAVE COMPONENTS • ISO 9001 ISO 14001 AS 9100 CERTIFIED O RoHS compliant
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 Iminicipatise.com

 Patent Pending
 The Design Engineers Search Engine finds the model you need, Instantly • For detailed performance specs & shopping online see
 Iminicipatise.com

Fixed Attenuator

Typical Performance Curves

23 24 25

0

500

1000

1500

2000

2500

3000 3500

Frequency (MHz)

4000

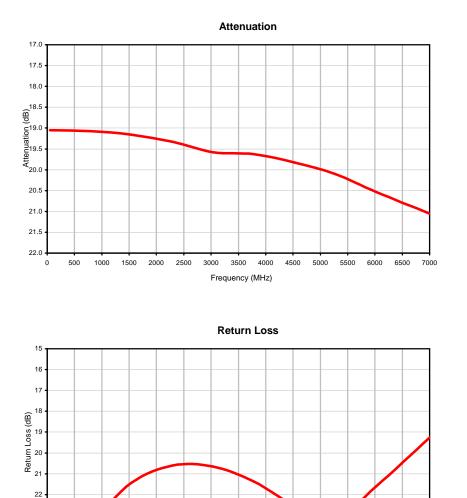
4500

5000

5500

6000

6500 7000



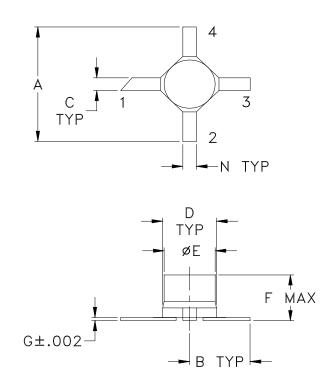


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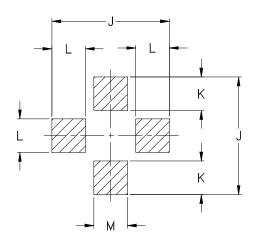
Case Style

Outline Dimensions



PCB Land Pattern

AF320



Suggested Layout, Tolerance to be within ±.002

CASE #	А	В	С	D	Е	F	G	Н	J	K	L	М	Ν	WT. GRAM
AF320	.200 (5.08)	.100 (2.54)	.020 (0.51)	.070 (1.78)	.068 (1.73)	.057 (1.45)	.005 (0.13)	-	.230 (5.84)	.065 (1.65)	.060 (1.52)	.080 (2.03)	.040 (1.02)	.04

Dimensions are in inches (mm). Tolerances: 2Pl. ±.03; 3Pl. ±.015

Notes:

- 1. Case material: Ceramic.
- 2. Termination material:

Nickel-Iron alloy 42.

3. Termination finish:

For RoHS Case Styles: Tin-Silver alloy plate over Nickel barrier or Matte-Tin. (See Data Sheet) For RoHS-5 Case Styles: Tin-Lead plate.

4. Termination (1):

May have diagonal cut. Input and output interchangeable for PAT models only.

5. Special Tolerances: Termination width \pm .005 inch, termination thickness \pm .002 inch,

cap diameter $\pm .005$ inch.



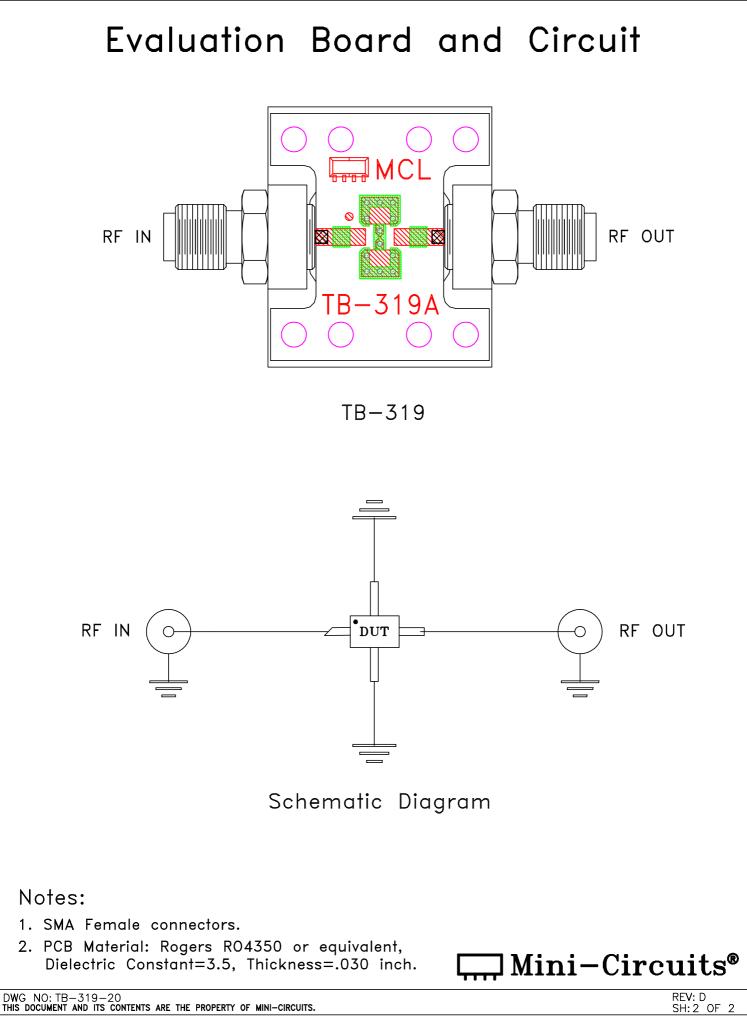
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	<u>AF</u>	<u>-320 CA</u>	<u>SE STY</u>	LES,	<u>"hl" PIN</u>	CONNECTIO	<u>N_</u>			
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2. BOTTOM	SIDE	OF THE	PCB IS	s coi	NTINUOUS	GROUND P	LANE.			
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Mini-Circuits

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-54° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-65° to 150° C Ambient Environment	Individual Model Data Sheet
Autoclave	15 psig, 100% RH, 121°C, 96 hours	JESD22-A102-C, Condition C
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Mechanical Shock	1.5Kg, 0.5 ms, 5 shock pulses, Y1 direction only	MIL-STD-883, Method 2002, Condition B, except Y1 direction only
Vibration (Variable Frequency)	50g peak	MIL-STD-883, Method 2007, Condition B
Constant Acceleration	Y1 plane only, 5 Kg	MIL-STD-883, Method 2001, Condition A, except Y1 plane only
Seal	Perfluorocarbon gross leak	MIL-STD-883, Method 1014, Condition C
HAST	130°C, 85% RH, 96 hours	JESD22-A110
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Solder Reflow Heat	Sn-Pb Eutetic Process: 240°C peak Pb-Free Process: 260°C peak	J-STD-020, Table 4-1, 4-2 and 5-2; Figure 5-1
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Specification	Test/Inspection Condition	Reference/Spec
Moisture Sensitivity: Level 1	Bake at 125°C for 24 hours Soak at 85°C/85% RH for 168 hours, Reflow 3 cycles at 260°C peak	J-STD-020
Marking Resistance to Solvents	lsopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215

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