



Mini-Circuits

ULTRA-SMALL CERAMIC

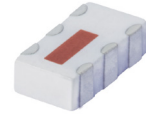
Power Splitter/Combiner

QCN-8AT+

2 Way-90° 50Ω 450 to 750 MHz

FEATURES

- Low insertion loss, 0.6 dB typ.
- Wrap-around terminal for excellent solderability
- Ultra small, 0.12"X0.06"X0.035"
- AEC-Q200 qualified component family



Generic photo used for illustration purposes only

CASE STYLE: FV1206-1

+RoHS Compliant

The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

APPLICATIONS

- Automotive

ELECTRICAL SPECIFICATIONS¹ AT +25°C

Parameter	Frequency (MHz)	Min.	Typ.	Max.	Unit
Frequency Range		450		750	MHz
Insertion Loss, above 3.0 dB	450-750		0.6	0.9	dB
	470-700		0.5	0.8	
Isolation	450-750	13	16		dB
	470-700	13	16		
Phase Unbalance	450-750		5	8	Degree
	470-700		5	8	
Amplitude Unbalance	450-750		0.5	1.0	dB
	470-700		0.3	0.8	
VSWR	450-750		1.4		(:1)
	470-700		1.4		

1. Tested on TB-255+ using QCN-8+ with evaluation board losses de-embedded.

2. For applications requiring DC voltage to be applied to the RF ports, add suffix letter "D" to part no. DC resistance to ground is 100 Mohms min.

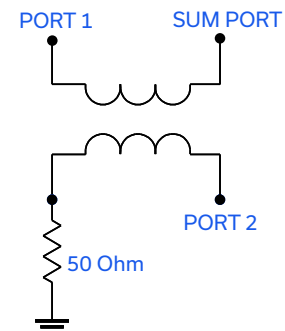
ABSOLUTE MAXIMUM RATINGS

Parameter	Ratings
Operating Temperature	-40°C to +105°C
Storage Temperature	-40°C to +105°C
Power Input (as a splitter)*	15 W max. at +25°C

* Derate linearly to 7 W at +105°C ambient.

Permanent damage may occur if any of these limits are exceeded.

ELECTRICAL SCHEMATIC



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REV. A
ECO-025912
QCN-8AT+
ED-11876D/1
MCL NY
250701
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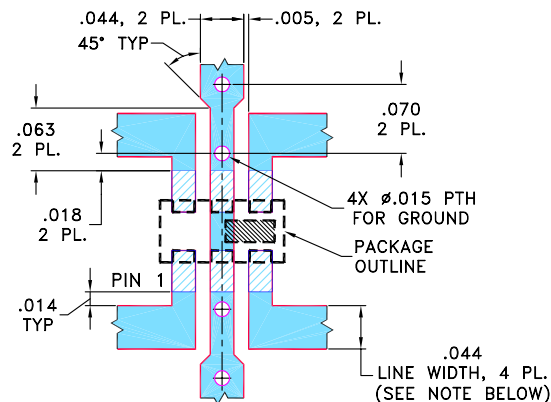
2 Way-90° 50Ω 450 to 750 MHz

PIN CONNECTIONS

SUM PORT	1
PORT 1 (0°)	4
PORT 2 (+90°)	6
GROUND	2,5
50 OHM TERM EXTERNAL	3

PRODUCT MARKING: N/A

DEMO BOARD MCL P/N: TB-QCN-3+
SUGGESTED PCB LAYOUT (PL-131)

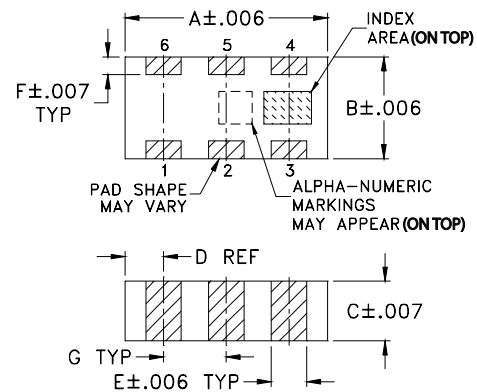


NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS $0.020" \pm 0.0015"$; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.

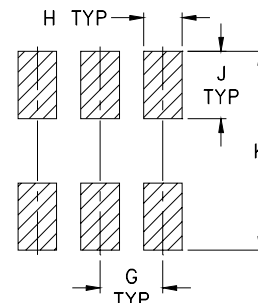
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

-  DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
-  DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

OUTLINE DRAWING



PCB Land Pattern



Suggested Layout,
Tolerance to be within ± 0.002

OUTLINE DIMENSIONS (Inches/mm)

A	B	C	D	E	F
.126	.063	.035	.024	.022	.011
3.20	1.60	0.89	0.61	0.56	0.28
G	H	J	K		wt
.039	.024	.042	.123		grams
0.99	0.61	1.07	3.12		.020

TAPE & REEL INFORMATION: F75





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ULTRA-SMALL CERAMIC

Power Splitter/Combiner

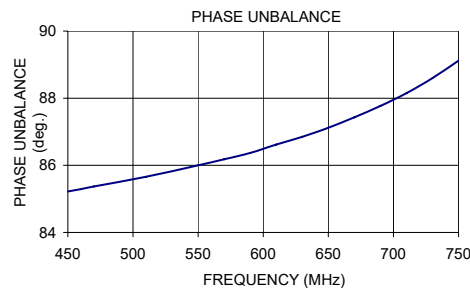
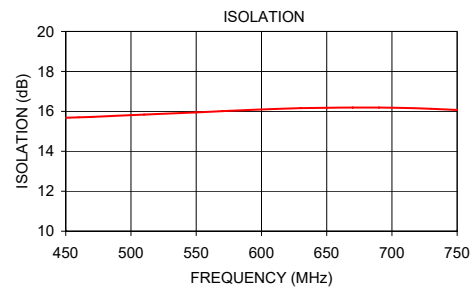
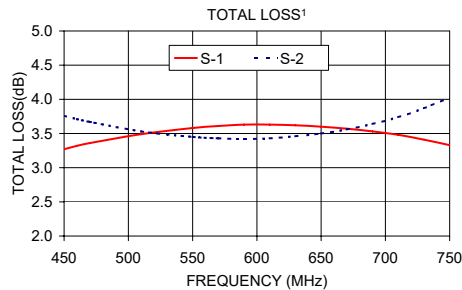
QCN-8AT+

2 Way-90° 50Ω 450 to 750 MHz

TYPICAL PERFORMANCE DATA

Frequency (MHz)	Total Loss ¹ (dB)		Amplitude Unbalance (dB)	Isolation (dB)	Phase Unbalance (deg.)	VSWR (:1)		
	S-1	S-2				S	1	2
450	3.27	3.76	0.49	15.68	85.22	1.41	1.41	1.38
460	3.32	3.71	0.40	15.70	85.29	1.40	1.41	1.38
470	3.36	3.67	0.31	15.72	85.37	1.40	1.41	1.37
510	3.49	3.53	0.04	15.84	85.66	1.39	1.40	1.36
550	3.58	3.45	0.13	15.95	86.00	1.37	1.40	1.36
570	3.61	3.43	0.18	16.01	86.18	1.37	1.40	1.36
590	3.63	3.42	0.20	16.07	86.37	1.36	1.40	1.36
610	3.63	3.43	0.20	16.12	86.62	1.36	1.40	1.36
630	3.62	3.46	0.17	16.16	86.85	1.36	1.40	1.37
650	3.60	3.50	0.11	16.18	87.12	1.36	1.41	1.38
670	3.57	3.56	0.01	16.19	87.43	1.37	1.41	1.39
690	3.53	3.64	0.11	16.19	87.77	1.37	1.43	1.40
710	3.48	3.74	0.27	16.17	88.15	1.38	1.44	1.42
730	3.41	3.87	0.46	16.13	88.60	1.40	1.46	1.45
750	3.33	4.03	0.70	16.07	89.12	1.41	1.48	1.48

1. Total Loss = Insertion Loss + 3 dB splitter loss.

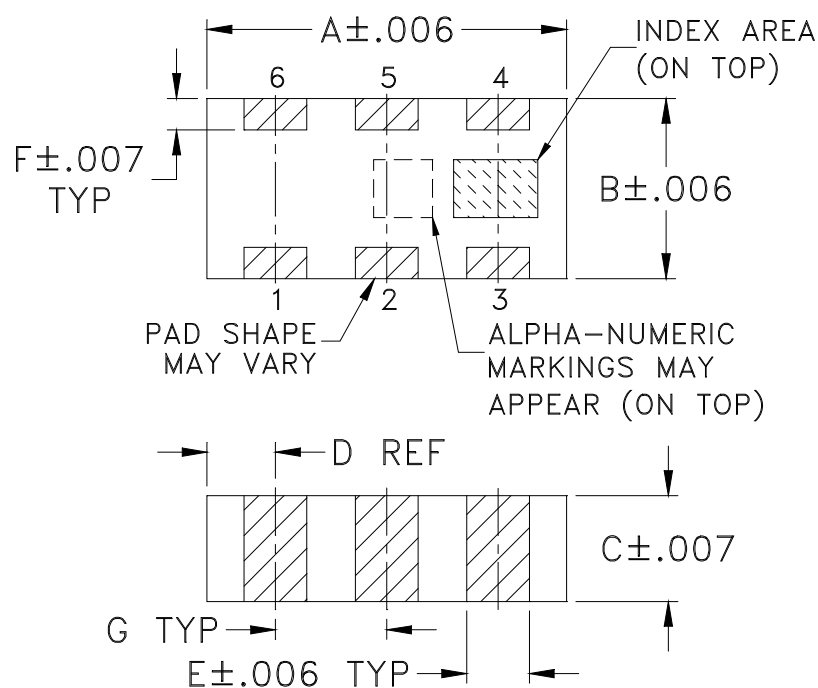


NOTES

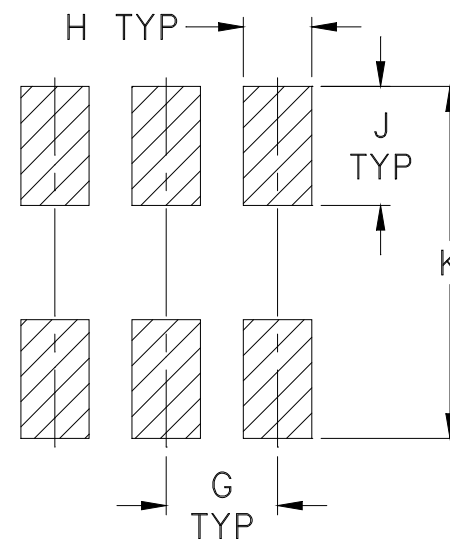
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Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	L	M	N	P	WT. GRAM
FV1206-1	.126 (3.20)	.063 (1.60)	.035 (0.89)	.024 (0.61)	.022 (0.56)	.011 (0.28)	.039 (0.99)	.024 (0.61)	.042 (1.07)	.123 (3.12)	--	--	--	--	.020

Dimensions are in inches (mm). Tolerances: 2 PL. $\pm .01$; 3 PL. $\pm .005$

Notes:

- Open style, ceramic base.
- Termination finish: **as shown below or indicated on Data Sheet.**
 For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
 For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



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RF/IF MICROWAVE COMPONENTS

DEVICE ORIENTATION IN T&R

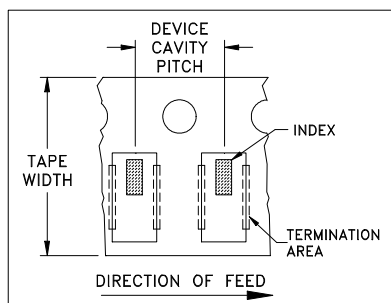


ILLUSTRATION 1

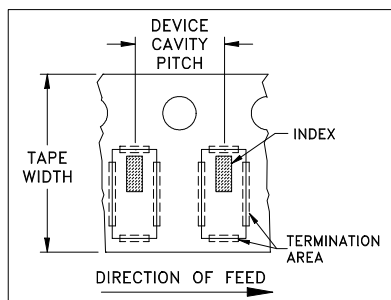


ILLUSTRATION 2

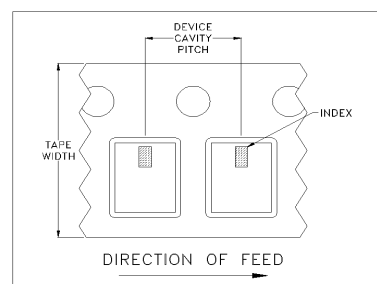


ILLUSTRATION 3

Applicable Case Styles

FV1206-1
FV1206-3

Applicable Case Styles

FV1206-4
FV1206-5
FV1206-6
FV1206-7
FV1206-9

Applicable Case Styles

FV1206-11
FV1206-12
GE0805C-18
NL1008C-6
NL1008C-7
NL1008C-9
NL1008C-10
NL1008C-12

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	3000

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

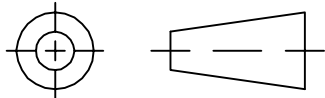


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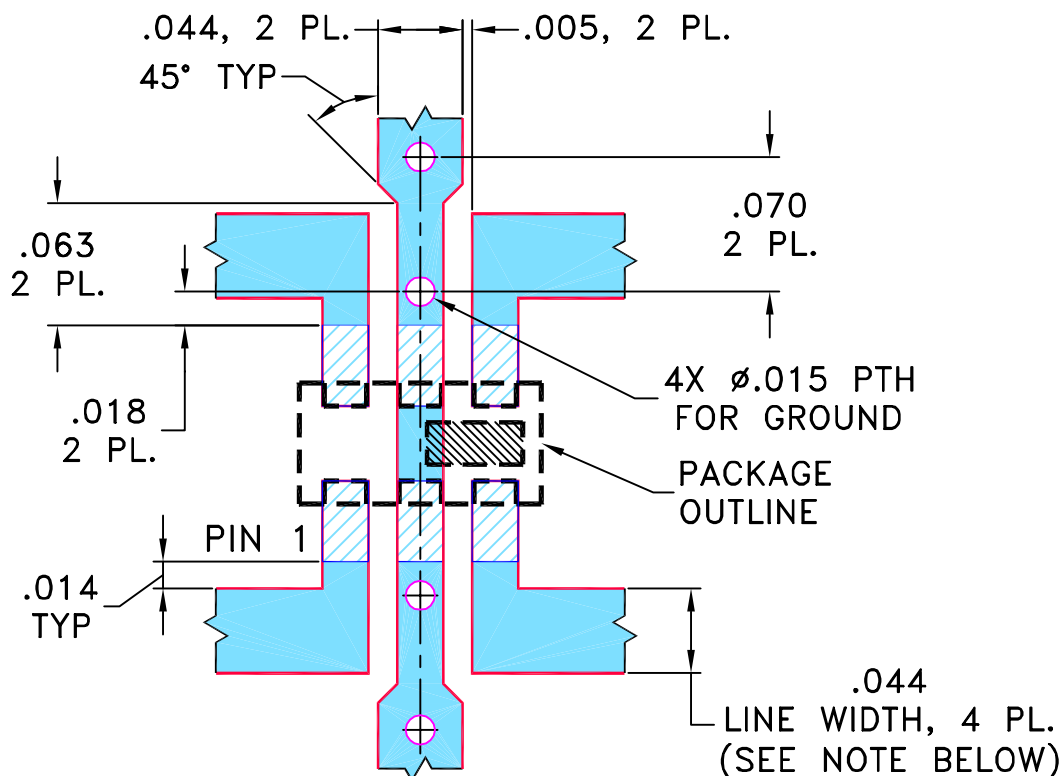
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M87001	NEW RELEASE	05/20/03	MMG	ABD
A	M87231	CORRECTED DWG.	05/28/03	MMG	ABD
B	M91636	ADDED "pn" PIN CONNECTION	04/07/04	AV	ABD
C	M102713	ADDED "...WITH SMOBC"	01/16/06	GF	IL

SUGGESTED MOUNTING CONFIGURATION
FOR FV1206-1 CASE STYLE, "pb/pn" PIN CONNECTIONS

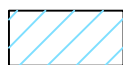


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2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005ANGLES $\pm$ FRACTIONS $\pm$

DRAWN

MMG

05/14/03

CHECKED

AV

05/19/03

APPROVED

ABD

05/20/03



Mini-Circuits®

13 Neptune Avenue
Brooklyn NY 11235

PL, pb/pn, FV1206-1, QCN/BDCN, TB-255

SIZE

CODE IDENT

DRAWING NO:

REV:

A

15542

98-PL-131

C

FILE:

98PL131

SCALE:

10:1

SHEET:

1 OF 1

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ASHEETA1.DWG REV:A DATE:01/12/95