

Engineering Development Model

Power Splitter/Combiner

QCN-ED12675B

2 Way-90°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

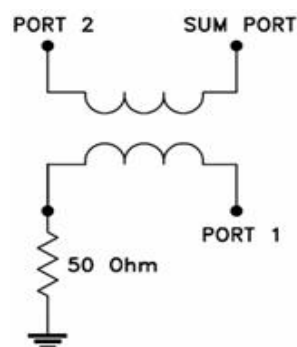
CASE STYLE : FV1206-1

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		150		500 MHz
Isolation	150 - 500 MHz		27	
Insertion Loss				
Average of Coupled Outputs	150 - 500 MHz		0.60	
above 3.0 dB				
Phase Unbalance from 90°	150 - 500 MHz		4.50	
Amplitude Unbalance	150 - 500 MHz		1.00	
VSWR	SUM Port		1.80	(:1)
	OUT Ports		1.07	(:1)

MAXIMUM RATINGS	
Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1 (0°)	4
PORT 2 (+90°)	6
50Ω TERM. EXTERNAL	3
GND	2,5

Functional Diagram



2 Way-90° Power Splitter/Combiner

QCN-ED12675B

Typical Performance Data

FREQ. (MHz)	TOTAL LOSS ¹			AMP. UNBAL. (dB)	ISOLATION (dB)	PHASE UNBAL. (deg.)	FREQ. (MHz)	VSWR		
	S-1	S-2	AVG.					S	(:1) 1	2
150.0	5.49	2.03	3.76	3.46	22.90	89.60	150.0	1.09	1.12	1.08
160.0	5.17	2.19	3.68	2.99	22.84	89.68	160.0	1.09	1.12	1.08
170.0	4.90	2.33	3.62	2.57	22.82	89.78	170.0	1.09	1.12	1.08
180.0	4.65	2.48	3.57	2.17	22.84	89.89	180.0	1.08	1.12	1.08
190.0	4.43	2.63	3.53	1.81	22.90	90.01	190.0	1.08	1.12	1.08
200.0	4.24	2.75	3.50	1.48	22.98	90.14	200.0	1.08	1.12	1.07
210.0	4.06	2.88	3.47	1.18	23.08	90.27	210.0	1.07	1.12	1.07
220.0	3.91	3.00	3.46	0.91	23.23	90.40	220.0	1.07	1.12	1.07
230.0	3.77	3.11	3.44	0.67	23.41	90.55	230.0	1.06	1.12	1.06
240.0	3.65	3.21	3.43	0.44	23.61	90.71	240.0	1.06	1.11	1.06
250.0	3.55	3.31	3.43	0.23	23.85	90.86	250.0	1.05	1.11	1.06
260.0	3.45	3.41	3.43	0.05	24.11	91.02	260.0	1.05	1.11	1.05
270.0	3.37	3.49	3.43	0.12	24.37	91.18	270.0	1.04	1.10	1.05
280.0	3.30	3.57	3.44	0.26	24.66	91.36	280.0	1.04	1.10	1.05
290.0	3.24	3.64	3.44	0.39	24.96	91.53	290.0	1.04	1.10	1.04
300.0	3.20	3.70	3.45	0.50	25.29	91.70	300.0	1.03	1.09	1.04
310.0	3.16	3.76	3.46	0.60	25.67	91.88	310.0	1.03	1.09	1.03
320.0	3.13	3.80	3.47	0.67	26.07	92.07	320.0	1.03	1.09	1.03
330.0	3.12	3.83	3.48	0.72	26.46	92.26	330.0	1.03	1.08	1.03
340.0	3.11	3.86	3.49	0.75	26.89	92.47	340.0	1.04	1.08	1.02
350.0	3.11	3.88	3.50	0.77	27.38	92.67	350.0	1.04	1.08	1.02
360.0	3.12	3.88	3.50	0.76	27.90	92.89	360.0	1.05	1.08	1.02
370.0	3.14	3.88	3.51	0.74	28.47	93.12	370.0	1.05	1.07	1.02
380.0	3.18	3.87	3.53	0.70	29.08	93.37	380.0	1.06	1.07	1.02
390.0	3.22	3.85	3.54	0.63	29.82	93.63	390.0	1.07	1.07	1.02
400.0	3.28	3.82	3.55	0.54	30.61	93.89	400.0	1.08	1.07	1.01
410.0	3.35	3.78	3.57	0.42	31.55	94.16	410.0	1.09	1.07	1.01
420.0	3.44	3.72	3.58	0.28	32.68	94.46	420.0	1.10	1.07	1.01
430.0	3.55	3.66	3.61	0.11	33.99	94.80	430.0	1.11	1.07	1.01
440.0	3.67	3.58	3.63	0.09	35.49	95.17	440.0	1.13	1.07	1.02
450.0	3.82	3.50	3.66	0.32	37.16	95.56	450.0	1.14	1.07	1.02
460.0	3.99	3.40	3.70	0.59	38.56	95.99	460.0	1.15	1.07	1.02
470.0	4.19	3.29	3.74	0.90	38.74	96.48	470.0	1.17	1.08	1.02
480.0	4.43	3.18	3.81	1.25	37.01	97.04	480.0	1.19	1.08	1.03
490.0	4.71	3.06	3.89	1.65	34.39	97.69	490.0	1.21	1.09	1.03
500.0	5.04	2.92	3.98	2.12	31.79	98.44	500.0	1.23	1.10	1.04
510.0	5.43	2.79	4.11	2.65	29.38	99.33	510.0	1.26	1.12	1.05
520.0	5.90	2.65	4.28	3.25	27.20	100.45	520.0	1.29	1.13	1.07
530.0	6.47	2.51	4.49	3.95	25.22	101.84	530.0	1.32	1.15	1.08
540.0	7.15	2.38	4.77	4.77	23.40	103.64	540.0	1.35	1.17	1.10
550.0	7.97	2.26	5.12	5.71	21.71	106.03	550.0	1.39	1.20	1.13

¹ Total Loss = Insertion Loss+ 3dB Splitter Loss

REV. X2

QCN-ED12675B

100705

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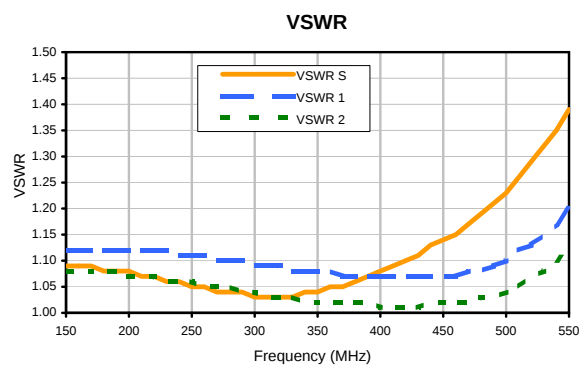
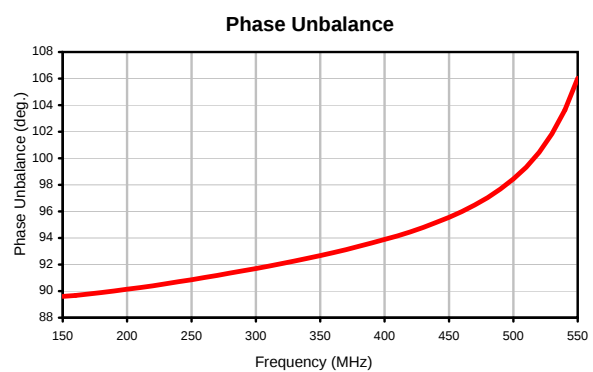
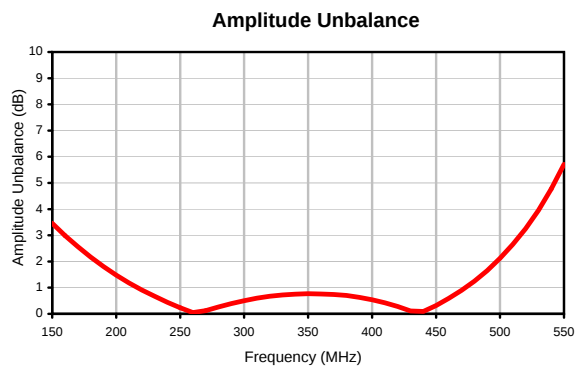
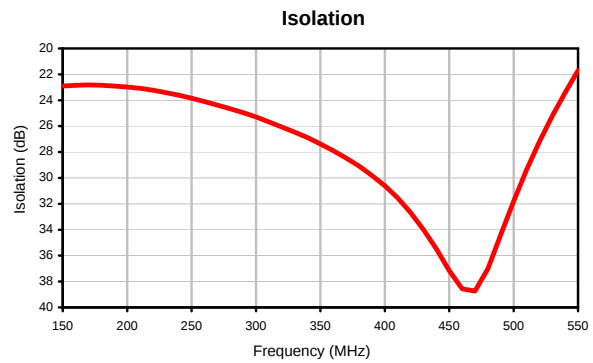
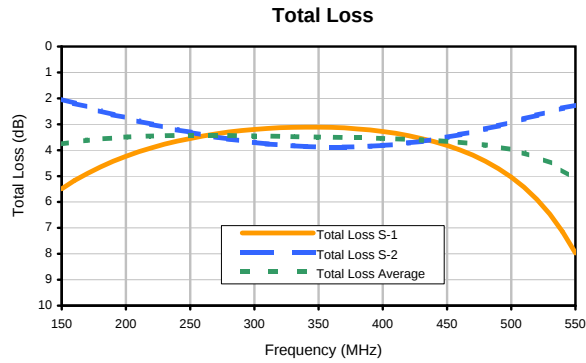
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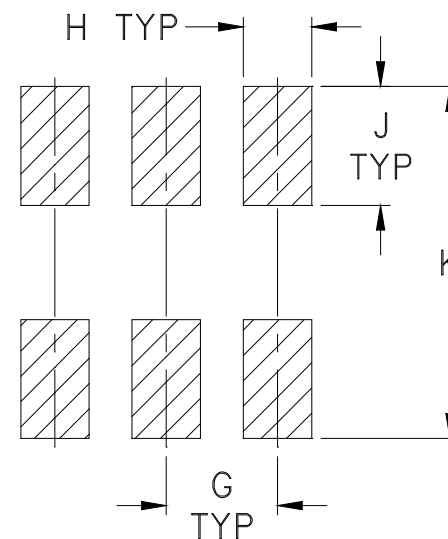
Typical Performance Curves



Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	L	M	N	P	WT. GRAM
FV1206-1	.126 (3.20)	.063 (1.60)	.035 (0.89)	.024 (0.61)	.022 (0.56)	.011 (0.28)	.039 (0.99)	.024 (0.61)	.042 (1.07)	.123 (3.12)	--	--	--	--	.020

Dimensions are in inches (mm). Tolerances: 2 PL. $\pm .01$; 3 PL. $\pm .005$

Notes:

- Open style, ceramic base.
- Termination finish: **as shown below or indicated on Data Sheet.**
For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



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RF/IF MICROWAVE COMPONENTS

DEVICE ORIENTATION IN T&R

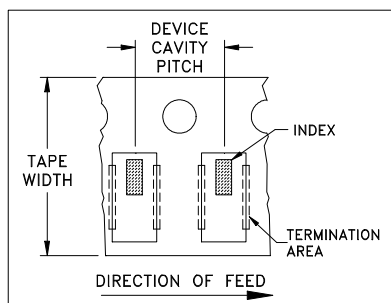


ILLUSTRATION 1



ILLUSTRATION 2



ILLUSTRATION 3

Applicable Case Styles

FV1206-1
FV1206-3

Applicable Case Styles

FV1206-4
FV1206-5
FV1206-6
FV1206-7
FV1206-9

Applicable Case Styles

FV1206-11
FV1206-12
GE0805C-18
NL1008C-6
NL1008C-7
NL1008C-9
NL1008C-10
NL1008C-12

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	3000

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

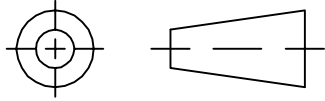


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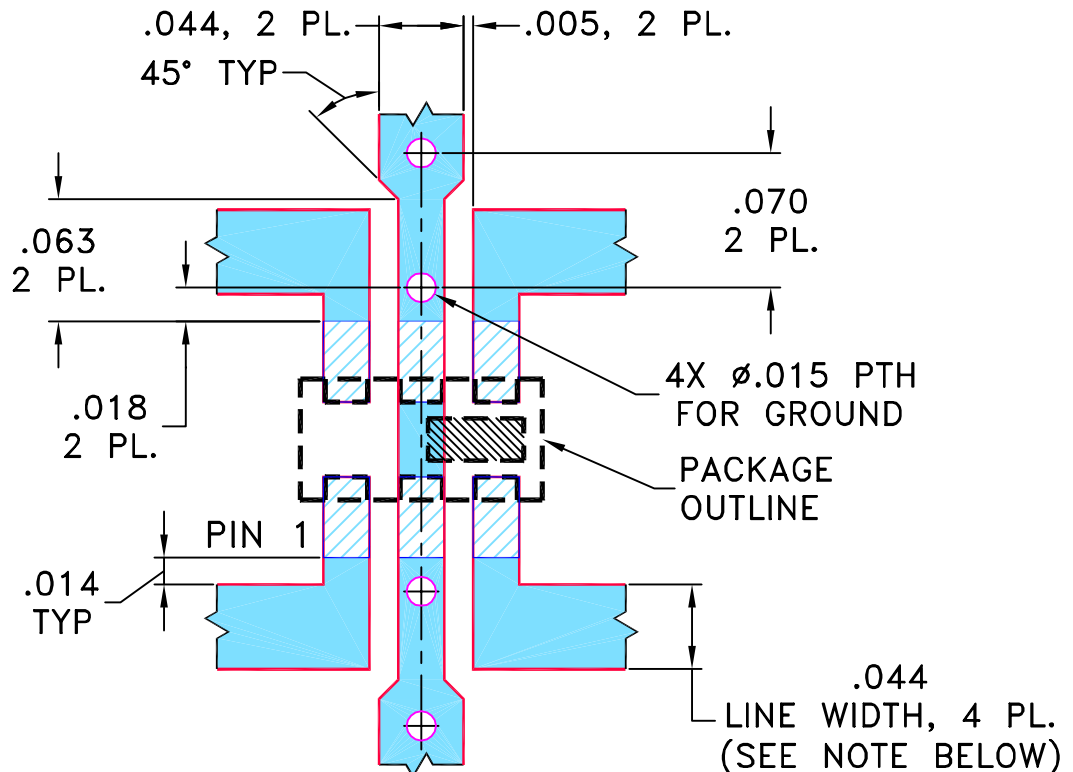
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M87001	NEW RELEASE	05/20/03	MMG	ABD
A	M87231	CORRECTED DWG.	05/28/03	MMG	ABD
B	M91636	ADDED "pn" PIN CONNECTION	04/07/04	AV	ABD
C	M102713	ADDED "...WITH SMOBC"	01/16/06	GF	IL

SUGGESTED MOUNTING CONFIGURATION
FOR FV1206-1 CASE STYLE, "pb/pn" PIN CONNECTIONS



NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS 0.020" $\pm$ 0.0015"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.

2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005ANGLES $\pm$ FRACTIONS $\pm$

DRAWN

MMG

05/14/03

CHECKED

AV

05/19/03

APPROVED

ABD

05/20/03

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PL, pb/pn, FV1206-1, QCN/BDCN, TB-255

SIZE
ACODE IDENT
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98-PL-131REV:
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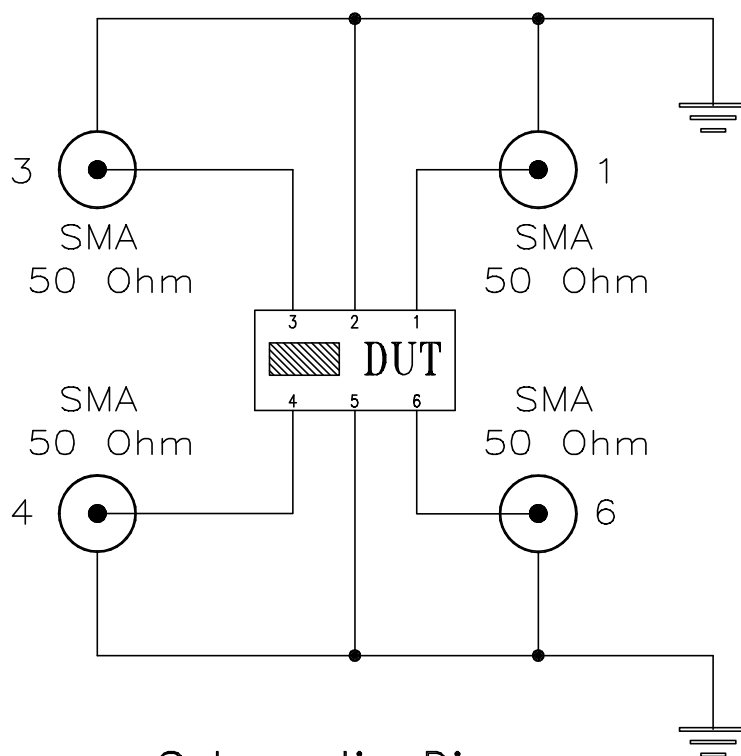
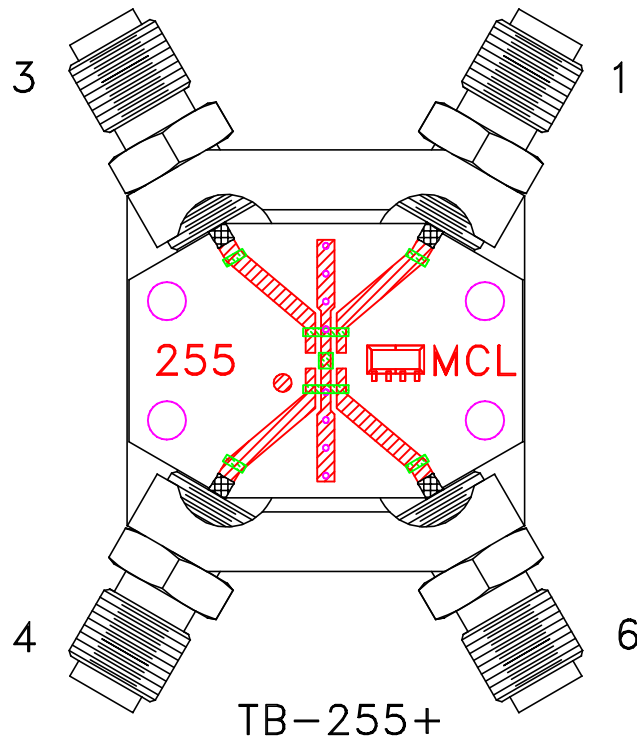
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SCALE: 10:1

SHEET: 1 OF 1

Evaluation Board and Circuit

For Pin Connections refer to Data Sheet of the DUT



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent, Dielectric Constant=3.5, Thickness=.020 inch.

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All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A