

Power Splitter/Combiner

QCN-ED13387

2 Way-90°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

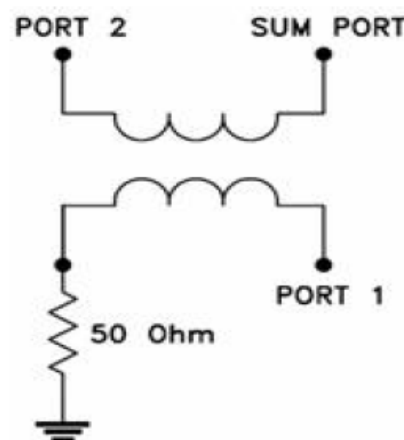
CASE STYLE : FV1206-1

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		500		1200 MHz
Isolation	500-1200 MHz		27	dB
Insertion Loss Average of Coupled Outputs above 3.0 dB	500-1200 MHz		0.40	dB
Phase Unbalance	500-1200 MHz		0.40	deg.
Amplitude Unbalance	500-1200 MHz		0.65	dB
VSWR	SUM Port		1.12	(:1)
	OUT Ports		1.10	(:1)

MAXIMUM RATINGS	
Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	1
PORT 1 (0°)	4
PORT 2 (90°)	6
50 TERM. EXTERNAL	3
GND	2,5

Functional Diagram

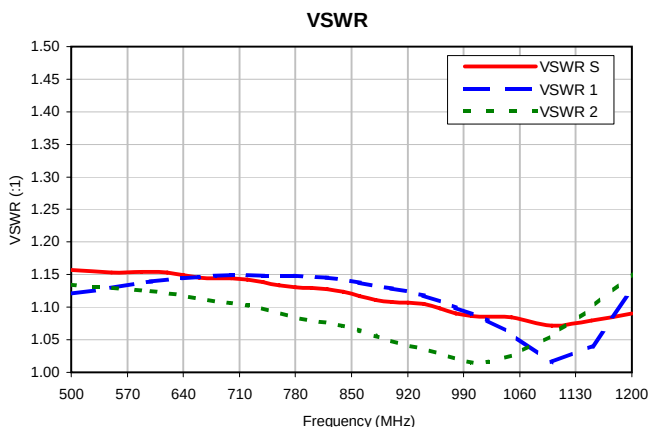
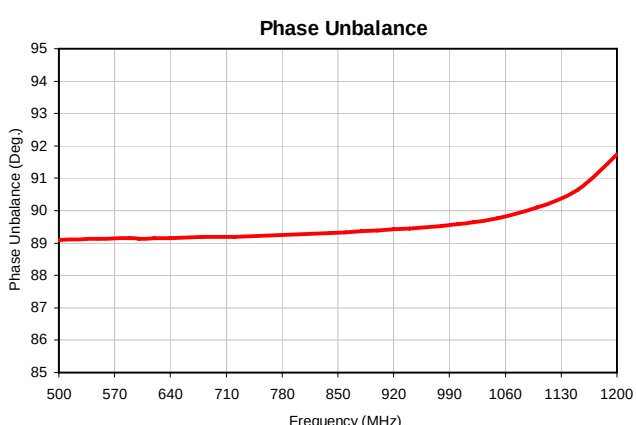
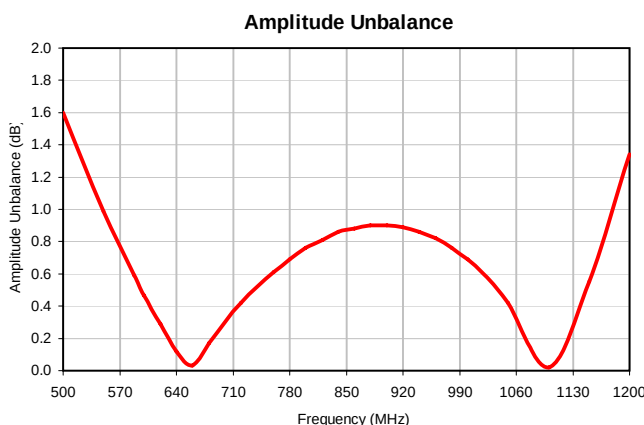
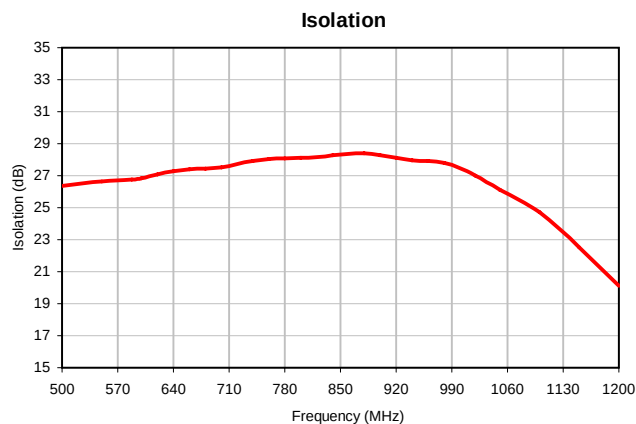
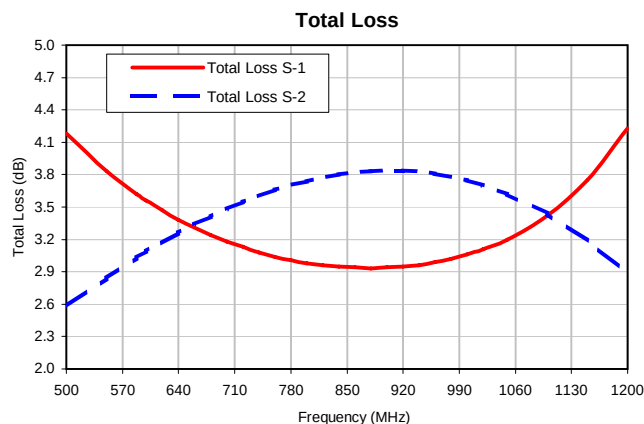


Typical Performance Data

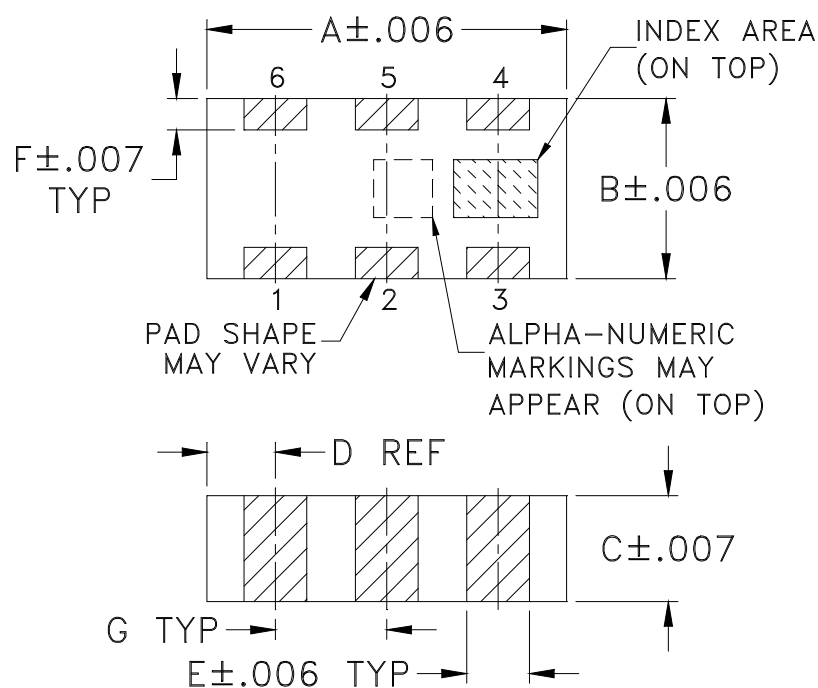
FREQ. (MHz)	TOTAL LOSS ¹ (dB)		AMP. UNBAL. (dB)	ISOLATION (dB)	PHASE UNBAL. (Deg.)	FREQ. (MHz)	VSWR (:1)		
	S-1	S-2					S	1	2
500.0	4.18	2.58	1.60	26.36	89.10	500.0	1.16	1.12	1.13
550.0	3.83	2.84	0.99	26.65	89.13	550.0	1.15	1.13	1.13
588.0	3.62	3.03	0.59	26.78	89.15	588.0	1.15	1.14	1.13
600.0	3.56	3.09	0.47	26.86	89.14	600.0	1.15	1.14	1.12
620.0	3.47	3.17	0.29	27.08	89.15	620.0	1.15	1.14	1.12
640.0	3.38	3.26	0.12	27.28	89.15	640.0	1.15	1.14	1.12
660.0	3.31	3.34	0.03	27.41	89.17	660.0	1.15	1.15	1.11
680.0	3.24	3.41	0.17	27.45	89.18	680.0	1.14	1.15	1.11
700.0	3.18	3.48	0.30	27.54	89.18	700.0	1.14	1.15	1.11
720.0	3.13	3.55	0.42	27.71	89.19	720.0	1.14	1.15	1.10
740.0	3.08	3.60	0.52	27.91	89.21	740.0	1.14	1.15	1.10
760.0	3.04	3.66	0.61	28.04	89.23	760.0	1.13	1.15	1.09
780.0	3.01	3.70	0.69	28.09	89.25	780.0	1.13	1.15	1.08
800.0	2.98	3.74	0.76	28.11	89.27	800.0	1.13	1.15	1.08
820.0	2.96	3.77	0.81	28.16	89.28	820.0	1.13	1.15	1.08
840.0	2.95	3.80	0.86	28.27	89.30	840.0	1.12	1.14	1.07
860.0	2.94	3.82	0.88	28.38	89.33	860.0	1.12	1.14	1.06
880.0	2.93	3.83	0.90	28.40	89.36	880.0	1.11	1.13	1.06
900.0	2.94	3.84	0.90	28.29	89.39	900.0	1.11	1.13	1.05
920.0	2.95	3.84	0.89	28.11	89.42	920.0	1.11	1.12	1.04
940.0	2.96	3.83	0.86	27.97	89.44	940.0	1.10	1.12	1.04
960.0	2.99	3.81	0.82	27.91	89.49	960.0	1.10	1.11	1.03
980.0	3.02	3.78	0.76	27.80	89.53	980.0	1.09	1.10	1.02
1000.0	3.06	3.75	0.69	27.49	89.58	1000.0	1.09	1.09	1.01
1020.0	3.11	3.70	0.60	26.99	89.64	1020.0	1.09	1.08	1.02
1050.0	3.20	3.62	0.42	26.14	89.77	1050.0	1.08	1.06	1.03
1100.0	3.42	3.44	0.02	24.74	90.09	1100.0	1.07	1.02	1.06
1150.0	3.75	3.19	0.55	22.53	90.64	1150.0	1.08	1.04	1.10
1200.0	4.23	2.89	1.34	20.14	91.73	1200.0	1.09	1.13	1.15

¹Total Loss = Insertion Loss + 3dB Splitter Loss

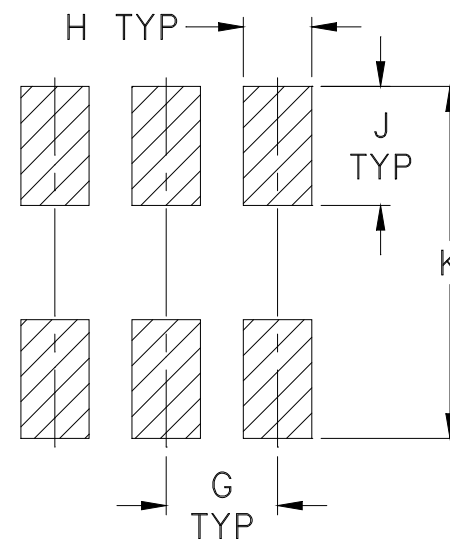
Typical Performance Curves



Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K	L	M	N	P	WT. GRAM
FV1206-1	.126 (3.20)	.063 (1.60)	.035 (0.89)	.024 (0.61)	.022 (0.56)	.011 (0.28)	.039 (0.99)	.024 (0.61)	.042 (1.07)	.123 (3.12)	--	--	--	--	.020

Dimensions are in inches (mm). Tolerances: 2 PL. $\pm .01$; 3 PL. $\pm .005$

Notes:

- Open style, ceramic base.
- Termination finish: **as shown below or indicated on Data Sheet.**
 For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
 For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.



P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661 For detailed performance specs & shopping online see Mini-Circuits web site



The Design Engineers Search Engine Provides ACTUAL Data Instantly From MINI-CIRCUITS At: www.minicircuits.com

RF/IF MICROWAVE COMPONENTS

DEVICE ORIENTATION IN T&R

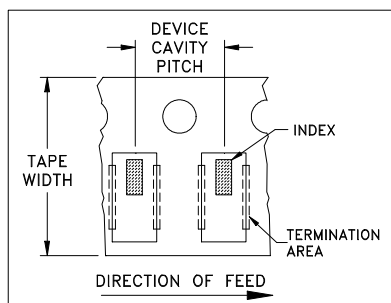


ILLUSTRATION 1

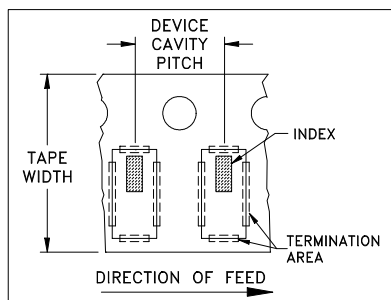


ILLUSTRATION 2

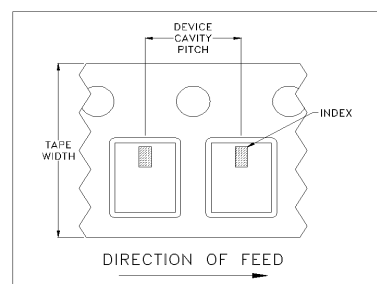


ILLUSTRATION 3

Applicable Case Styles

FV1206-1
FV1206-3

Applicable Case Styles

FV1206-4
FV1206-5
FV1206-6
FV1206-7
FV1206-9

Applicable Case Styles

FV1206-11
FV1206-12
GE0805C-18
NL1008C-6
NL1008C-7
NL1008C-9
NL1008C-10
NL1008C-12

Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
8	4	7	Small quantity standards (see note)	20
				50
				100
				200
				500
				1000
			Standard	3000

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

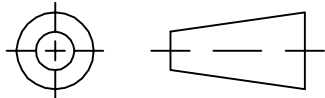


Distribution Centers NORTH AMERICA 800-654-7949 • 417-335-5935 • Fax 417-335-5945 • **EUROPE** 44-1252-832600 • Fax 44-1252-837010
Mini-Circuits ISO 9001 & ISO 14001 Certified

INTERNET <http://www.minicircuits.com>

P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661

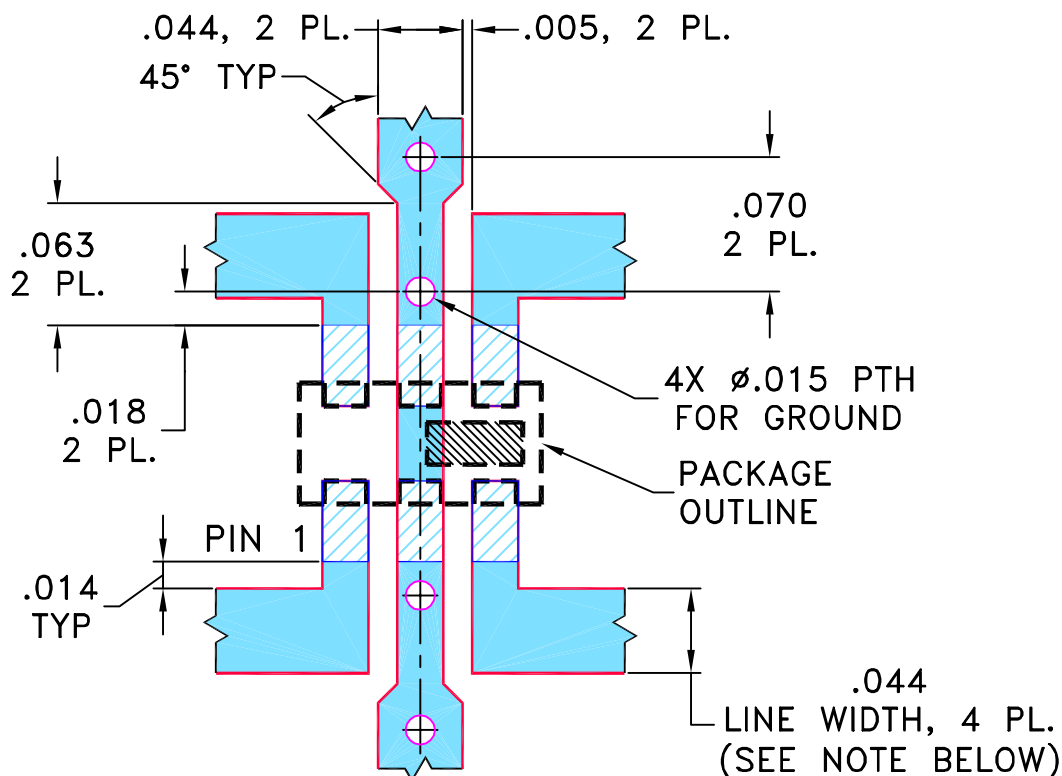
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	M87001	NEW RELEASE	05/20/03	MMG	ABD
A	M87231	CORRECTED DWG.	05/28/03	MMG	ABD
B	M91636	ADDED "pn" PIN CONNECTION	04/07/04	AV	ABD
C	M102713	ADDED "...WITH SMOBC"	01/16/06	GF	IL

SUGGESTED MOUNTING CONFIGURATION
FOR FV1206-1 CASE STYLE, "pb/pn" PIN CONNECTIONS

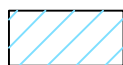


NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS 0.020" $\pm$ 0.0015"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.

2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005ANGLES $\pm$ FRACTIONS $\pm$

DRAWN

MMG

05/14/03

CHECKED

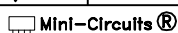
AV

05/19/03

APPROVED

ABD

05/20/03



THIS DOCUMENT AND ITS CONTENTS ARE THE PROPERTY OF MINI-CIRCUITS. EXCEPT FOR USE EXPRESSLY GRANTED, IN WRITING, TO ITS VENDORS, VENDEE AND THE UNITED STATES GOVERNMENT, MINI-CIRCUITS RESERVES ALL PROPRIETARY DESIGN, USE, MANUFACTURING AND REPRODUCTION RIGHTS THERETO. THESE CONTENTS SHALL NOT BE USED, DUPLICATED OR DISCLOSED TO ANY OUTSIDE PARTY, IN WHOLE OR IN PART, WITHOUT WRITTEN PERMISSION OF MINI-CIRCUITS.

ASHEETA1.DWG REV:A DATE:01/12/95



Mini-Circuits®

13 Neptune Avenue
Brooklyn NY 11235

PL, pb/pn, FV1206-1, QCN/BDCN, TB-255

SIZE

A

CODE IDENT

15542

DRAWING NO:

98-PL-131

REV:

C

FILE:

98PL131

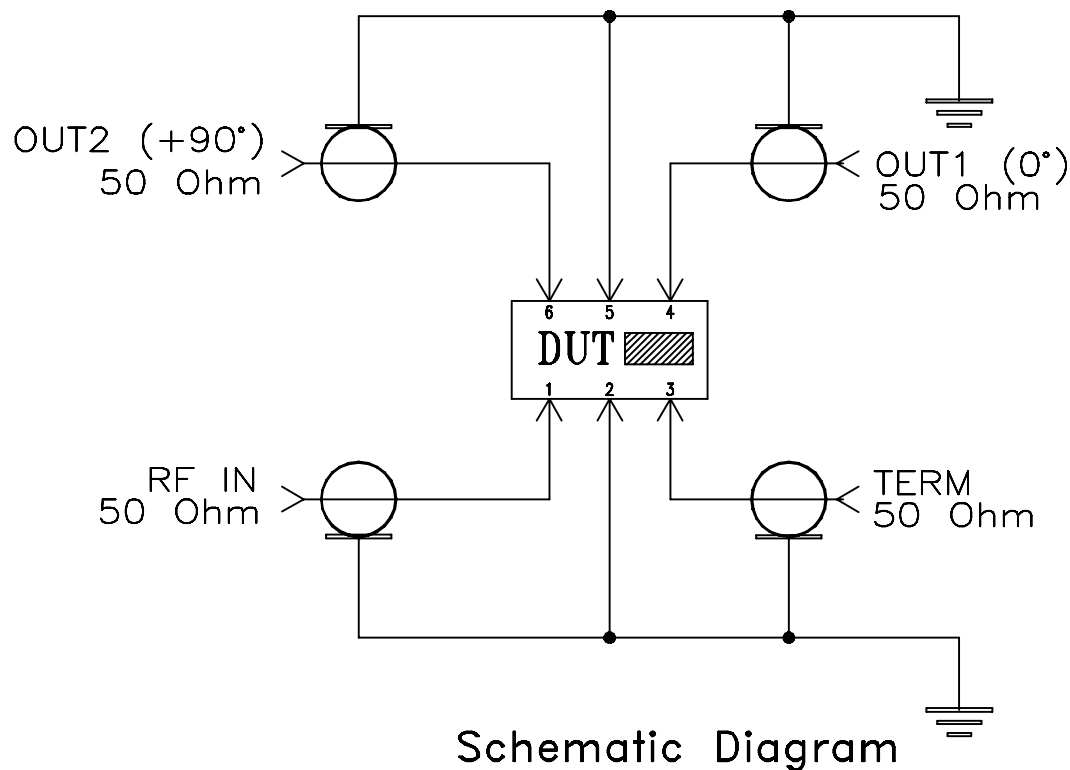
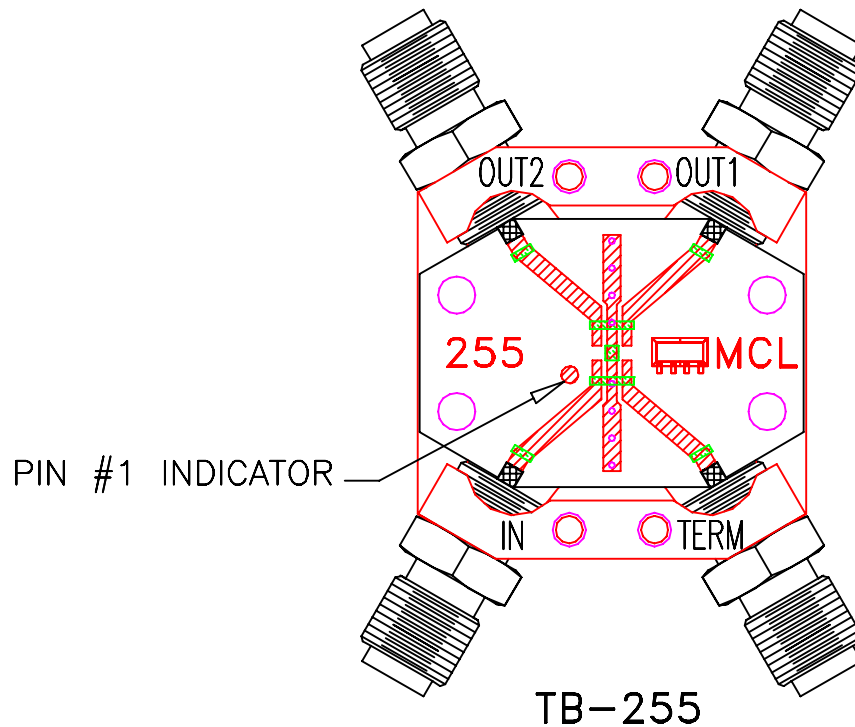
SCALE:

10:1

SHEET:

1 OF 1

Evaluation Board and Circuit



Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent, Dielectric Constant=3.5, Thickness=.020 inch.

 **Mini-Circuits®**



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 100°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A