

Engineering Development Model

Voltage Controlled Oscillator

ROS-ED12617/2

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : DV874

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	470		520	MHz
Tuning Voltage	.5		4.7	V
Power Output		0		dBm
Phase Noise	at 1 kHz offset	-89		dBc/Hz
	at 10 KHz offset	-114		dBc/Hz
	at 100 KHz offset	-133		dBc/Hz
	at 1000 kHz offset	-153		dBc/Hz
Pulling at 12 dBm PK-PK all phases		.5		MHz
Pushing at Vcc ± 5%		.8		MHz/V
Tuning Sensitivity		16-18		MHz/V
Harmonic Suppression		-16		dBc
3 dB Modulation Bandwidth		90,000		kHz
Supply Voltage		5		V
Supply Current			25	mA

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+7V
Absolute Tuning Voltage (Vtune)	+6.7V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	2
MODULATION	6
GROUND	1,3,4,5,7,8,9,11,12,13,15,16



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RF/MICROWAVE COMPONENTS



REV. X2
ROS-ED12617/2
12/17/2008
Page 1 of 1

Voltage Controlled Oscillator

ROS-ED12617/2

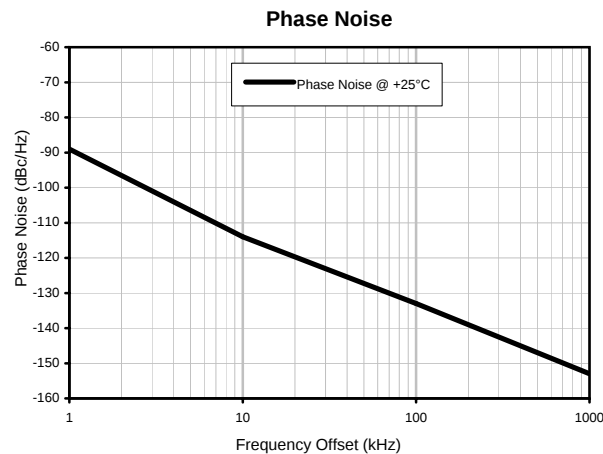
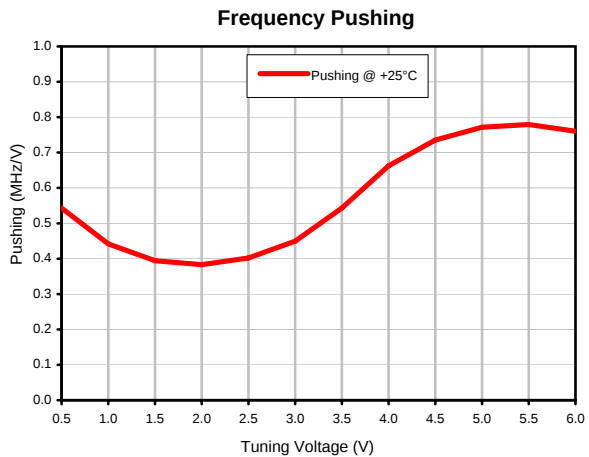
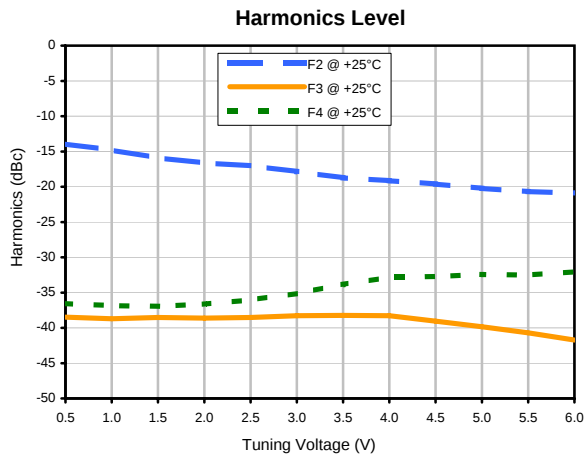
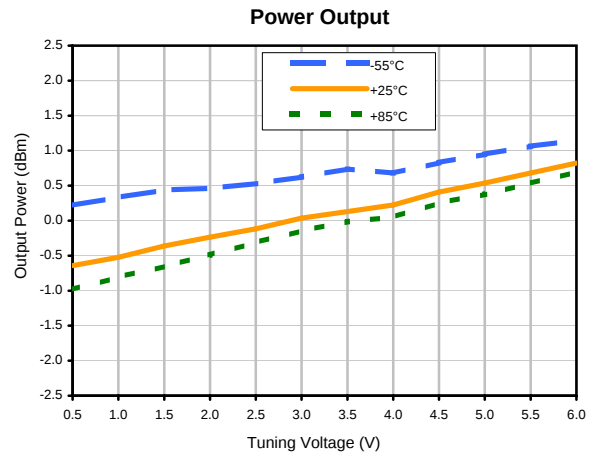
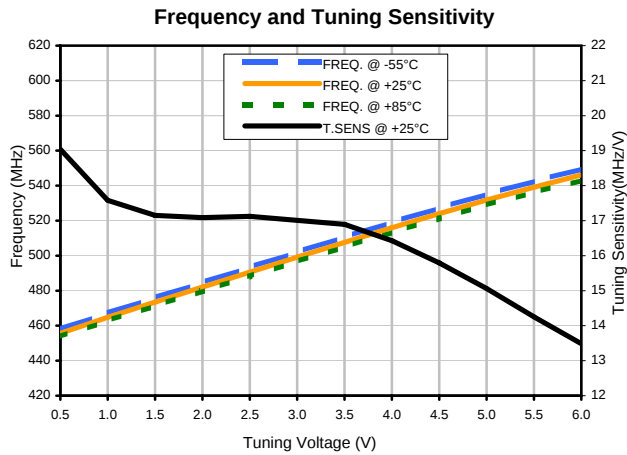
Typical Performance Data

V TUNE	TUNE SENS (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)			HARMONICS (dBc)			FREQ. PUSH (MHz/V)	FREQ OFFSET (KHz)	PHASE NOISE (dBc/Hz)
		-55°C	+25°C	+85°C	-55°C	+25°C	+85°C	F2	F3	F4			
0.5	19.04	458.0	455.7	453.8	0.22	-0.64	-0.98	-13.9	-38.5	-36.6	0.54	1	-89
1.0	17.58	467.1	464.8	462.8	0.33	-0.52	-0.81	-14.8	-38.7	-36.8	0.44	10	-114
1.5	17.15	475.9	473.5	471.4	0.44	-0.36	-0.65	-15.9	-38.5	-36.9	0.39	100	-133
2.0	17.09	484.6	482.0	479.8	0.46	-0.24	-0.49	-16.6	-38.6	-36.6	0.38	1000	-153
2.5	17.12	493.3	490.6	488.2	0.53	-0.12	-0.31	-17.0	-38.5	-36.0	0.40		
3.0	17.01	502.0	499.1	496.6	0.62	0.04	-0.14	-17.9	-38.3	-35.1	0.45		
3.5	16.90	510.6	507.6	505.0	0.74	0.13	-0.01	-18.7	-38.2	-33.9	0.54		
4.0	16.43	519.1	516.0	513.3	0.68	0.22	0.05	-19.1	-38.3	-32.8	0.66		
4.5	15.79	527.2	524.0	521.3	0.83	0.41	0.25	-19.6	-39.1	-32.7	0.74		
5.0	15.06	535.0	531.8	528.9	0.95	0.54	0.38	-20.2	-39.9	-32.4	0.77		
5.5	14.25	542.5	539.1	536.2	1.06	0.68	0.53	-20.7	-40.7	-32.5	0.78		
6.0	13.48	549.5	546.1	543.0	1.15	0.82	0.69	-20.9	-41.7	-32.0	0.76		

Voltage Controlled Oscillator

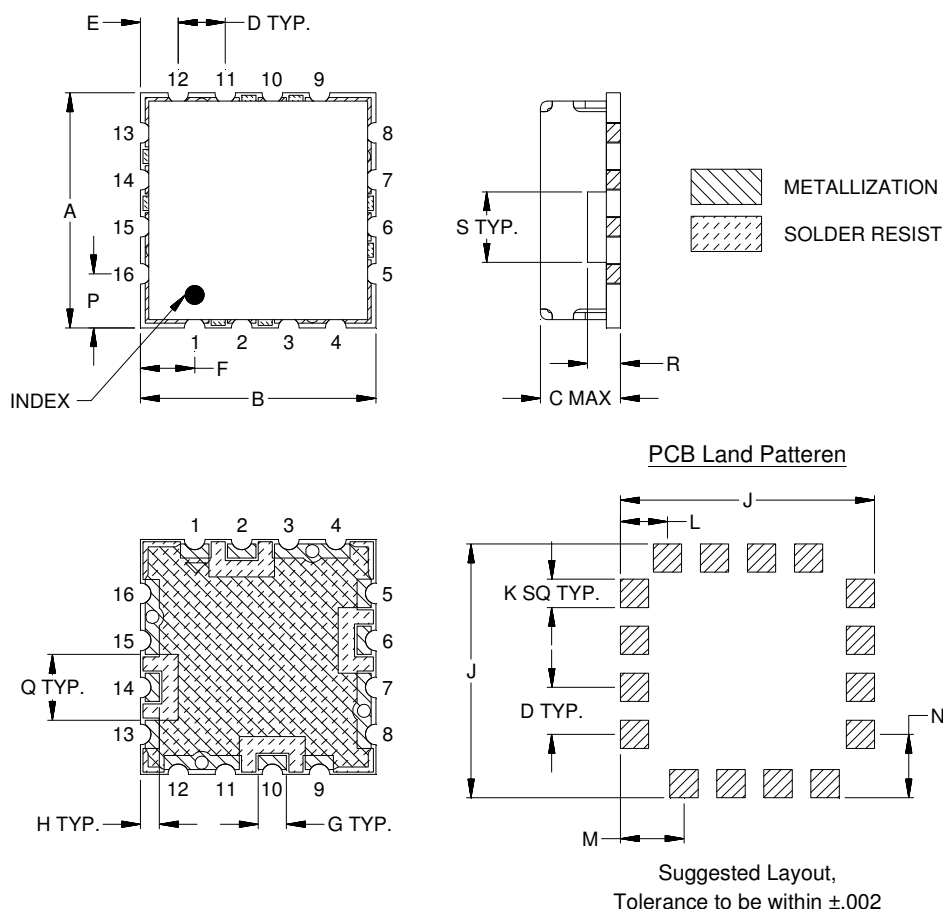
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Typical Performance Data



Outline Dimensions

DV874



CASE#	A	B	C	D	E	F	G	H	J	K	L	M
DV874	.500 (12.70)	.500 (12.70)	.195 (4.95)	.100 (2.54)	.080 (2.03)	.115 (2.92)	.060 (1.52)	.040 (1.02)	.540 (13.72)	.060 (1.52)	.100 (2.54)	.135 (3.43)

CASE#	N	P	Q	R	S	WT.GRAM
DV874	.135 (3.43)	.115 (2.92)	.140 (3.56)	.070 (1.78)	.150 (3.81)	1.0

Dimensions are in inches (mm). Tolerances: 2Pl. $\pm .03$; 3Pl. $\pm .015$

Notes:

- Case material: Nickel-Silver alloy.
- Base: Printed wiring laminate.
- Termination finish:
 - For RoHS Case Styles: 3-5 μ inch (.08-.13 microns) Gold over 120-240 μ inch (3.05-6.10 microns) Nickel plate.
 - For RoHS-5 Case Styles: Tin-Lead plate.

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RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F37



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
24	16	7	Small quantity standards (see note)	10
				20
				50
				100
		13	Standard	200
				500

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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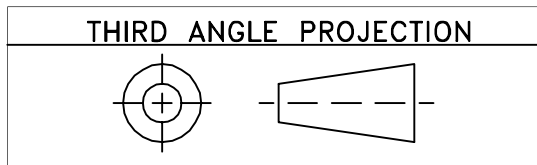


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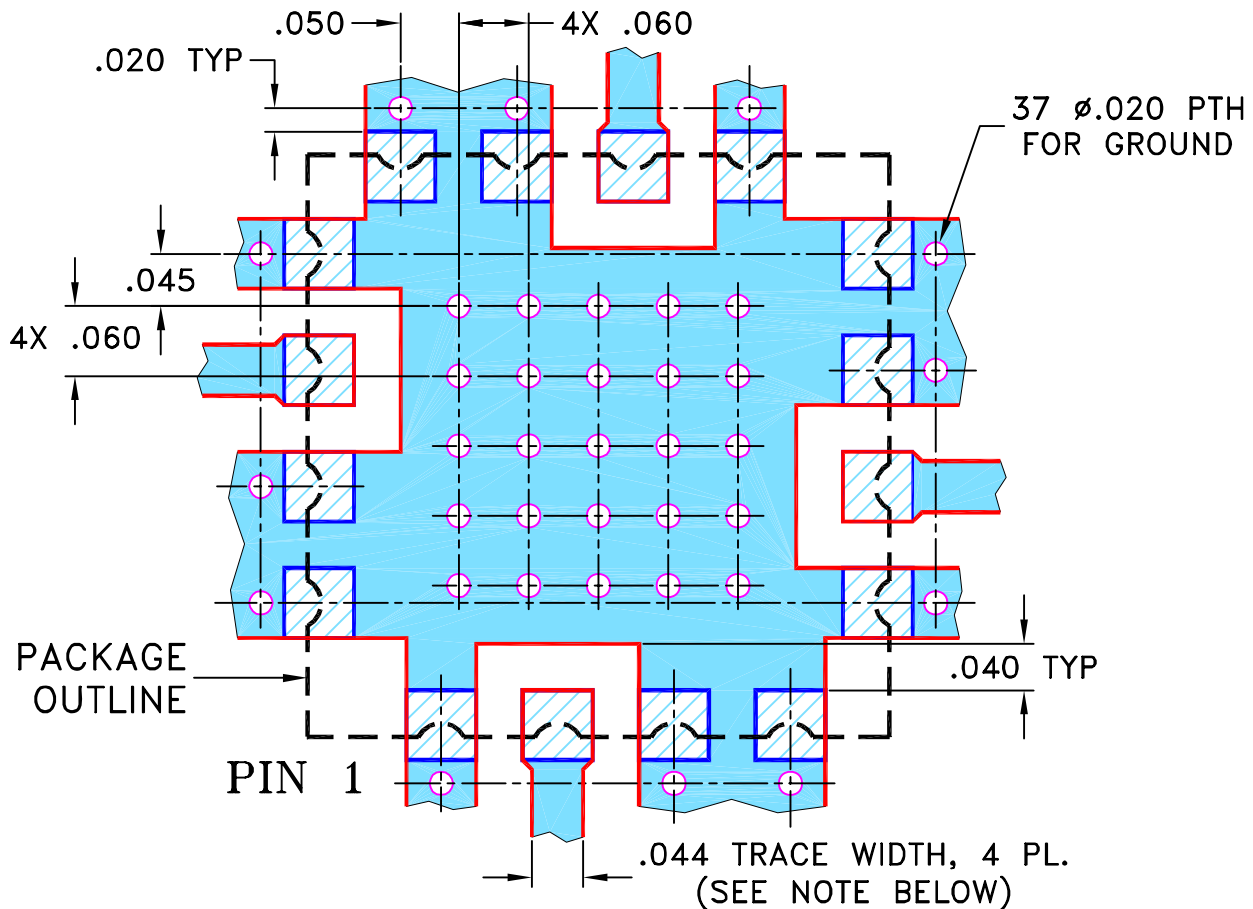
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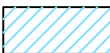


REVISIONS					
REV OR	ECN No.	DESCRIPTION	DATE	DR	AUTH
	M108586	NEW RELEASE	11/22/06	MMG	DJ

SUGGESTED MOUNTING CONFIGURATION FOR DV874 CASE STYLE, "rt" PIN CONNECTION



- NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS R04350B WITH DIELECTRIC THICKNESS .020" $\pm$.0015"; COPPER: 1/2 OZ. EACH SIDE.
FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.

-  DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
 DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED	INITIALS		DATE
DIMENSIONS ARE IN INCHES TOLERANCES ON: 2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005 ANGLES $\pm$ FRACTIONS $\pm$	DRAWN	MMG	11/21/06
	CHECKED	AY	11/22/06
	APPROVED	DJ	11/22/06



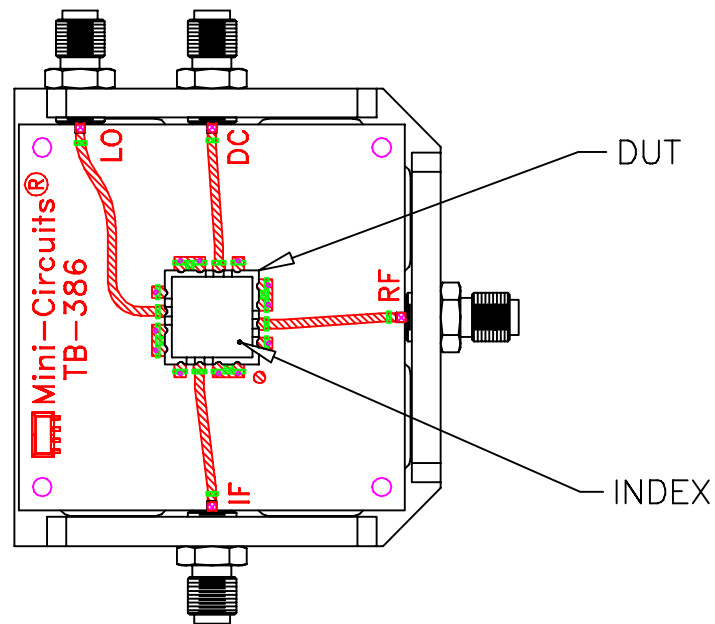
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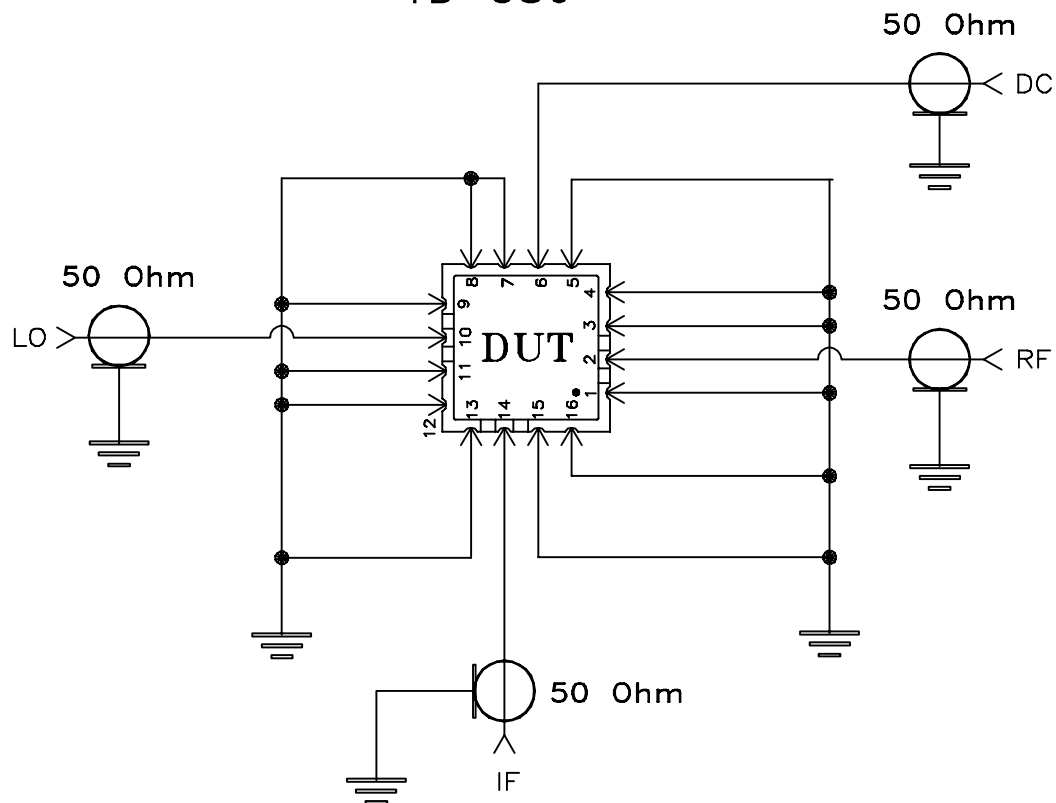
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SIZE A	CODE IDENT 15542	DRAWING NO: 98-PL-256	REV: OR
FILE: 98PL256	SCALE: 6:1	SHEET: 1 OF 1	

Evaluation Board and Circuit



TB-386



Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers R04350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.

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All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85° C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
HAST	130°C, 85% RH, 96 hours	JESD22-A110
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215