Engineering Development Model

Voltage Controlled Oscillator ROS-ED14583/1

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : 99-01-1277

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	**Min.**	**Typ.**	**Max.**	**Units**
Frequency	1811		1875	MHz
Tuning Voltage	.4		5.4	V
Power Output		4		dBm
Phase Noise				
at 1 kHz offset		-86		dBc/Hz
at 10 KHz offset		-112		dBc/Hz
at 100 KHz offset		-132		dBc/Hz
at 1000 kHz offset		-151		dBc/Hz
Pulling at 12 dBr PK-PK all phases		.8		MHz
Pushing at Vcc ± 5%		.2		MHz/V
Tuning Sensitivity		21-26		MHz/V
Harmonic Suppression		-23		dBc
3 dB Modulation Bandwidth		61,000		kHz
Supply Voltage		4.75		V
Supply Current			34	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+6V
Absolute Tuning Voltage (Vtune)	+7V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	2
GROUND	1,3,4,5,6,7,8,9,11,12,13,15,16

Voltage Controlled Oscillator ROS-ED14583/1

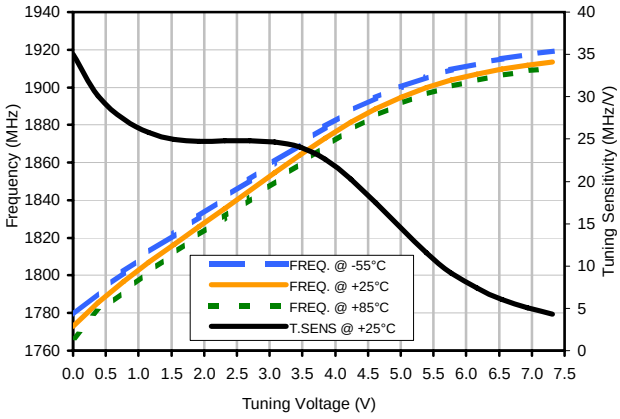
Typical Performance Data

V TUNE	TUNE SENS (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)			HARMONICS (dBc)			FREQ. PUSH (MHz/V)
		-55°C	+25°C	+85°C	-55°C	+25°C	+85°C	F2	F3	F4	
0.00	35.03	1778.78	1773.08	1767.22	3.28	3.33	3.05	-28.43	-24.03	-36.90	-0.36
0.38	30.11	1790.95	1785.61	1780.33	3.41	3.46	3.23	-28.02	-24.07	-36.00	-0.23
0.77	27.38	1801.83	1796.62	1791.63	3.52	3.55	3.33	-27.50	-24.38	-35.60	-0.16
1.15	25.78	1812.00	1806.82	1802.02	3.58	3.63	3.45	-26.95	-24.57	-35.81	-0.12
1.54	24.96	1821.80	1816.57	1811.88	3.66	3.70	3.55	-26.51	-24.86	-35.62	-0.07
1.92	24.73	1831.48	1826.12	1821.48	3.79	3.77	3.60	-26.41	-25.26	-35.47	-0.02
2.31	24.78	1841.16	1835.65	1831.03	3.84	3.83	3.67	-26.29	-25.92	-35.65	0.03
2.69	24.80	1850.86	1845.19	1840.61	3.89	3.90	3.78	-26.03	-26.52	-36.22	0.07
3.08	24.67	1860.53	1854.73	1850.20	3.97	3.96	3.85	-25.97	-27.43	-37.09	0.07
3.46	24.04	1870.00	1864.13	1859.69	4.06	4.02	3.91	-25.83	-28.38	-38.14	0.00
3.85	22.60	1878.95	1873.13	1868.86	4.13	4.07	3.97	-25.50	-29.34	-39.62	-0.15
4.23	20.29	1887.11	1881.41	1877.35	4.19	4.11	4.01	-25.44	-30.06	-40.45	-0.31
4.62	17.46	1894.27	1888.70	1884.86	4.22	4.15	4.07	-25.55	-30.87	-41.68	-0.40
5.01	14.44	1900.36	1894.85	1891.20	4.23	4.17	4.12	-25.73	-31.67	-42.58	-0.36
5.39	11.60	1905.36	1899.85	1896.37	4.27	4.19	4.14	-25.80	-32.24	-43.50	-0.23
5.78	9.16	1909.36	1903.83	1900.48	4.32	4.22	4.16	-25.73	-32.58	-43.44	-0.06
6.16	7.38	1912.54	1906.99	1903.75	4.36	4.23	4.16	-25.62	-32.76	-43.98	0.09
6.55	6.05	1915.16	1909.56	1906.40	4.41	4.24	4.16	-25.46	-32.89	-44.62	0.22
6.93	5.05	1917.33	1911.69	1908.58	4.44	4.25	4.16	-25.34	-32.97	-44.92	0.32
7.32	4.30	1919.16	1913.48	1910.42	4.45	4.26	4.16	-25.22	-33.00	-44.98	0.39

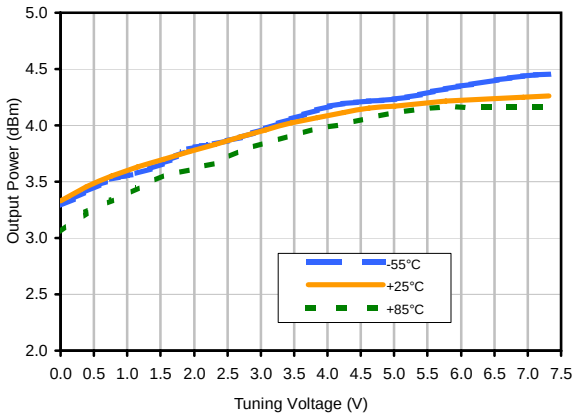
FREQ OFFSET (kHz)	PHASE NOISE (dBc/Hz)
1	-86
10	-112
100	-132
1000	-151

Typical Performance Curves

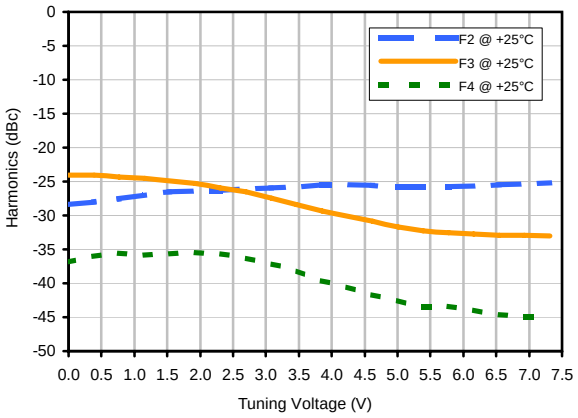
Frequency and Tuning Sensitivity



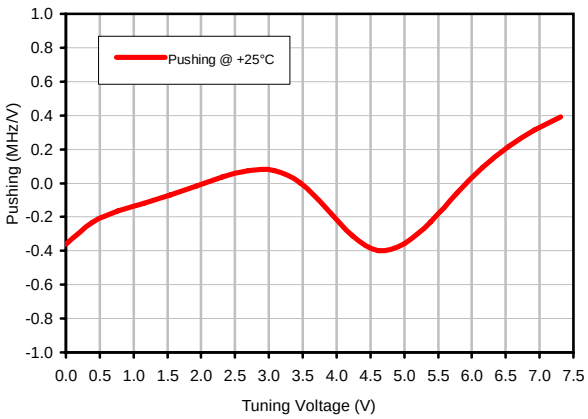
Power Output



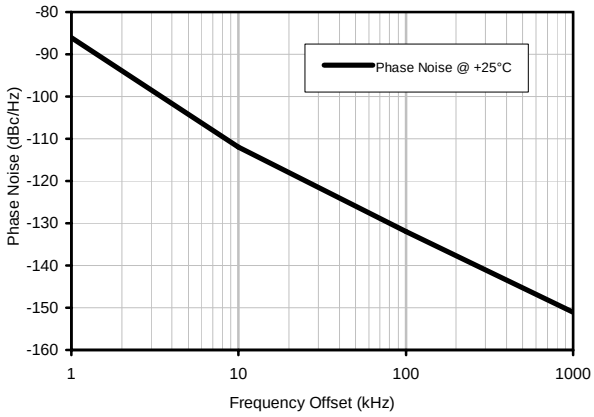
Harmonics Level



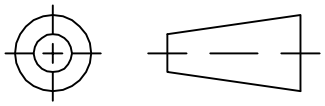
Frequency Pushing



Phase Noise

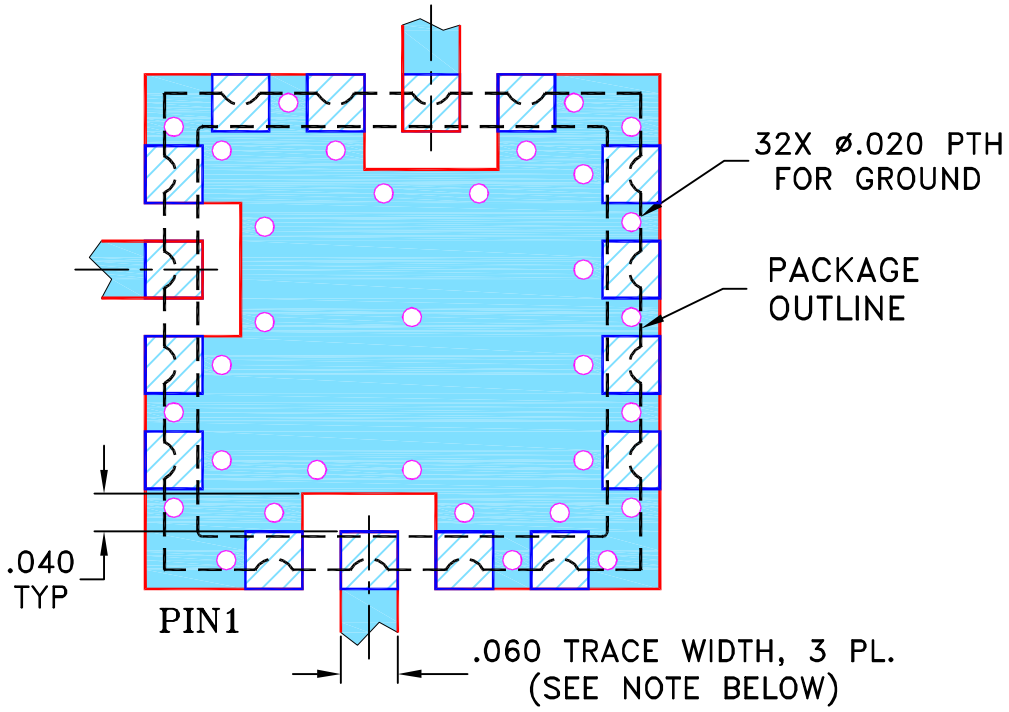


THIRD ANGLE PROJECTION



REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
E	M105563	ADDED "rl" PIN CONNECTION	06/02/06	MMG	DJ
F	M105640	CORRECTED NOTE 2	06/08/06	MMG	MM
G	M124395	ADDED "RAMP"	09/09	EM	HH
G	R77589	ADDED "RAMP"	09/09	EM	HH

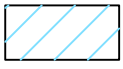
SUGGESTED MOUNTING CONFIGURATION FOR CK605 CASE STYLE, "kg/rl/16AM01" PIN CONNECTION



NOTES:

1. TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS .030" ± .002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE BOTTOM IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	AV	08/07/00
TOLERANCES ON:	CHECKED	SK	08/08/00
2 PL DECIMALS ±	APPROVED	DB	08/08/00
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

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ASHEETA1.DWG REV:A DATE:01/12/95

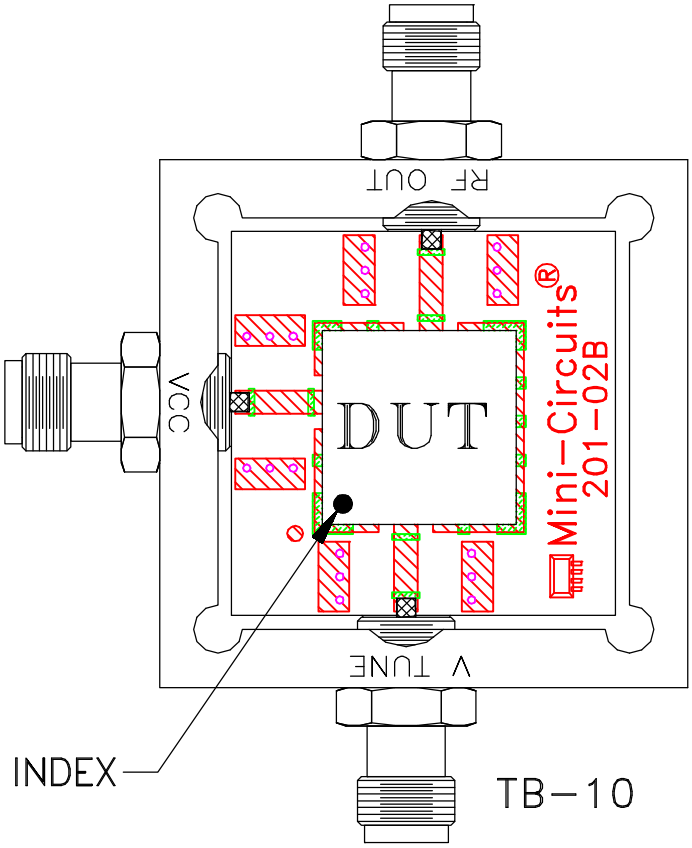
Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

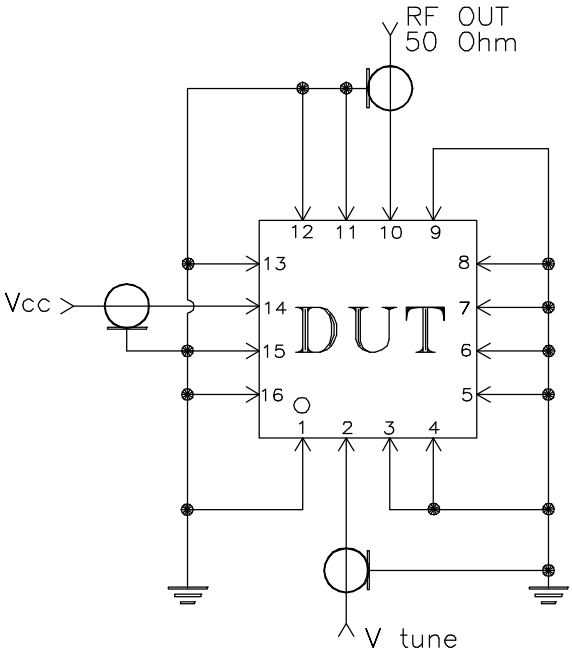
PL,kg/rl/16AM01,CK605,ROS/LAVI/RAMP

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-012	G

FILE:	SCALE:	SHEET:
98PL012	5:1	1 OF 1

Evaluation Board and Circuit





Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.

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Environmental Specifications ENV03

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
HAST	130°C, 85% RH, 96 hours	JESD22-A110
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215

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