Engineering Development Model

Voltage Controlled Oscillator

ROS-ED6476/1

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : CH513

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	**Min.**	**Typ.**	**Max.**	**Units**
Frequency	840		970	MHz
Tuning Voltage	0.5		4.5	V
Power Output		+6		dBm
Phase Noise				
at 1 kHz offset		-79		dBc/Hz
at 10 KHz offset		-103		dBc/Hz
at 100 KHz offset		-123		dBc/Hz
at 1000 kHz offset		-142		dBc/Hz
Pulling at 12 dBr PK-PK all phases		2		MHz
Pushing at Vcc=5V±0.25V		3		MHz/V
Tuning Sensitivity		47 - 72		MHz/V
Harmonic Suppression		-15		dBc
3 dB Modulation Bandwidth		2000		kHz
Supply Voltage		5		V
Supply Current			25	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+7V
Absolute Tuning Voltage (Vtune)	+6.5V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	8
GROUND	2,3,4,6,7,9,10,11,12

Voltage Controlled Oscillator ROS-ED6476/1

Typical Performance Data

V TUNE	TUNE SENS (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)			HARMONICS (dBc)			FREQ. PUSH (MHz/V)
		-55ºC	+25ºC	+85ºC	-55ºC	+25ºC	+85ºC	F2	F3	F4	
0.5	57.30	819.2	811.1	805.0	6.31	7.37	7.80	-15.0	-41.3	-50.1	
1.0	48.30	842.9	835.3	829.3	7.04	8.18	8.49	-17.3	-41.1	-49.0	1.18
1.5	46.30	866.1	858.4	852.3	8.04	8.66	9.17	-19.3	-40.1	-47.5	
2.0	46.90	889.7	881.9	875.6	8.15	9.34	9.74	-21.1	-41.1	-45.1	1.91
2.5	49.00	914.8	906.4	899.6	7.85	9.09	9.63	-23.0	-41.3	-44.7	
3.0	54.60	943.0	933.7	925.6	7.36	8.63	9.08	-26.8	-41.6	-41.0	2.08
3.5	54.40	969.6	960.9	953.2	6.94	7.79	8.55	-31.3	-40.3	-36.9	
4.0	48.50	993.9	985.1	977.6	6.15	7.50	7.96	-34.3	-41.6	-35.3	2.23
4.5	43.60	1015.6	1006.9	999.5	6.10	7.22	7.60	-35.6	-44.6	-35.5	
5.0	38.80	1034.9	1026.3	1019.0	5.85	6.99	7.61	-37.3	-49.3	-35.8	2.76
5.5	34.50	1052.0	1043.5	1036.4	6.33	7.54	7.96	-39.0	-51.3	-36.8	
6.0	30.70	1067.3	1058.9	1051.8	6.36	7.48	7.87	-39.5	-46.7	-37.7	
6.5	27.40	1080.9	1072.6	1065.6	6.07	7.41	7.90	-40.2	-42.6	-38.6	
7.0	24.50	1093.0	1084.9	1077.9	5.96	7.16	7.59	-41.0	-39.8	-40.1	

FREQ OFFSET (KHz)	PHASE NOISE (dBc/Hz)
1	-79
10	-103
100	-123
1000	-142

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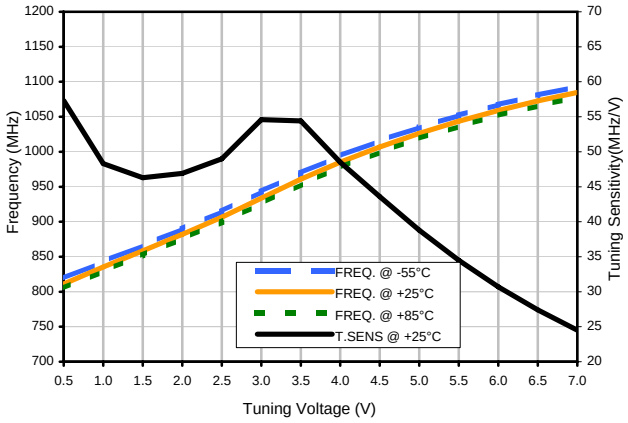
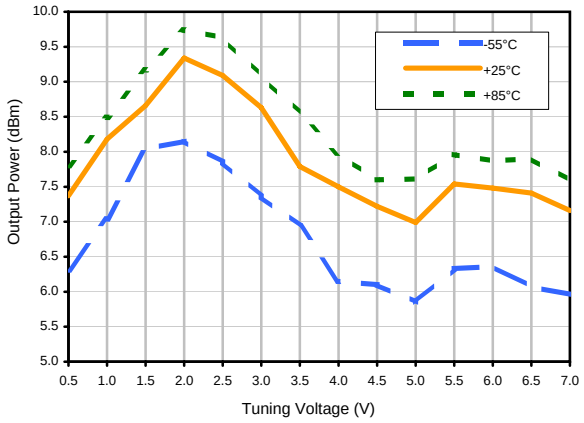
Typical Performance Data

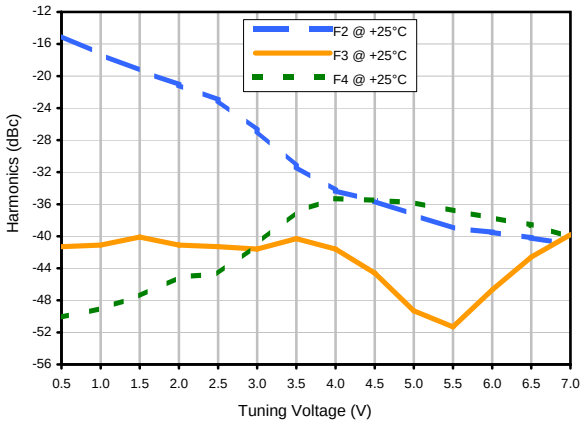
Frequency and Tuning Sensitivity



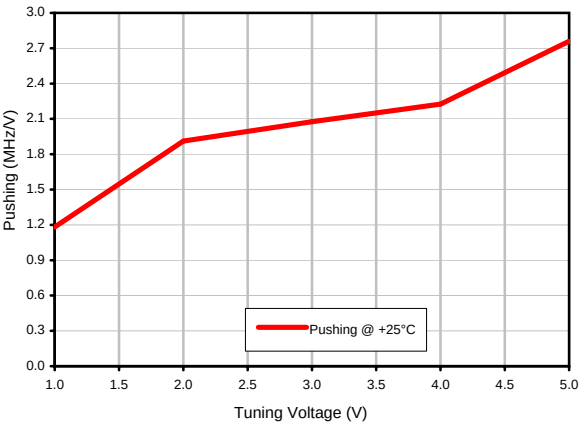
Power Output



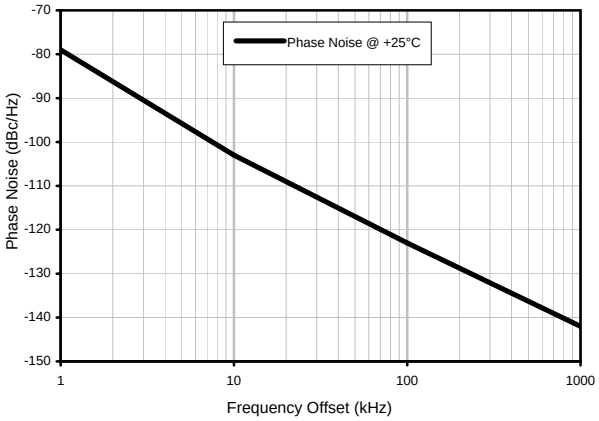
Harmonics Level



Frequency Pushing



Phase Noise

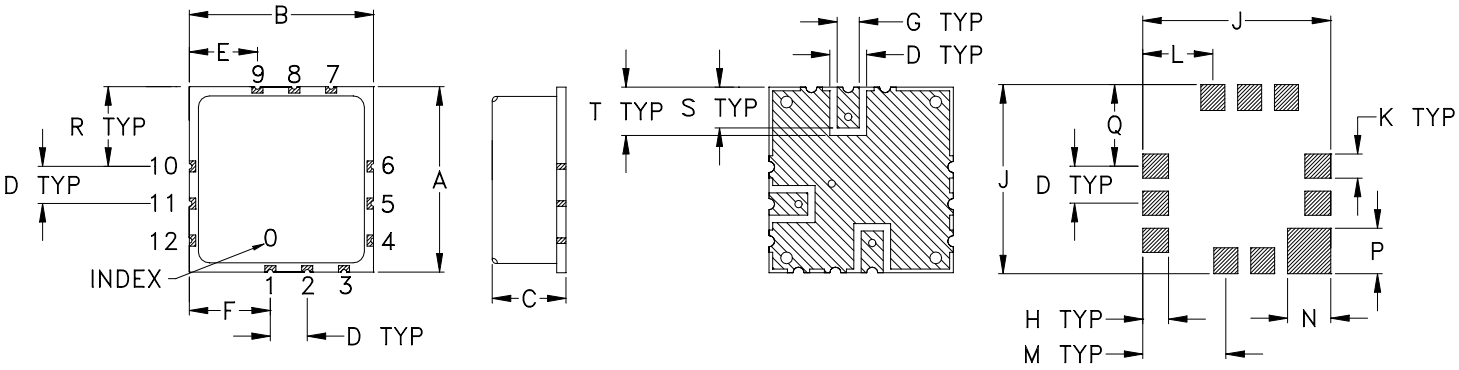


Case Style CH

Outline Dimensions

CH513
CH549

PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE #	A	B	C	D	E	F	G	H	J	K	L	M	N
CH513	.500 (12.70)	.500 (12.70)	.200 (5.08)	.100 (2.54)	.185 (4.70)	.220 (5.59)	.060 (1.52)	.070 (1.78)	.510 (12.95)	.065 (1.65)	.190 (4.83)	.225 (5.72)	.117 (2.97)
CH549			.170 (4.32)										

CASE #	P	Q	R	S	T	WT. GRAM
CH513	.123 (3.12)	.220 (5.59)	.215 (5.46)	.110 (2.79)	.130 (3.30)	1.0
CH549						.85

Dimensions are in inches (mm). Tolerances: 2Pl. ± .03; 3 Pl. ± .015

Notes:

1. Case material: Nickel-Silver alloy.
2. Header: Printed wiring laminate.
3. Termination finish:
   For For RoHS Case Styles: Tin plate over Nickel plate. All models, (+) suffix.
   For RoHS-5 Case Styles: Tin-Lead plate. All models, no (+) suffix.

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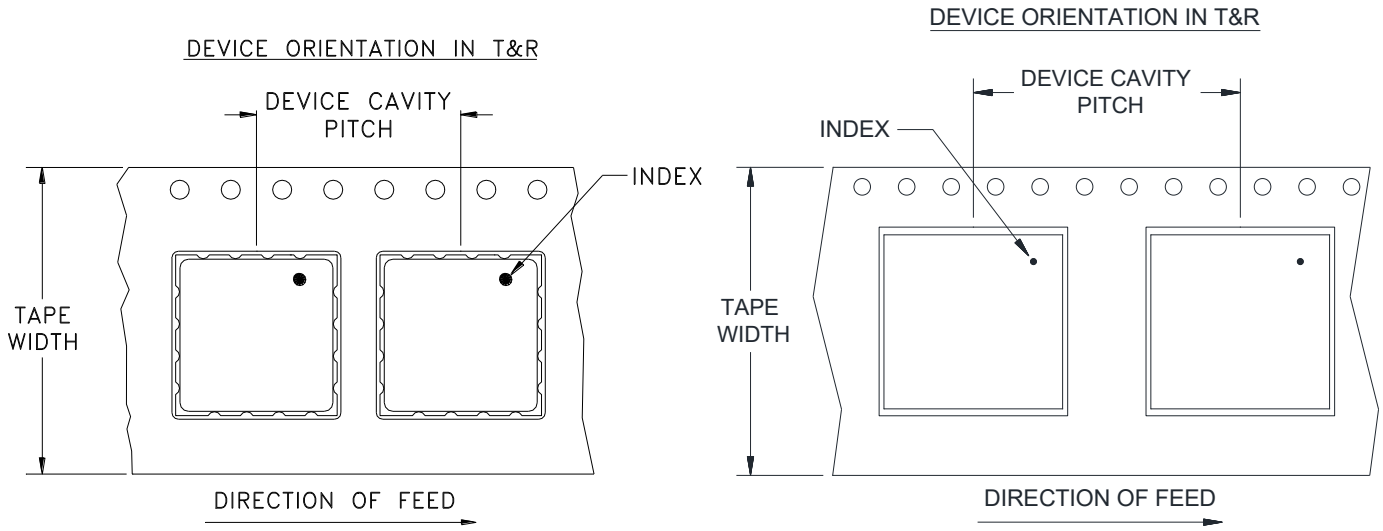
Tape & Reel Packaging TR-F37



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
24	16	7	Small quantity standards (see note)	10
				20
				50
				100
		13	Standard	200
				500

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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Environmental Specifications ENV03

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
HAST	130°C, 85% RH, 96 hours	JESD22-A110
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215