Engineering Development Model

Voltage Controlled Oscillator

ROS-EDR4770

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : CK605

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	**Min.**	**Typ.**	**Max.**	**Units**
Frequency	1840		2005	MHz
Tuning Voltage	0.5		14	V
Power Output		0		dBm
Phase Noise				
at 1 kHz offset		-68		dBc/Hz
at 10 KHz offset		-94		dBc/Hz
at 100 KHz offset		-116		dBc/Hz
at 1000 kHz offset		-137		dBc/Hz
Pulling at 12 dBr PK-PK all phases		2		MHz
Pushing at Vcc=5V±0.25V		0.5		MHz/V
Tuning Sensitivity		12.5 - 21		MHz/V
Harmonic Suppression		-22	-14	dBc
3 dB Modulation Bandwidth		9000		kHz
Supply Voltage		3.5		V
Supply Current			25	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+5.5V
Absolute Tuning Voltage (Vtune)	+16V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	2
GROUND	1,3,4,5,6,7,8,9,11,12,13,15,16

Voltage Controlled Oscillator ROS-EDR4770

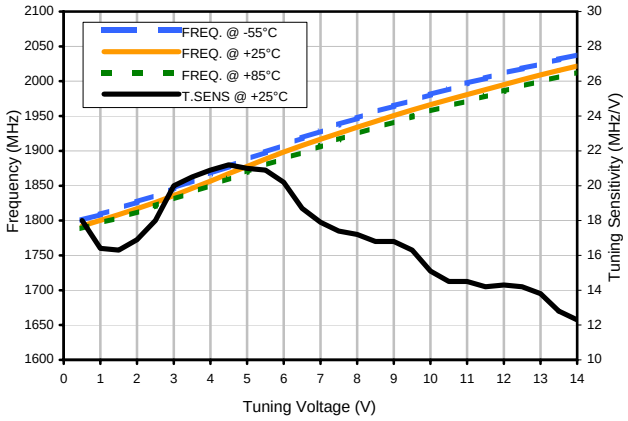
Typical Performance Data

V TUNE	TUNE SENS (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)			HARMONICS (dBc)			FREQ. PUSH (MHz/V)
		-55ºC	+25ºC	+85ºC	-55ºC	+25ºC	+85ºC	F2	F3	F4	
0.5	18.00	1800.7	1792.2	1788.6	1.41		-1.09	-27.2	-22.8	-44.3	0.00
1.0	16.40	1808.8	1800.4	1796.6	1.46	0.02	-1.21	-28.1	-23.0	-42.9	0.06
1.5	16.30	1817.3	1808.5	1804.3	1.55	0.02	-1.25	-28.7	-22.9	-42.3	0.00
2.0	16.90	1826.6	1817.0	1812.5	1.68	0.08	-1.24	-28.8	-23.0	-41.5	0.06
2.5	18.00	1836.4	1826.0	1821.2	1.81	0.18	-1.17	-29.2	-22.7	-40.1	-0.06
3.0	20.00	1846.7	1836.0	1830.7	1.73	0.30	-1.06	-29.8	-23.0	-38.9	0.00
3.5	20.50	1856.8	1846.2	1840.4	1.54	0.39	-0.89	-30.5	-23.0	-39.3	-0.06
4.0	20.90	1867.3	1856.6	1850.6	1.28	0.49	-0.85	-31.4	-22.8	-39.0	-0.06
4.5	21.20	1877.6	1867.3	1860.4	1.09	0.54	-0.99	-31.9	-22.8	-38.7	-0.13
5.0	21.00	1887.7	1877.8	1870.1	0.98	0.52	-1.17	-32.6	-22.8	-38.5	-0.19
5.5	20.90	1898.1	1888.2	1879.6	0.96	0.48	-1.31	-33.2	-22.7	-38.6	-0.19
6.0	20.20	1908.5	1898.3	1888.8	0.98	0.39	-1.34	-33.9	-22.3	-38.2	-0.19
6.5	18.70	1918.7	1907.7	1898.0	1.12	0.29	-1.34	-34.7	-21.9	-37.3	-0.19
7.0	17.90	1928.5	1916.6	1907.1	1.19	0.18	-1.25	-35.5	-21.6	-37.5	-0.26
7.5	17.40	1937.9	1925.3	1916.2	1.10	0.01	-1.15	-36.2	-21.6	-38.3	-0.26
8.0	17.20	1947.1	1933.9	1924.9	0.96	-0.15	-1.03	-37.2	-21.2	-37.6	-0.32
8.5	16.80	1956.2	1942.3	1933.4	0.76	-0.30	-0.98	-38.0	-20.7	-37.6	-0.38
9.0	16.80	1964.5	1950.7	1941.6	0.67	-0.42	-1.02	-38.2	-20.4	-37.3	-0.38
9.5	16.30	1972.6	1958.8	1949.4	0.67	-0.52	-1.09	-38.8	-20.5	-37.3	-0.38
10.0	15.10	1980.9	1966.3	1957.2	0.71	-0.58	-1.22	-38.8	-20.6	-38.1	-0.32
10.5	14.50	1989.0	1973.6	1964.6	0.78	-0.63	-1.27	-39.3	-20.3	-38.0	-0.38
11.0	14.50	1997.1	1980.8	1971.7	0.84	-0.63	-1.31	-39.9	-20.1	-38.5	-0.45
11.5	14.20	2004.5	1987.9	1978.9	0.79	-0.61	-1.32	-40.4	-19.8	-38.3	-0.45
12.0	14.30	2011.5	1995.1	1985.9	0.66	-0.66	-1.24	-40.7	-19.5	-38.9	-0.38
12.5	14.20	2018.2	2002.2	1993.0	0.53	-0.76	-1.10	-40.9	-19.5	-40.2	-0.45
13.0	13.80	2024.8	2009.1	2000.1	0.40	-0.84	-0.96	-41.1	-19.6	-40.1	-0.38
13.5	12.80	2031.3	2015.5	2006.5	0.29	-0.87	-0.92	-41.4	-19.6	-40.2	-0.45
14.0	12.30	2038.0	2021.6	2012.8	0.26	-0.87	-0.99	-40.8	-19.7	-39.6	-0.38

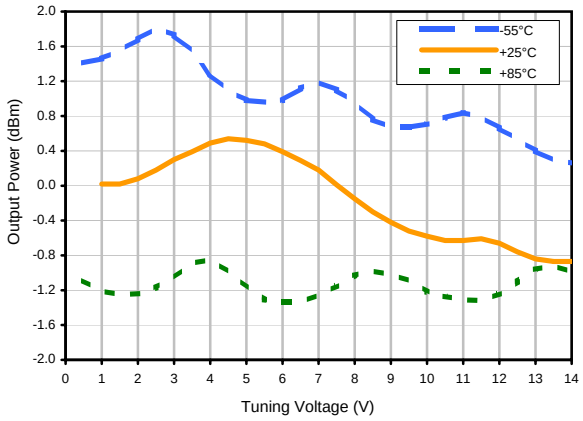
FREQ OFFSET (KHz)	PHASE NOISE (dBc/Hz)
1	-68
10	-94
100	-116
1000	-137

Typical Performance Data

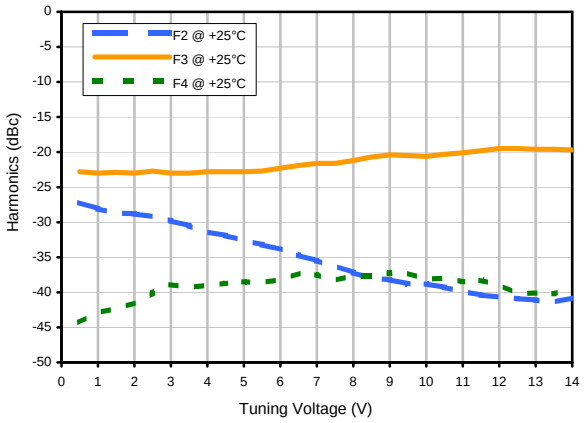
Frequency and Tuning Sensitivity



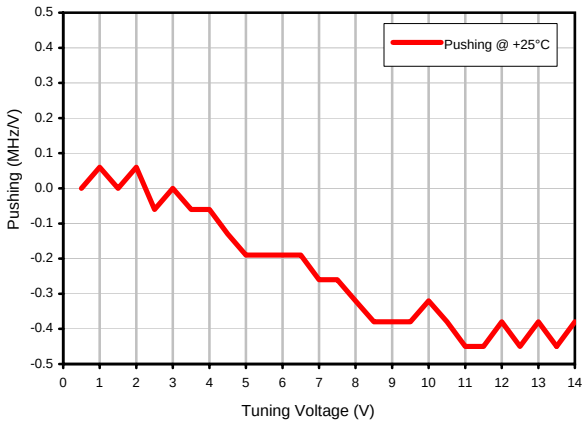
Power Output



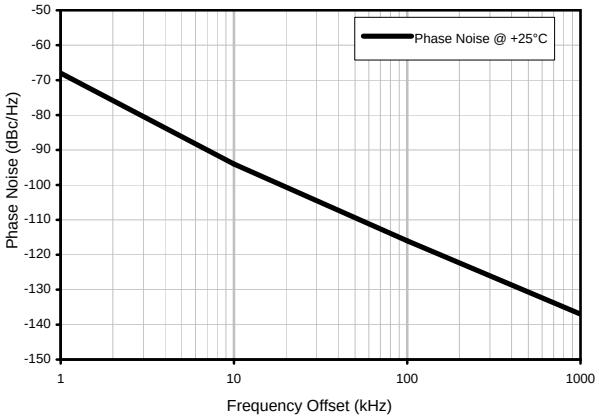
Harmonics Level



Frequency Pushing



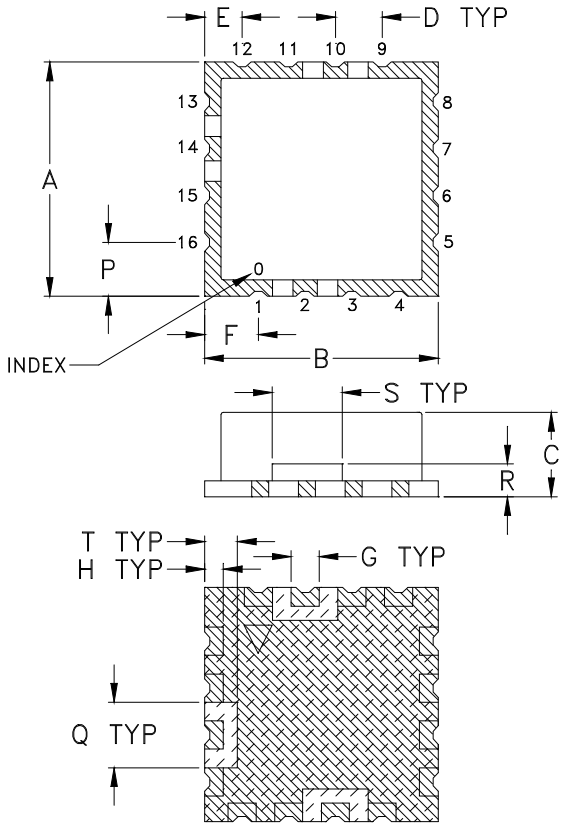
Phase Noise



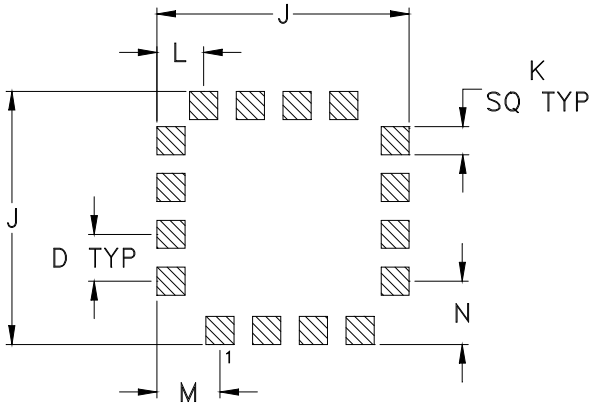
Case Style CK

CK605

Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE #	A	B	C	D	E	F	G	H	J	K
CK605	.500 (12.70)	.500 (12.70)	.180 (4.57)	.100 (2.54)	.080 (2.03)	.115 (2.92)	.060 (1.52)	.040 (1.02)	.540 (13.72)	.060 (1.52)

CASE #	L	M	N	P	Q	R	S	T	WT. GRAM	
CK605	.100 (2.54)	.135 (3.43)	.135 (3.43)	.115 (2.92)	.140 (3.56)	.070 (1.78)	.150 (3.81)	.070 (1.78)	1.2	+0.5 -0.0

Dimensions are in inches (mm). Tolerances: 2 Pl. ± .03; 3Pl. ± .015

Notes:

1. Case material: Nickel-Silver alloy.
2. Base: Printed wiring laminate.
3. Termination finish:
   For RoHS Case Styles: 3-5 µ inch (.08-.13 microns) Gold over 120-240 µ inch (3.05-6.10 microns) Nickel plate. All models, (+) suffix.

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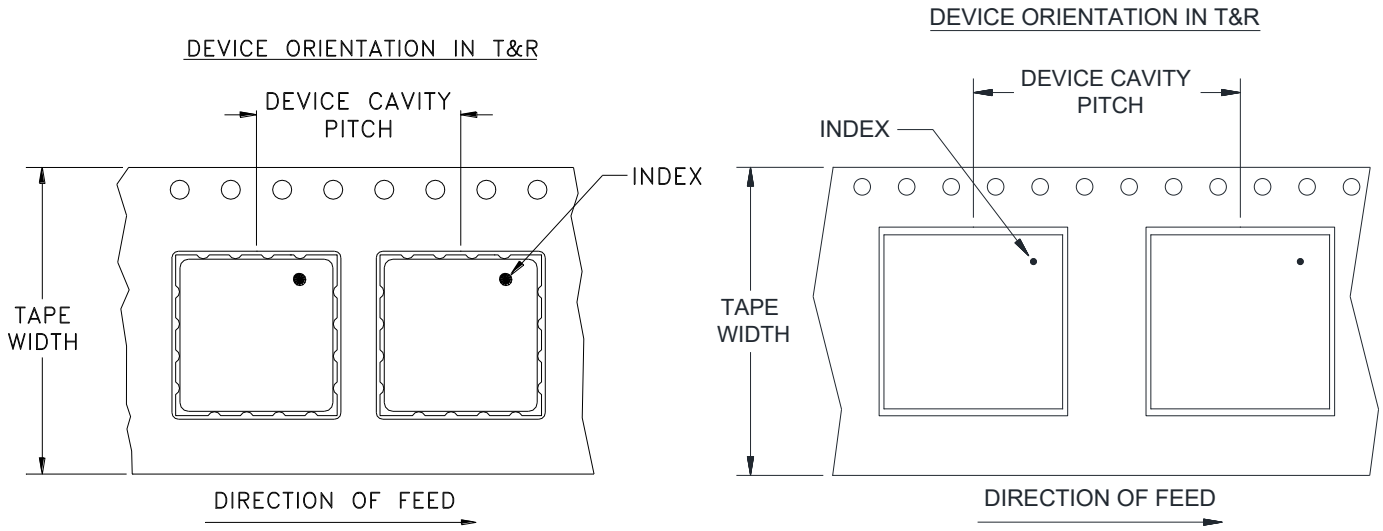
Tape & Reel Packaging TR-F37



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
24	16	7	Small quantity standards (see note)	10
				20
				50
				100
		13	Standard	200
				500

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

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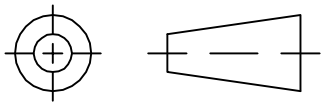
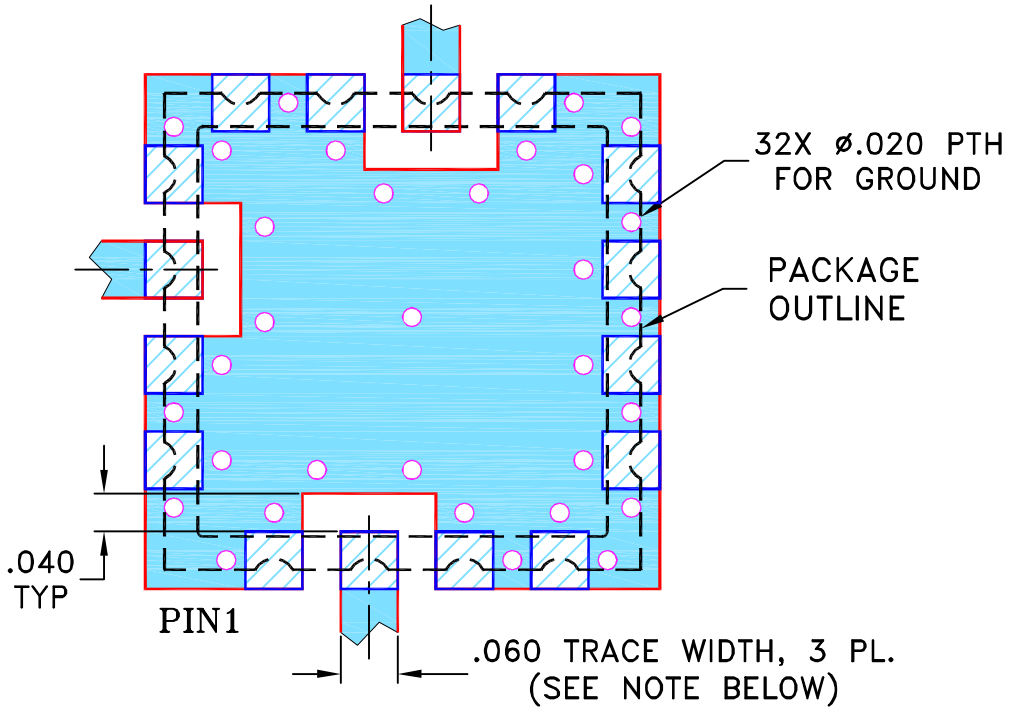
THIRD ANGLE PROJECTION



REVISIONS					
REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
E	M105563	ADDED "rl" PIN CONNECTION	06/02/06	MMG	DJ
F	M105640	CORRECTED NOTE 2	06/08/06	MMG	MM
G	M124395	ADDED "RAMP"	09/09	EM	HH
G	R77589	ADDED "RAMP"	09/09	EM	HH

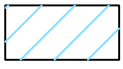
SUGGESTED MOUNTING CONFIGURATION FOR CK605 CASE STYLE, "kg/rl/16AM01" PIN CONNECTION



NOTES:

1. TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS .030" ± .002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
2. BOTTOM SIDE OF THE BOTTOM IS CONTINUOUS GROUND PLANE.

DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED		INITIALS	DATE
DIMENSIONS ARE IN INCHES	DRAWN	AV	08/07/00
TOLERANCES ON:	CHECKED	SK	08/08/00
2 PL DECIMALS ±	APPROVED	DB	08/08/00
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			

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ASHEETA1.DWG REV:A DATE:01/12/95

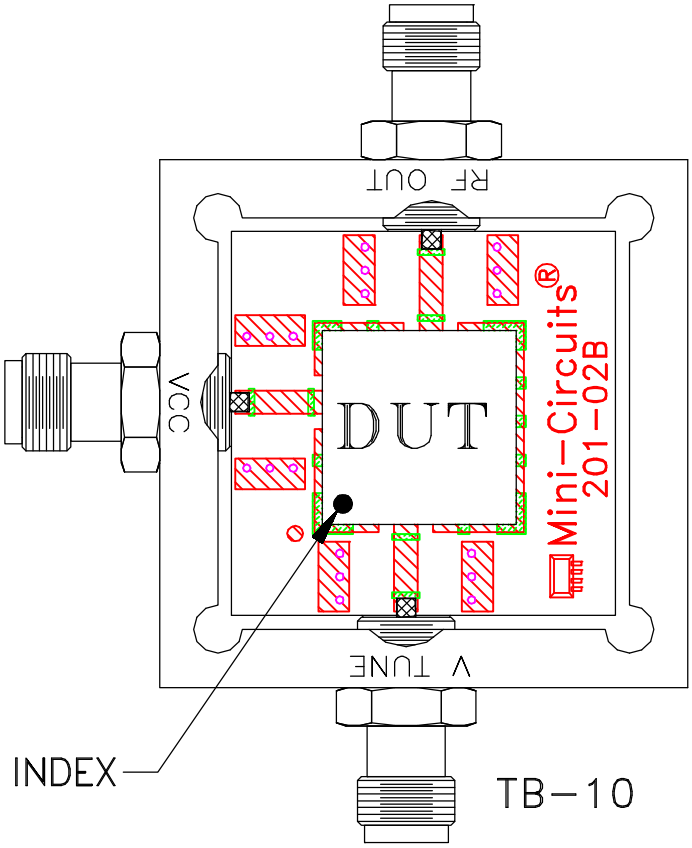
Mini-Circuits® 13 Neptune Avenue Brooklyn NY 11235

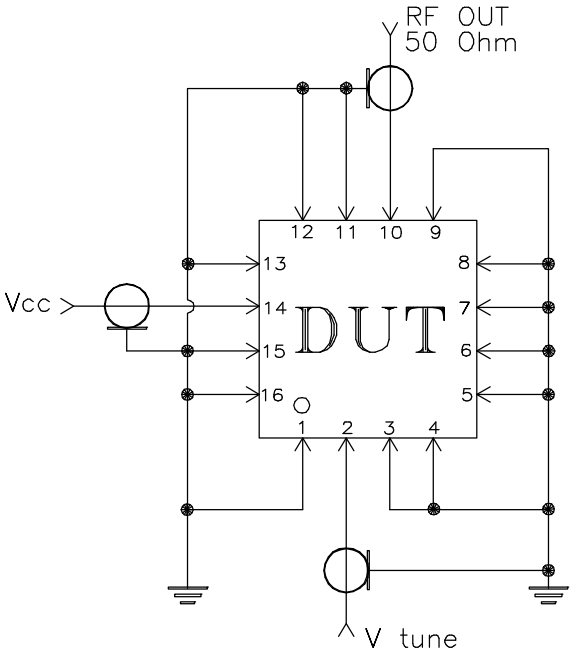
PL,kg/rl/16AM01,CK605,ROS/LAVI/RAMP

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-012	G

FILE:	SCALE:	SHEET:
98PL012	5:1	1 OF 1

Evaluation Board and Circuit





Schematic Diagram

Notes:

1. SMA Female connectors.
2. PCB Material: Rogers RO4350 or equivalent, Dielectric Constant=3.5, Thickness=.030 inch.

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Environmental Specifications ENV03

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
HAST	130°C, 85% RH, 96 hours	JESD22-A110
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215

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