

Engineering Development Model

Voltage Controlled Oscillator

ROS-EDR9339/2

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : CK829

ELECTRICAL SPECIFICATIONS 50Ω				
Parameter	Min.	Typ.	Max.	Units
Frequency	1280		1280	MHz
Tuning Voltage	1		10.0	V
Power Output		+7		dBm
Phase Noise				
at 1 kHz offset		-87		dBc/Hz
at 10 KHz offset		-115		dBc/Hz
at 100 KHz offset		-137		dBc/Hz
at 1000 kHz offset		-157		dBc/Hz
Pulling at 12 dBr PK-PK all phases		1.3		MHz
Pushing at Vcc=8V±0.4V		0.1		MHz/V
Tuning Sensitivity		2		MHz/V
Harmonic Suppression		-17		dBc
3 dB Modulation Bandwidth		65000		kHz
Supply Voltage		8		V
Supply Current			34	mA

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C
Absolute Supply Voltage (Vcc)	+10V
Absolute Tuning Voltage (Vtune)	+12V

PIN CONNECTIONS	
RF OUT	10
VCC	14
V-TUNE	2
GROUND	1,3,4,5,6,7,8,9,11,12,13,15,16

Notes

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B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
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Typical Performance Data

V TUNE	TUNE SENS (MHz/V)	FREQUENCY (MHz)			POWER OUTPUT (dBm)			HARMONICS (dBc)			FREQ. PUSH (MHz/V)	FREQ OFFSET (KHz)	PHASE NOISE (dBc/Hz)
		-55°C	+25°C	+85°C	-55°C	+25°C	+85°C	F2	F3	F4			
0.0	2.11	1272.07	1269.14	1267.08	7.88	7.35	6.79	-15.66	-28.26	-22.19	0.09	1	-87
0.5	1.82	1273.09	1270.20	1268.16	7.87	7.34	6.78	-15.72	-28.81	-22.02	0.10	10	-115
1.0	1.70	1273.99	1271.11	1269.08	7.85	7.32	6.78	-15.77	-29.01	-21.97	0.11	100	-137
1.5	1.66	1274.84	1271.96	1269.92	7.84	7.31	6.77	-15.82	-29.27	-21.99	0.11	1000	-157
2.0	1.66	1275.67	1272.79	1270.74	7.82	7.30	6.77	-15.88	-29.53	-22.05	0.12		
2.5	1.69	1276.50	1273.62	1271.56	7.81	7.28	6.76	-15.92	-29.74	-22.14	0.12		
3.0	1.75	1277.34	1274.47	1272.39	7.80	7.27	6.75	-15.99	-29.96	-22.25	0.12		
3.5	1.83	1278.22	1275.34	1273.25	7.79	7.26	6.74	-16.06	-30.17	-22.36	0.13		
4.0	1.93	1279.14	1276.25	1274.15	7.78	7.24	6.73	-16.23	-29.87	-22.94	0.13		
4.5	2.03	1280.10	1277.22	1275.10	7.76	7.23	6.72	-16.23	-30.59	-22.67	0.13		
5.0	2.14	1281.11	1278.23	1276.10	7.75	7.21	6.71	-16.30	-30.78	-22.73	0.14		
5.5	2.26	1282.19	1279.30	1277.16	7.73	7.20	6.69	-16.38	-30.75	-22.54	0.14		
6.0	2.37	1283.32	1280.43	1278.28	7.72	7.18	6.68	-17.21	-34.83	-22.10	0.14		
6.5	2.47	1284.51	1281.62	1279.45	7.70	7.16	6.67	-16.76	-30.58	-22.81	0.14		
7.0	2.55	1285.74	1282.85	1280.67	7.68	7.15	6.65	-16.78	-30.42	-22.49	0.15		
7.5	2.62	1287.01	1284.12	1281.94	7.66	7.13	6.64	-16.86	-30.15	-22.54	0.15		
8.0	2.67	1288.32	1285.43	1283.23	7.65	7.11	6.63	-16.83	-29.83	-22.46	0.15		
8.5	2.72	1289.65	1286.77	1284.56	7.63	7.09	6.61	-16.82	-29.37	-22.54	0.15		
9.0	2.76	1291.00	1288.13	1285.92	7.62	7.07	6.60	-16.76	-28.91	-22.49	0.15		
9.5	2.78	1292.38	1289.51	1287.29	7.60	7.05	6.58	-16.70	-28.44	-22.40	0.16		
10.0	2.82	1293.77	1290.90	1288.68	7.59	7.03	6.56	-16.69	-28.02	-22.32	0.16		
10.5	2.85	1295.18	1292.31	1290.09	7.59	7.02	6.54	-16.72	-27.66	-22.21	0.16		
11.0	2.88	1296.59	1293.74	1291.51	7.59	7.00	6.53	-16.75	-27.38	-22.12	0.16		
11.5	2.89	1298.02	1295.18	1292.94	7.58	6.99	6.51	-16.78	-27.17	-22.03	0.17		
12.0	2.89	1299.45	1296.62	1294.37	7.58	6.98	6.50	-16.82	-27.00	-21.94	0.17		

Notes

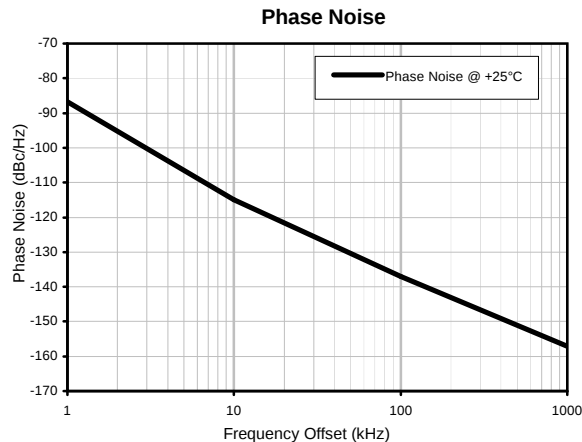
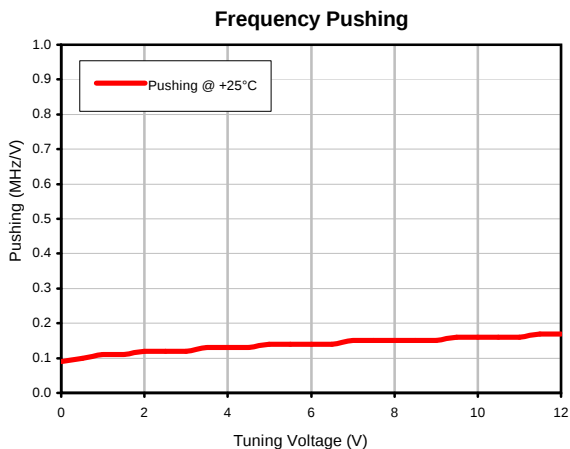
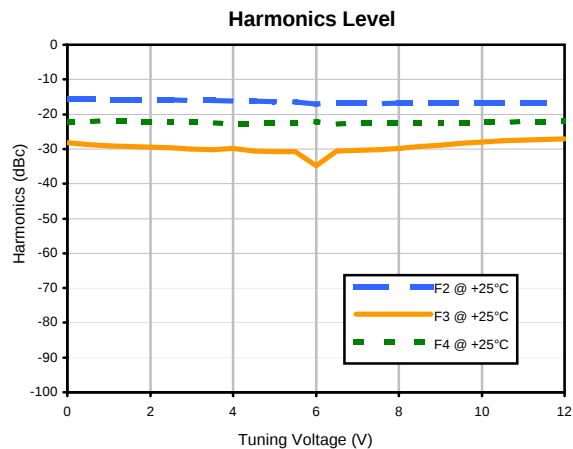
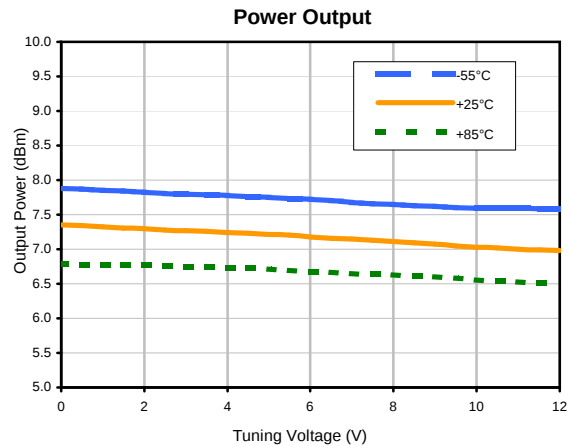
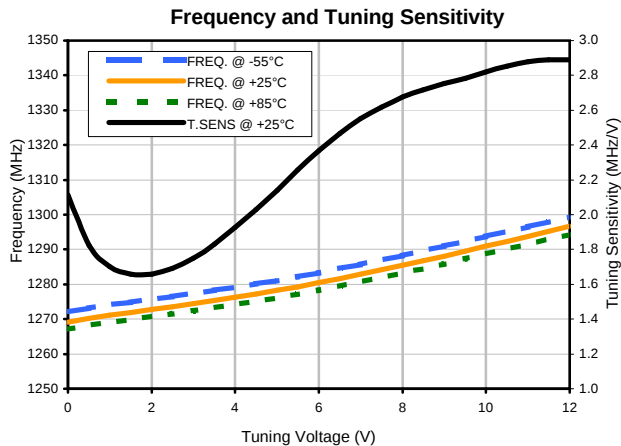
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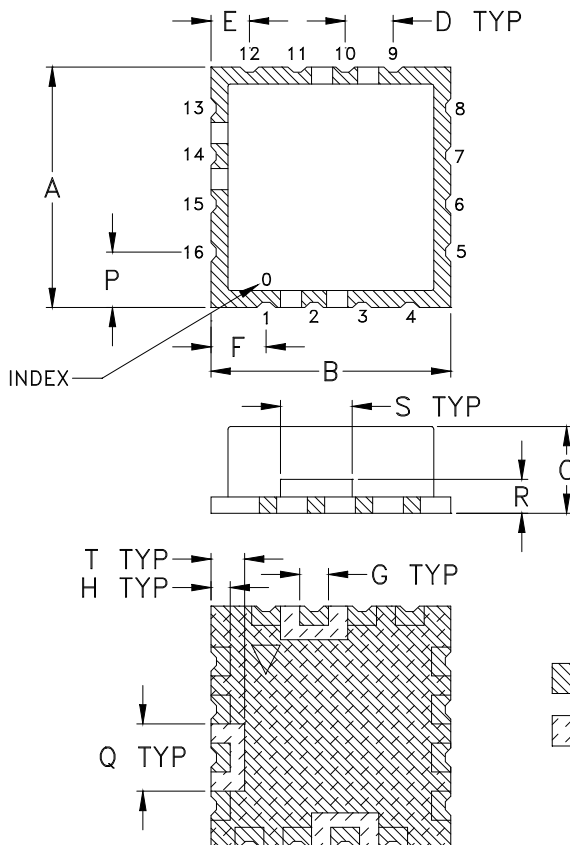


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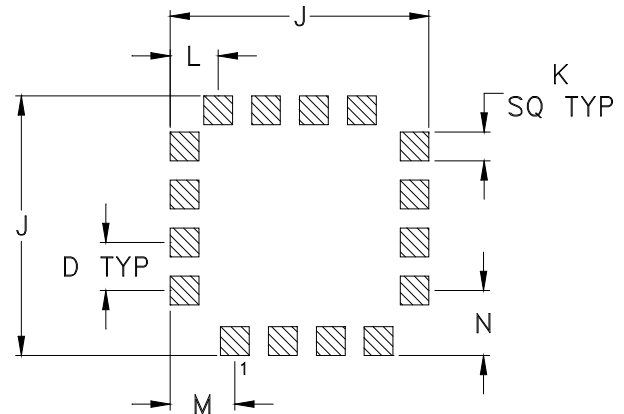
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CK829

Outline Dimensions



PCB Land Pattern



Suggested Layout,
Tolerance to be within $\pm .002$

CASE #	A	B	C	D	E	F	G	H	J	K
CK829	.500 (12.70)	.500 (12.70)	.100 (2.54)	.100 (2.54)	.080 (2.03)	.115 (2.92)	.060 (1.52)	.040 (1.02)	.540 (13.72)	.060 (1.52)

CASE #	L	M	N	P	Q	R	S	T	WT. GRAM
CK829	.100 (2.54)	.135 (3.43)	.135 (3.43)	.115 (2.92)	.140 (3.56)	.070 (1.78)	.150 (3.81)	.070 (1.78)	1.0

Dimensions are in inches (mm). Tolerances: 2 Pl. $\pm .03$; 3Pl. $\pm .015$

Notes:

- Case material: Nickel-Silver alloy.
- Base: Printed wiring laminate.
- Termination finish:

For RoHS Case Styles: 3-5 μ inch (.08-.13 microns) Gold over 120-240 μ inch (3.05-6.10 microns) Nickel plate.
All models, (+) suffix.

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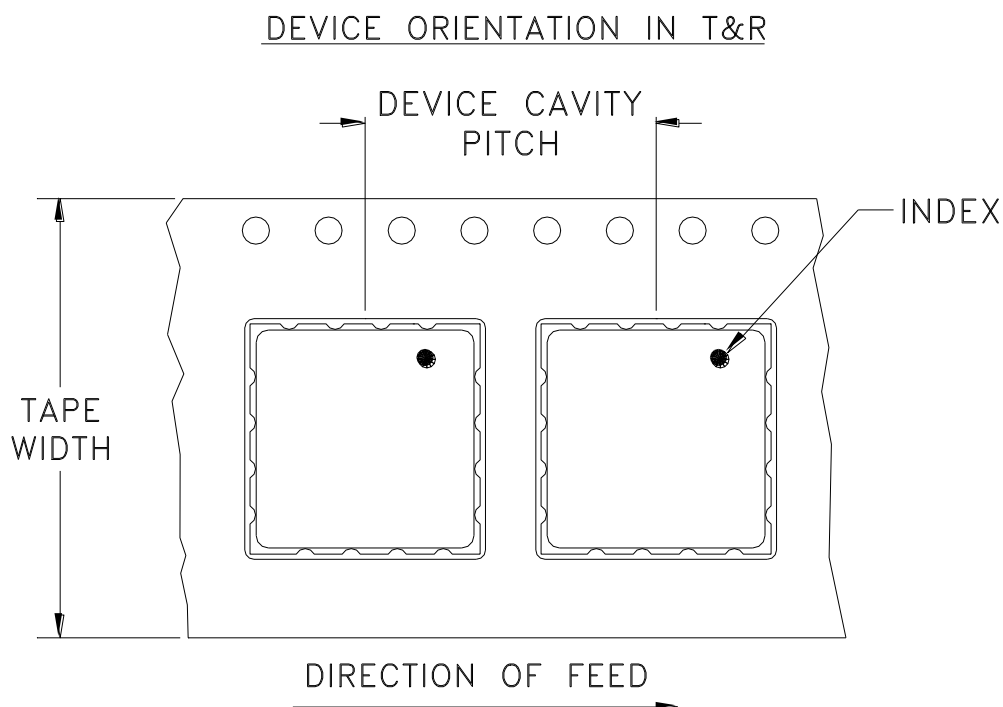
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RF/IF MICROWAVE COMPONENTS

Tape & Reel Packaging TR-F37



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel	
24	16	7	Small quantity standards (see note)	10
				20
				50
				100
		13	Standard	200
				500

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

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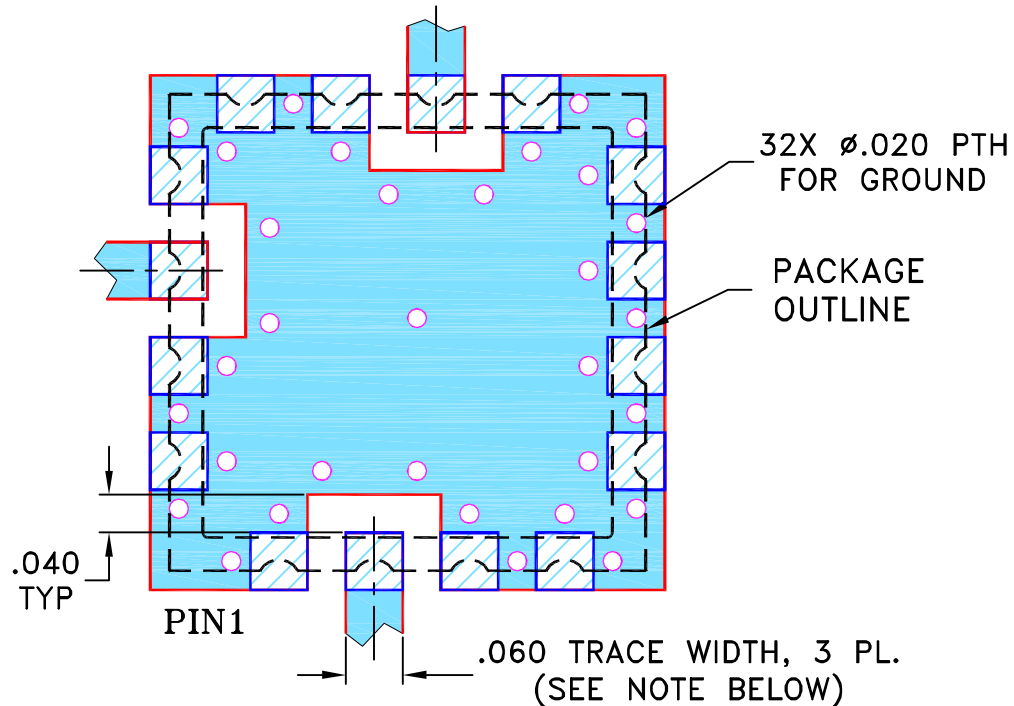
THIRD ANGLE PROJECTION



REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
E	M105563	ADDED "rl" PIN CONNECTION	06/02/06	MMG	DJ
F	M105640	CORRECTED NOTE 2	06/08/06	MMG	MM
G	M124395	ADDED "RAMP"	09/09	EM	HH
G	R77589	ADDED "RAMP"	09/09	EM	HH

SUGGESTED MOUNTING CONFIGURATION FOR
CK605 CASE STYLE, "kg/rl/16AM01" PIN CONNECTION

NOTES:

- TRACE WIDTH IS SHOWN FOR FR4 WITH DIELECTRIC THICKNESS .030" $\pm$.002"; COPPER: 1/2 OZ. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED.
- BOTTOM SIDE OF THE BOTTOM IS CONTINUOUS GROUND PLANE.



DENOTES PCB COPPER LAYOUT WITH SMOBC
(SOLDER MASK OVER BARE COPPER)



DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED

INITIALS

DATE

DIMENSIONS ARE IN INCHES

TOLERANCES ON:

2 PL DECIMALS $\pm$ 3 PL DECIMALS $\pm$.005ANGLES $\pm$ FRACTIONS $\pm$

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PL,kg/rl/16AM01,CK605,ROS/LAVI/RAMP

SIZE
A

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REV:
G

FILE: 98PL012

SCALE: 5:1

SHEET: 1 OF 1



All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-55° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutectic Process: 225°C peak Pb-Free Process, 245°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, Para 4.2.5, Test S, 95% Coverage
Vibration (High Frequency)	20g peak, 20-2000 Hz, 4 times in each of three axes (total 12)	MIL-STD-883, Method 2007.3, Condition A
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215