Engineering Development Model

Power Splitter/Combiner SBTCJ-ED11064

2 Way-180°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : AT790

ELECTRICAL SPECIFICATIONS 75Ω @ +25°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		0.4		955	MHz
Isolation	**0.4 - 4 MHz**		24		dB
	4 - 477.5 MHz		35		dB
	477.5 - 955 MHz		25		dB
Insertion Loss Above 3.0 dB	**0.4 - 4 MHz**		0.60		dB
	4 - 477.5 MHz		0.80		dB
	477.5 - 955 MHz		1.00		dB
Phase Unbalance	**0.4 - 4 MHz**		180.041		deg.
	4 - 477.5 MHz		179.480		deg.
	477.5 - 955 MHz		179.453		deg.
Amplitude Unbalance	**0.4 - 4 MHz**		0.011		dB
	4 - 477.5 MHz		0.020		dB
	477.5 - 955 MHz		0.433		dB
VSWR	**SUM Port**		1.35		(:1)
	OUT Ports		1.35		(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	6
PORT 1	1
PORT 2	3
GND EXT	2, 4
NOT USED	5

Functional Diagram

2 Way-180° Power Splitter/Combiner SBTCJ-ED11064

Typical Performance Data

FREQ. (MHz)	TOTAL LOSS[1] (dB)		AMP. UNBAL. (dB)	ISOLATION (dB)	PHASE UNBAL. (deg.)	FREQ. (MHz)	VSWR (:1)		
	S-1	S-2					S	1	2
0.4	5.02	5.03	0.01	20.81	180.01	0.4	1.84	1.61	1.61
0.5	4.84	4.85	0.01	22.29	180.03	0.5	1.72	1.55	1.56
0.6	4.72	4.73	0.02	23.49	180.04	0.6	1.64	1.52	1.53
0.7	4.62	4.63	0.01	24.51	180.04	0.7	1.59	1.49	1.50
0.8	4.54	4.55	0.02	25.39	180.04	0.8	1.54	1.47	1.48
0.9	4.47	4.48	0.01	26.19	180.00	0.9	1.51	1.45	1.46
1.0	4.41	4.42	0.01	26.90	180.07	1.0	1.48	1.44	1.44
4.0	3.87	3.87	0.00	41.87	180.10	4.0	1.22	1.27	1.27
7.0	3.80	3.79	0.01	49.79	180.10	7.0	1.17	1.22	1.23
10.0	3.77	3.77	0.01	48.39	179.91	10.0	1.16	1.20	1.20
43.0	3.76	3.75	0.02	44.54	179.84	43.0	1.14	1.17	1.17
76.0	3.78	3.77	0.01	43.98	179.72	76.0	1.15	1.17	1.17
109.0	3.79	3.78	0.01	42.99	179.63	109.0	1.15	1.18	1.17
142.0	3.81	3.80	0.01	41.68	179.55	142.0	1.17	1.19	1.18
175.0	3.81	3.81	0.00	40.31	179.49	175.0	1.18	1.20	1.18
180.0	3.81	3.81	0.00	40.07	179.43	180.0	1.18	1.20	1.18
197.0	3.82	3.82	0.00	39.29	179.39	197.0	1.19	1.20	1.18
214.0	3.82	3.82	0.00	38.65	179.35	214.0	1.20	1.20	1.19
231.0	3.83	3.83	0.01	38.11	179.37	231.0	1.22	1.21	1.19
248.0	3.84	3.85	0.01	37.38	179.31	248.0	1.23	1.22	1.20
265.0	3.83	3.86	0.02	36.80	179.26	265.0	1.24	1.23	1.21
282.0	3.84	3.86	0.02	36.27	179.25	282.0	1.25	1.23	1.22
299.0	3.84	3.87	0.03	35.62	179.22	299.0	1.26	1.24	1.22
316.0	3.84	3.87	0.04	35.12	179.18	316.0	1.27	1.25	1.23
333.0	3.85	3.89	0.04	34.59	179.21	333.0	1.28	1.25	1.24
350.0	3.87	3.92	0.05	34.17	179.14	350.0	1.29	1.26	1.26
430.0	3.88	3.98	0.10	32.52	179.14	430.0	1.33	1.29	1.29
510.0	3.89	4.05	0.16	31.39	179.24	510.0	1.36	1.29	1.32
590.0	3.88	4.13	0.25	30.51	179.32	590.0	1.39	1.31	1.36
670.0	3.87	4.22	0.36	30.08	179.38	670.0	1.40	1.33	1.40
750.0	3.84	4.33	0.49	29.62	179.50	750.0	1.42	1.35	1.46
818.6	3.84	4.46	0.62	28.63	179.60	818.6	1.41	1.36	1.53
887.1	3.86	4.58	0.72	26.82	179.68	887.1	1.41	1.38	1.59
955.7	3.88	4.69	0.82	24.63	179.27	955.7	1.41	1.39	1.64
1024.3	3.91	4.86	0.95	22.49	179.07	1024.3	1.41	1.44	1.70
1092.9	4.00	5.00	1.00	20.46	179.17	1092.9	1.42	1.48	1.78
1161.4	4.14	5.11	0.97	18.62	179.25	1161.4	1.42	1.54	1.89
1230.0	4.25	5.24	0.99	17.06	180.67	1230.0	1.43	1.61	2.01

[1]Total Loss = Insertion Loss + 3dB Splitter Loss

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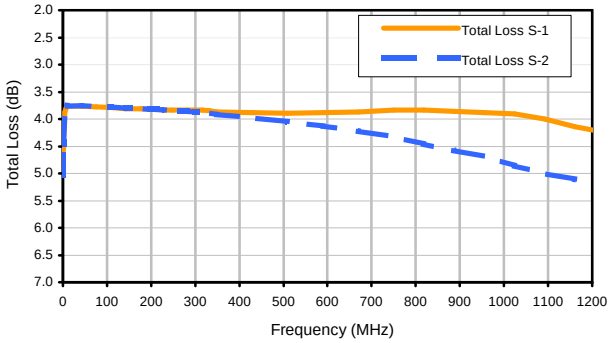
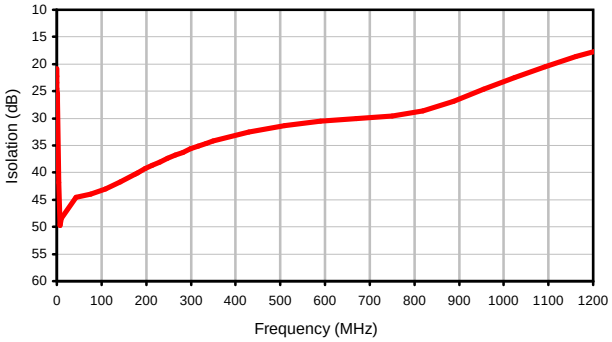
Typical Performance Curves

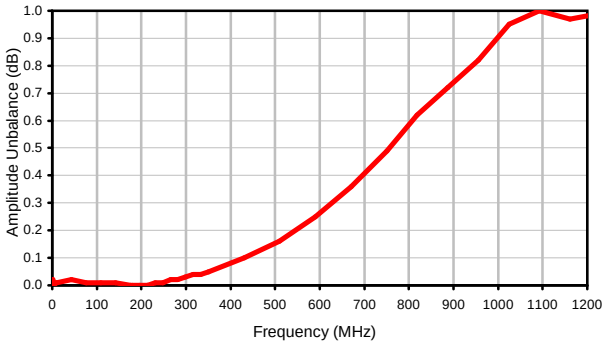
Total Loss



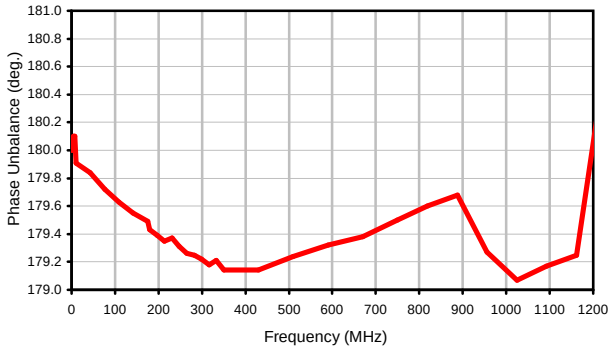
Isolation



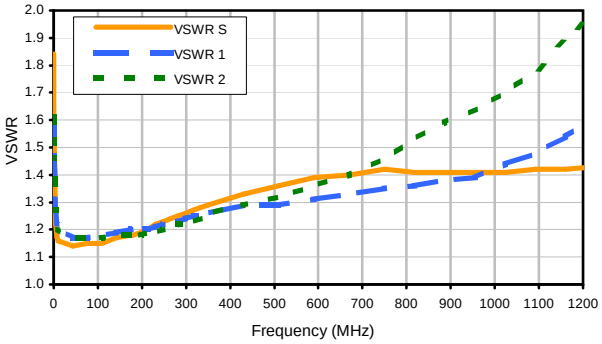
Amplitude Unbalance



Phase Unbalance



VSWR

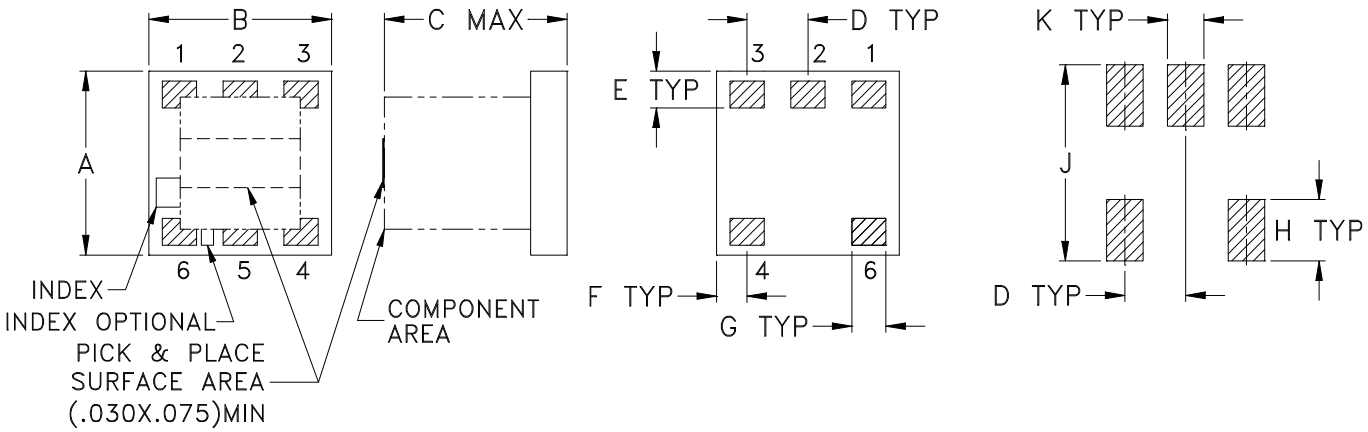


Case Style AT

Outline Dimensions

AT790

PCB Land Pattern



Suggested Layout,
Tolerance to be within ±.002

CASE #	A	B	C	D	E	F	G	H	J	K	L	WT. GRAMS
AT790	.150 (3.81)	.150 (3.81)	.150 (3.81)	.050 (1.27)	.030 (0.76)	.025 (0.64)	.028 (0.71)	.050 (1.27)	.160 (4.06)	.030 (0.76)	-- --	.10

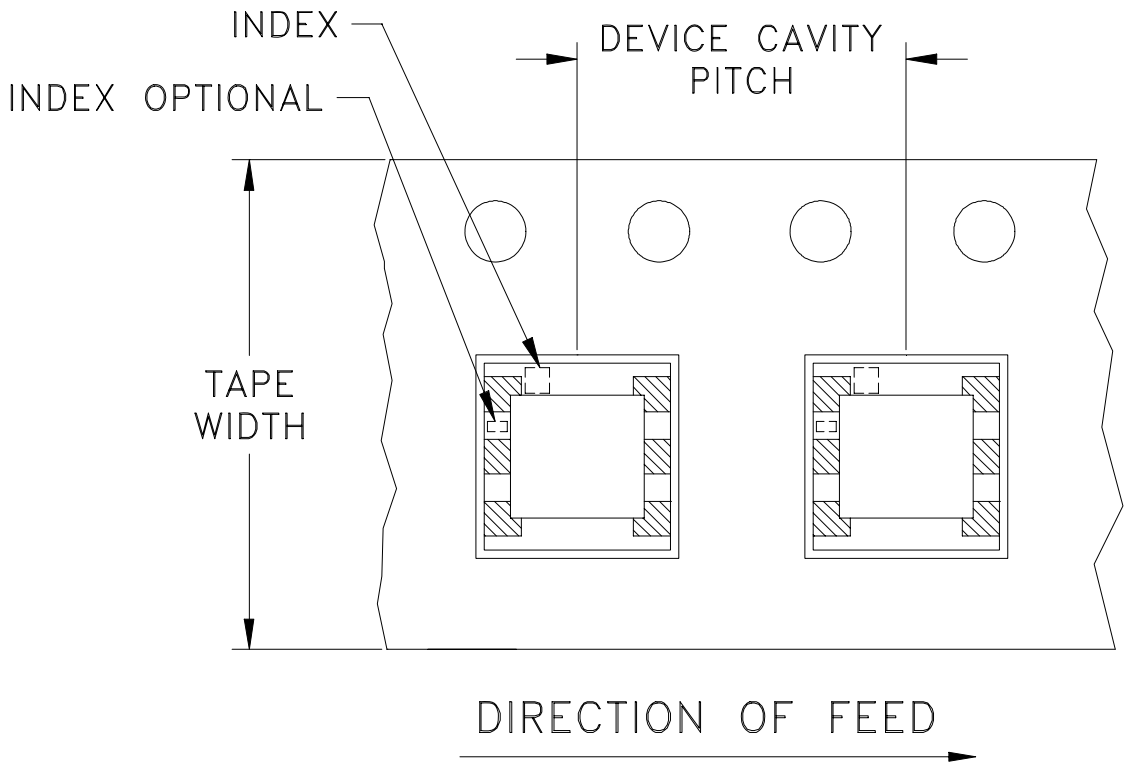
Dimensions are in inches (mm). Tolerances: 2 Pl. ± .01; 3 Pl. ± .005

Notes:

1. Open style, Ceramic base.
2. Termination finish: Silver palladium or gold over nickel based on stock availability.

Tape & Reel Packaging TR-F15

DEVICE ORIENTATION IN T&R



Tape Width, mm	Device Cavity Pitch, mm	Reel Size, inches	Devices per Reel
12	8	7	20
			50
			100
			200
		13	500
			1000
			2000

Note: Please consult individual model data sheet to determine device per reel availability.

Mini-Circuits carrier tape materials provide protection from ESD (Electro-Static Discharge) during handling and transportation. Tapes are static dissipative and comply with industry standards EIA-481/EIA-541.

Go to: www.minicircuits.com/pages/pdfs/tape.pdf

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Environmental Specifications ENV02T1

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test.

Specification	Test/Inspection Condition	Reference/Spec
Operating Temperature	-40° to 85°C Ambient Environment	Individual Model Data Sheet
Storage Temperature	-55° to 100° C Ambient Environment	Individual Model Data Sheet
Humidity	90 to 95% RH, 240 hours, 50°C	MIL-STD-202, Method 103, Condition A, Except 50°C and end-point electrical test done within 12 hours
Thermal Shock	-55° to 100°C, 100 cycles	MIL-STD-202, Method 107, Condition A-3, except +100°C
Solder Reflow Heat	Sn-Pb Eutetic Process: 225°C peak Pb-Free Process 245° - 250°C peak	J-STD-020, Table 4-1, 4-2 and 5-2, Figure 5-1
Solderability	10X Magnification	J-STD-002, 95% Coverage
Vibration (High Frequency)	20g peak, 10-2000 Hz, 12 times in each of three perpendicular directions (total 36)	MIL-STD-202, Method 204, Condition D
Mechanical Shock	50g, 11 ms, 1/2-sine, 18 shocks: 3 each direction, each of 3 axes	MIL-STD-202, Method 213, Condition A
Marking Resistance to Solvents	Isopropyl alcohol + mineral spirits at 25°C; terpene defluxer at 25°C; distilled water + proylene glycol monomethyl ether + monoethanolamine at 63°C to 70°C	MIL-STD-202, Method 215